

## NCE N-Channel Super Trench II Power MOSFET

### Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(on)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### General Features

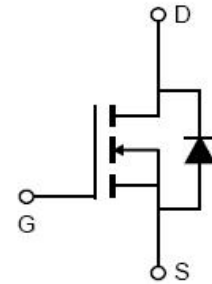
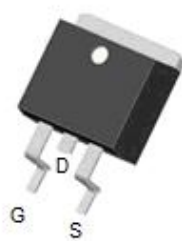
- $V_{DS} = 100V, I_D = 185A$   
 $R_{DS(on)} = 2.6m\Omega$ , typical (TO-220) @  $V_{GS} = 10V$   
 $R_{DS(on)} = 2.4m\Omega$ , typical (TO-263) @  $V_{GS} = 10V$
- Excellent gate charge x  $R_{DS(on)}$  product (FOM)
- Very low on-resistance  $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

**100% UIS TESTED!**  
**100%  $\Delta V_{ds}$  TESTED!**

TO-220



TO-263



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP029N10	NCEP029N10	TO-220	-	-	-
NCEP029N10D	NCEP029N10D	TO-263	-	-	-

### Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous ( $T_c = 25^\circ C$ )	$I_D$	185	A
Drain Current-Continuous ( $T_c = 100^\circ C$ )	$I_D (100^\circ C)$	130	A
Pulsed Drain Current ( $T_c = 25^\circ C$ )	$I_{DM}$	740	A
Maximum Power Dissipation ( $T_c = 25^\circ C$ )	$P_D$	275	W
Derating factor		1.83	W/°C
Single pulse avalanche energy <sup>(Note 1)</sup>	$E_{AS}$	1692	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.55	$^{\circ}\text{C/W}$
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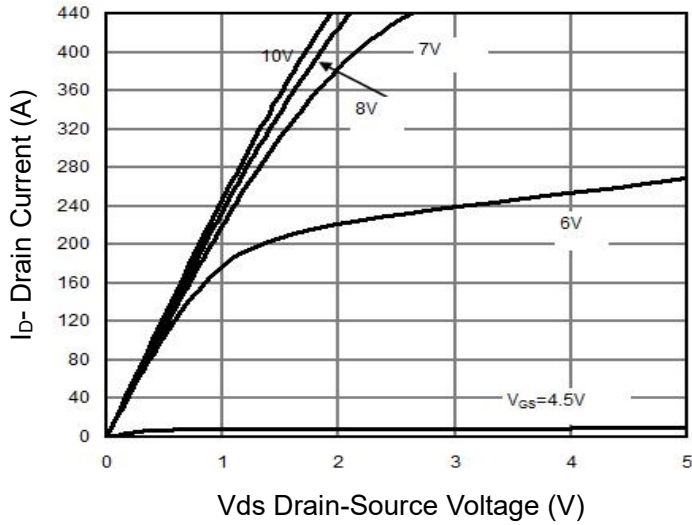
## Electrical Characteristics ( $T_c=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100		-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =92.5A	TO-220	-	2.6	2.9	mΩ
			TO-263		2.4	2.9	mΩ
Gate resistance	R <sub>G</sub>		-	1.9	-	Ω	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =92.5A	90	-	-	S	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, F=1.0MHz	-	13700	-	PF	
Output Capacitance	C <sub>oss</sub>		-	1050	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	41	-	PF	
Switching Characteristics <small>(Note 2)</small>							
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =92.5A V <sub>GS</sub> =10V, R <sub>G</sub> =1.6Ω	-	34	-	nS	
Turn-on Rise Time	t <sub>r</sub>		-	38	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	84	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	28	-	nS	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, I <sub>D</sub> =92.5A, V <sub>GS</sub> =10V	-	185	-	nC	
Gate-Source Charge	Q <sub>gs</sub>		-	54		nC	
Gate-Drain Charge	Q <sub>gd</sub>		-	48		nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =92.5A	-		1.2	V	
Diode Forward Current	I <sub>S</sub>		-	-	185	A	
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 185A	-	86	-	nS	
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs	-	210	-	nC	

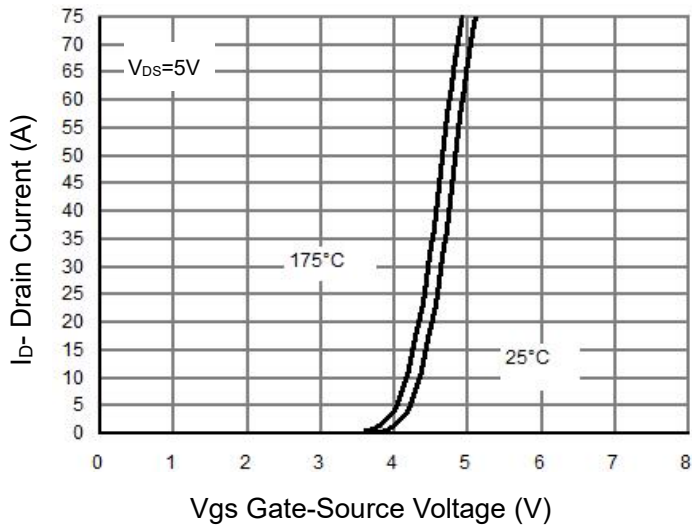
## Notes:

1. EAS condition :  $T_J=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25\Omega$
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of  $T_J(MAX)=175^{\circ}\text{C}$ . The SOA curve provides a single pulse rating.

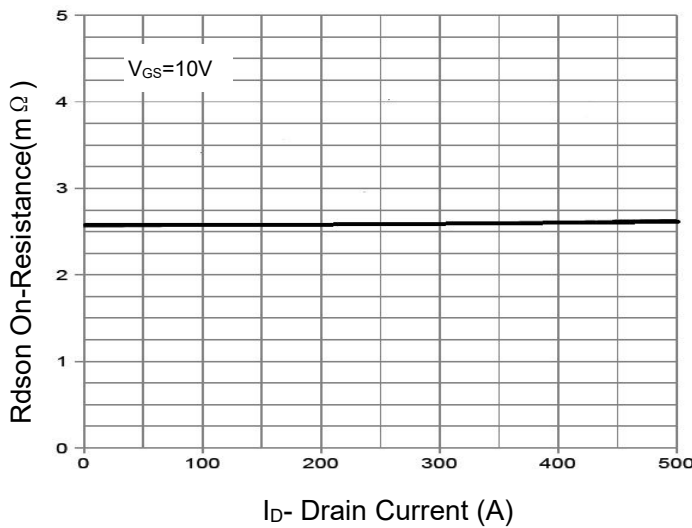
## Typical Electrical and Thermal Characteristics



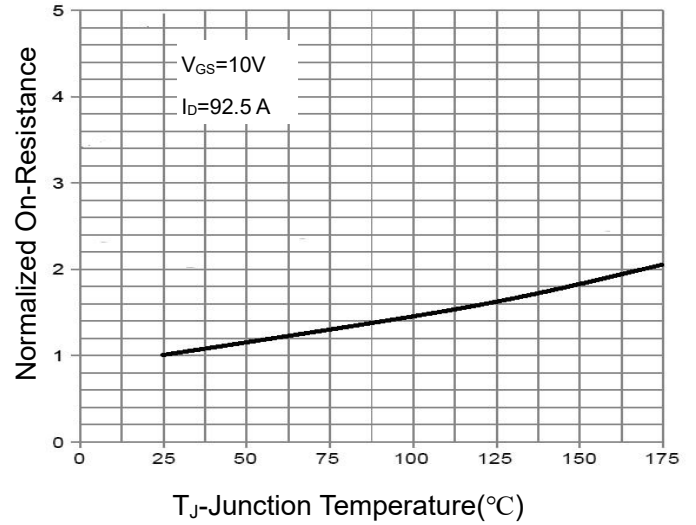
**Figure 1 Output Characteristics**



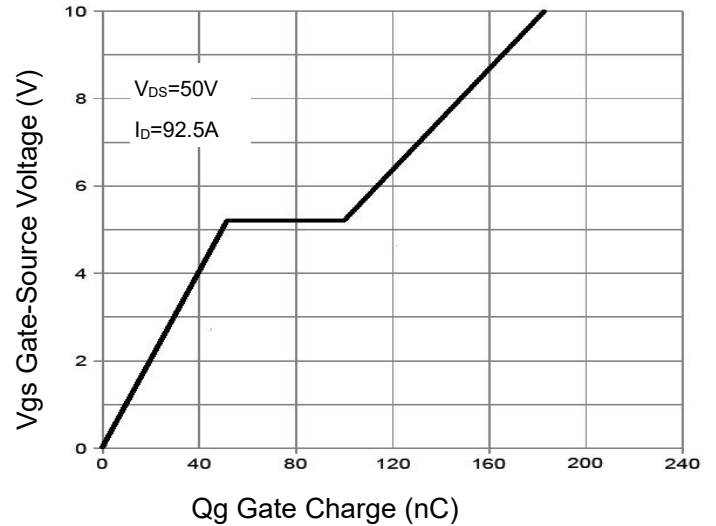
**Figure 2 Transfer Characteristics**



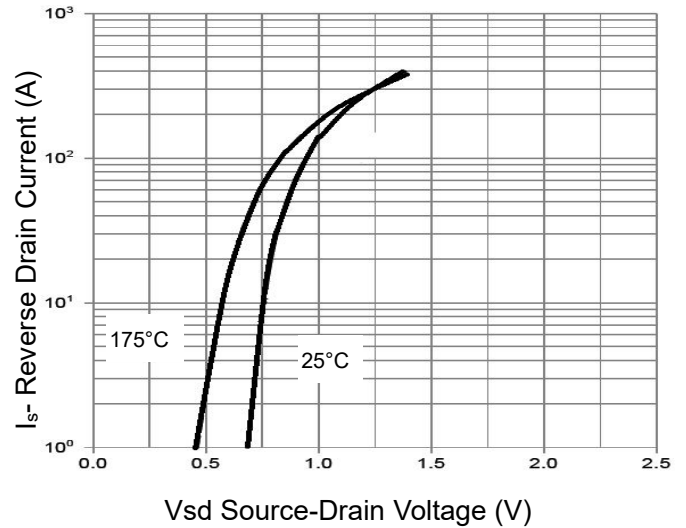
**Figure 3  $R_{DS(on)}$ - Drain Current**



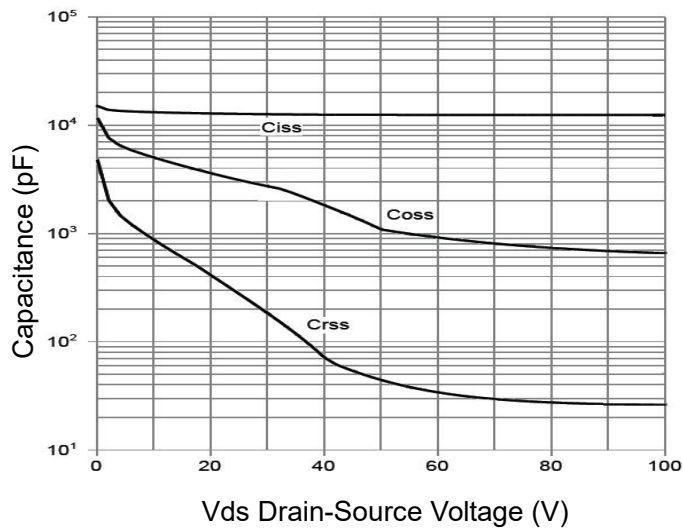
**Figure 4  $R_{DS(on)}$ -Junction Temperature**



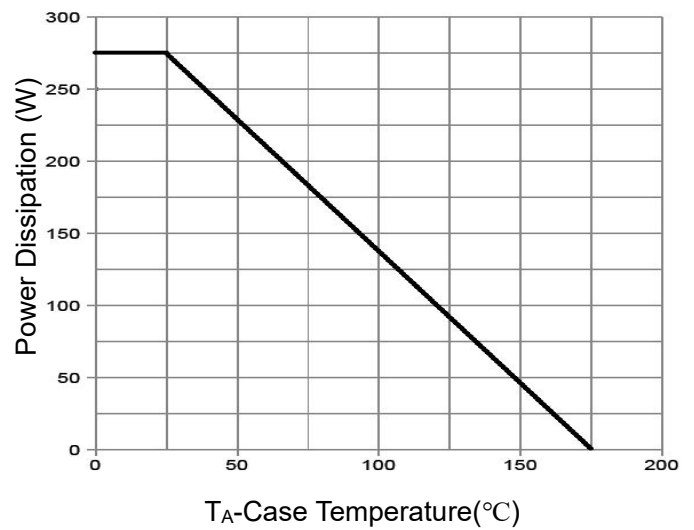
**Figure 5 Gate Charge**



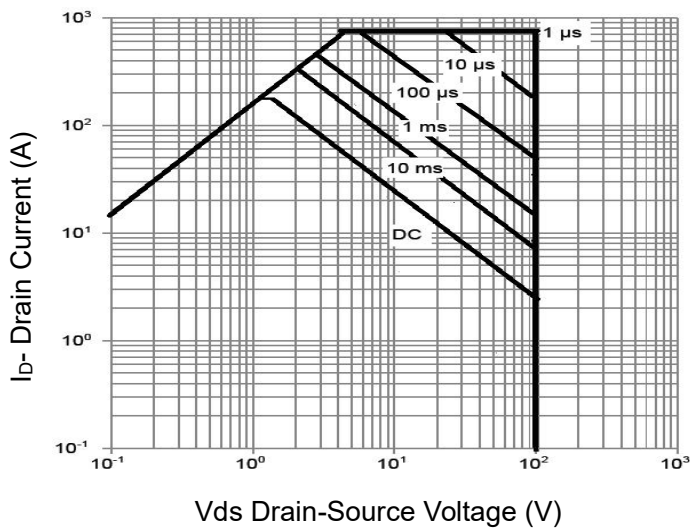
**Figure 6 Source- Drain Diode Forward**



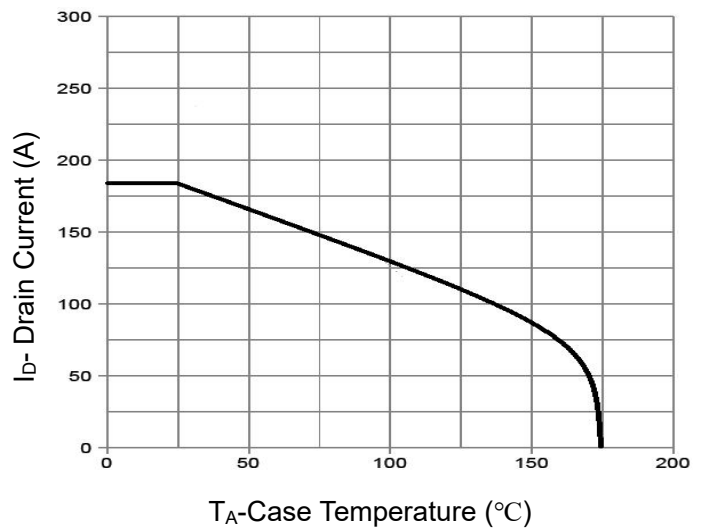
**Figure 7 Capacitance vs Vds**



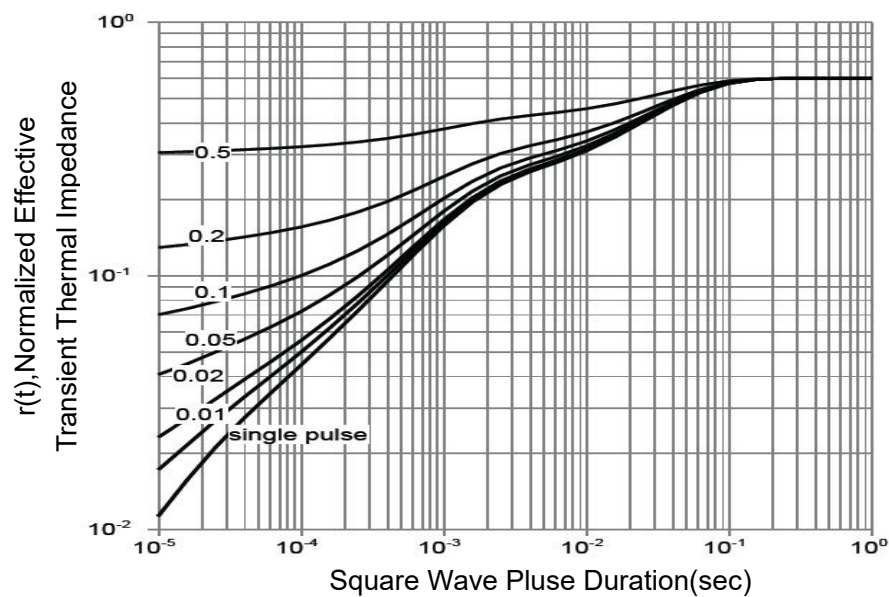
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area** (Note 3)

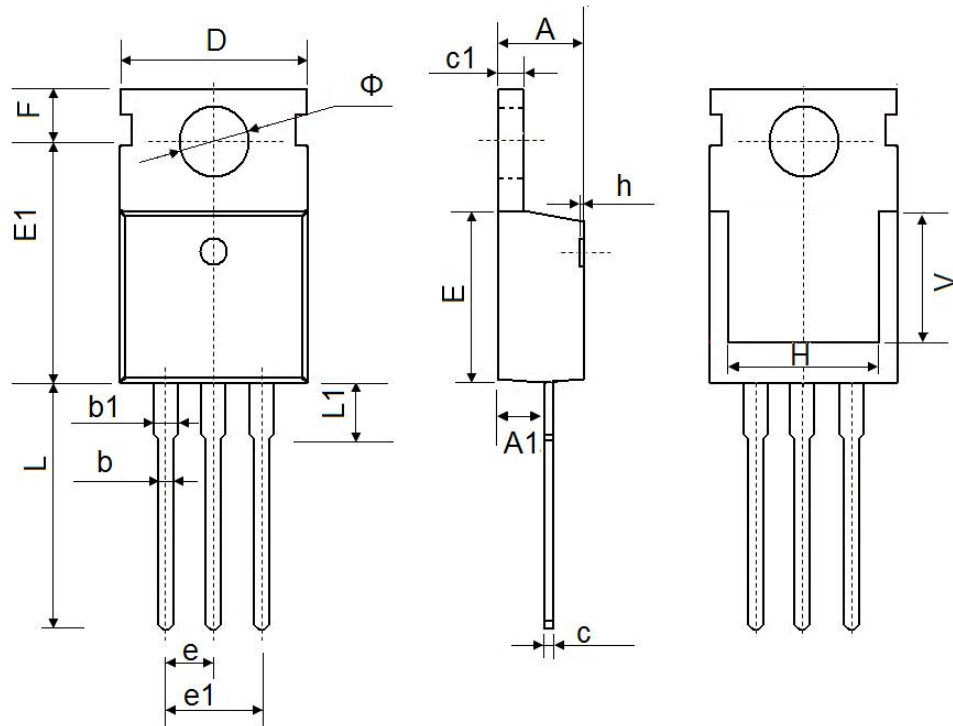


**Figure 10 Current De-rating**



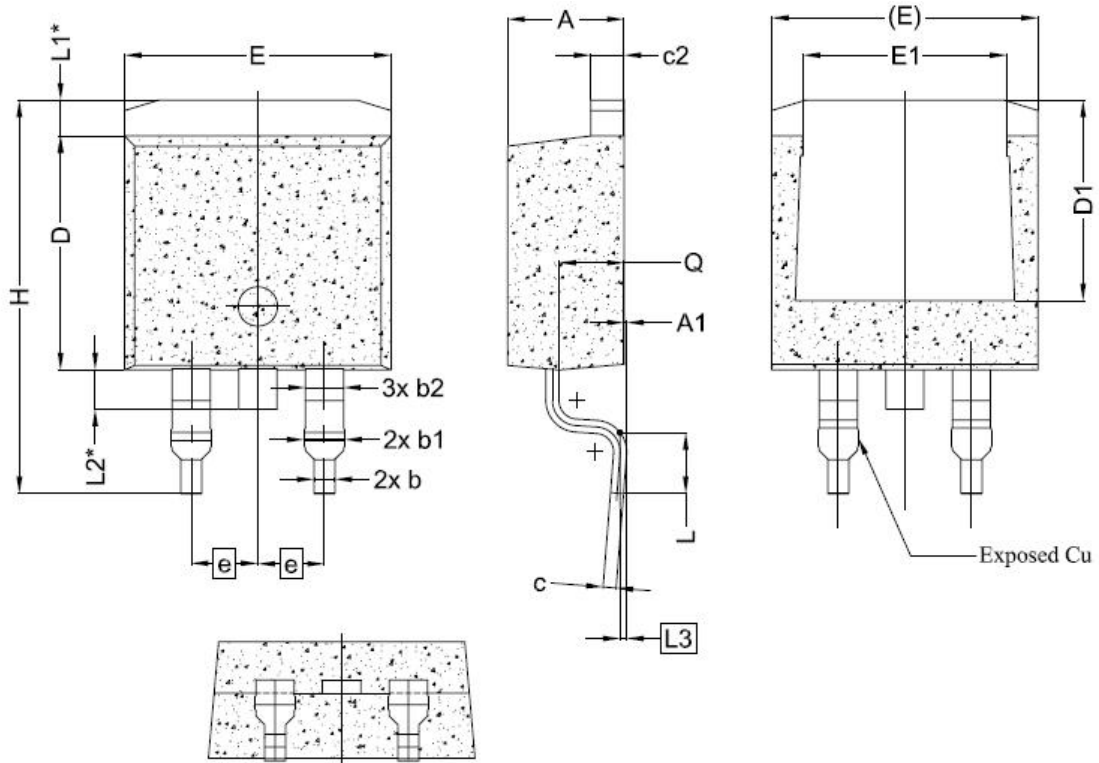
**Figure 11 Normalized Maximum Transient Thermal Impedance**

## TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	6.900 REF.		0.276 REF.	
Φ	3.400	3.800	0.134	0.150

## TO-263-2L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	4.24	4.44	4.64
A1	0.00	0.10	0.25
b	0.70	0.80	0.90
b1	1.20	1.55	1.75
b2	1.20	1.45	1.70
c	0.40	0.50	0.60
c2	1.15	1.27	1.40
D	8.82	8.92	9.02
D1	6.86	7.65	-
E	9.96	10.16	10.36
E1	6.89	7.77	7.89
e	2.54BSC		
H	14.61	15.00	15.88
L	1.78	2.32	2.79
L1	1.36 REF.		
L2	1.50 REF.		
L3	0.25 BSC		
Q	2.30	2.48	2.70

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