

## **NCE N-Channel Super Trench Power MOSFET**

#### **Description**

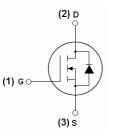
The NCEP02T11T uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{\text{DS(ON)}}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

#### **General Features**

- $V_{DS}$  =200V, $I_{D}$  =110A  $R_{DS(ON)}$  <10.5mΩ @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

### **Application**

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic diagram



TO-247 top view

100% UIS TESTED! 100% ΔVds TESTED!

### **Package Marking and Ordering Information**

			<u> </u>			
<b>Device Marking</b>	Device	Device Package	Reel Size	Tape width	Quantity	
NCEP02T11T	NCEP02T11T	TO-247-3L	-	-	-	

Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	200	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	110	Α
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	77.7	А
Pulsed Drain Current	I <sub>DM</sub>	440	А
Maximum Power Dissipation	P <sub>D</sub>	330	W
Derating factor		2.2	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	2000	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	°C

#### **Thermal Characteristic**

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	R <sub>eJC</sub>	0.45	°C/W
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# Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	200		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =200V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2.5		4.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =55A	-	9.4	10.5	mΩ
Gate resistance	$R_G$		-	3.8	-	Ω
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =10V,I <sub>D</sub> =55A	70	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ 400\/\/ 0\/	-	6635	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =100V, $V_{GS}$ =0V,	-	450.6	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	11	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	$t_{d(on)}$		-	19.5	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =100 $V$ , $I_{D}$ =55 $A$	-	28	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =4.7 $\Omega$	-	48	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS
Total Gate Charge	Qg	V 400V/I 55A	-	89.5		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =100V,I <sub>D</sub> =55A,	-	40.5		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	15.7		nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =110A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	110	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25^{\circ}C, I_F = 55A$	-	184		nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	1.96		uC

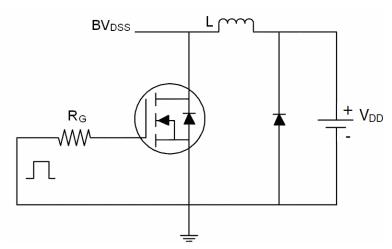
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25  $^{\circ}\text{C}$  ,V  $_{DD}$  =50 V ,V  $_{G}$  =10 V ,L=0.5 mH ,Rg=25  $\Omega$

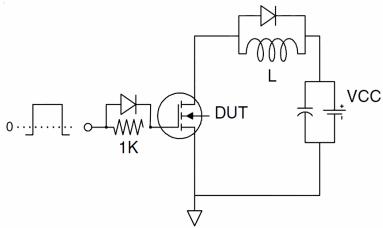


## **Test Circuit**

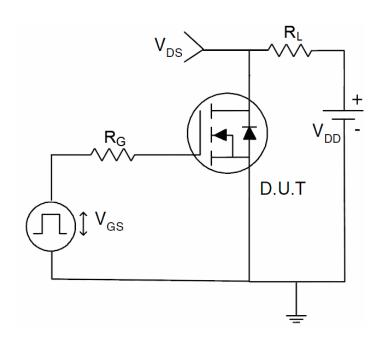
# 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit

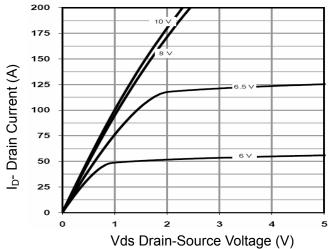


# 3) Switch Time Test Circuit

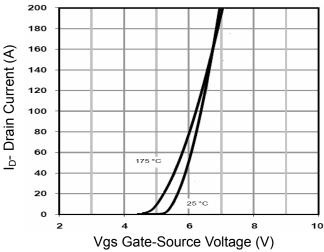




## **Typical Electrical and Thermal Characteristics**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

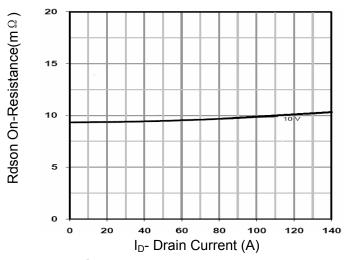


Figure 3 Rdson- Drain Current

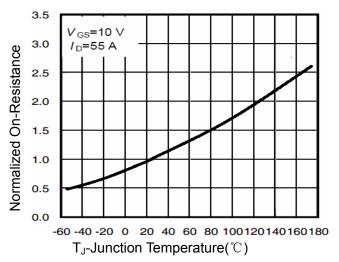


Figure 4 Rdson-JunctionTemperature

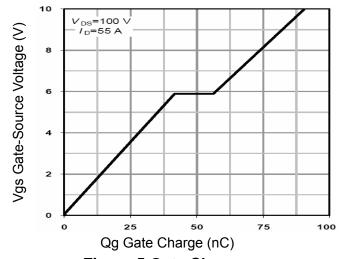


Figure 5 Gate Charge

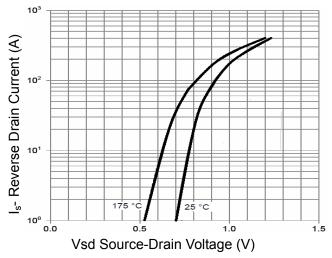
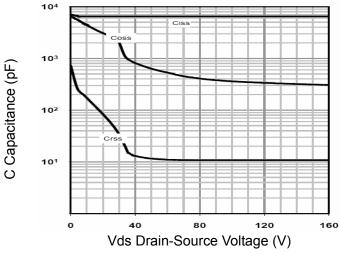


Figure 6 Source- Drain Diode Forward





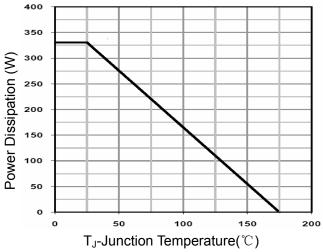
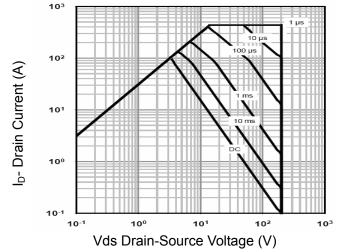
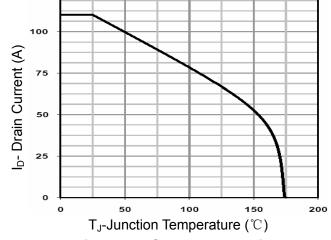


Figure 7 Capacitance vs Vds Figure 9 Power De-rating

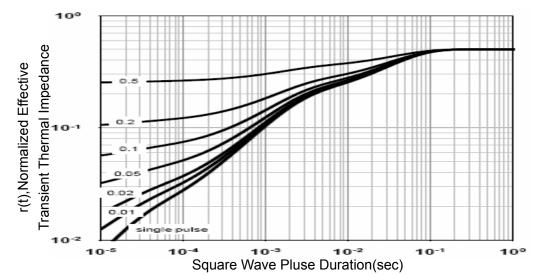
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**Figure 8 Safe Operation Area** 

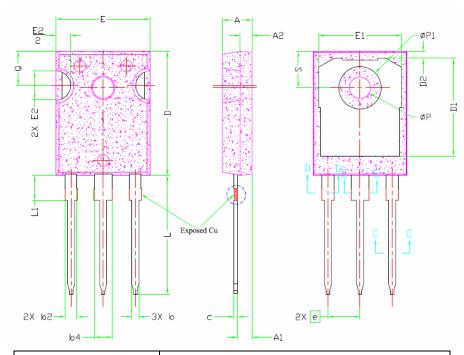
Figure 10 Current De-rating



**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **TO-247 Package Information**



Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
А	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.50	2.00	2.49	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	
b5	2.87	3.00	3.18	
С	0.55	0.60	0.69	
c1	0.55	0.60	0.65	
D	20.80	20.95	21.1	
D1	16.25	16.55	17.65	
D2	0.51	1.19	1.35	
Е	15.75	15.94	16.13	
E1	13.46	14.02	14.16	
E2	4.32	4.91	5.49	
L	19.81	20.07	20.32	
L1	4.10	4.19	4.40	
Q	5.39	5.79	6.20	
ФР	3.56	3.61	3.65	
S	6.04	6.17	6.30	

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# NCEP02T11T

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