NCE N-Channel Super Trench II Power MOSFET

Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

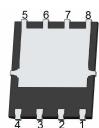
General Features

- V_{DS} =30V, I_D =65A $R_{DS(ON)}$ =2.65m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =4m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

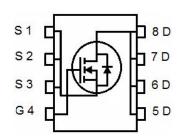
DFN 5X6





Top View

Bottom View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P030N30GU	NCEP030N30GU	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	65	Α
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	50.5	А
Pulsed Drain Current	I _{DM}	260	А
Maximum Power Dissipation	P _D	40	W
Derating factor		0.32	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	180	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$



NCEP030N30GU

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2) R _{θJC} 3.12 °C/W	Thermal Resistance,Junc	tion-to-Case ^(Note 2)	$R_{ heta JC}$	3.12	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	30		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.0	1.6	2.5	V
Dunin Course On State Desistance	Б	V _{GS} =10V, I _D =32.5A	-	2.65	3	mΩ
Drain-Source On-State Resistance Forward Transconductance Dynamic Characteristics (Note4) nput Capacitance Dutput Capacitance Reverse Transfer Capacitance Switching Characteristics (Note 4)	R _{DS(ON)}	V _{GS} =4.5V, I _D =32.5A	-	4	4.9	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =32.5A		50	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	V _{DS} =15V,V _{GS} =0V, F=1.0MHz	-	1626	-	PF
Output Capacitance	Coss		-	788	-	PF
Reverse Transfer Capacitance	Crss		-	32	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	4.5	-	nS
Turn-on Rise Time	t _r	V_{DD} =15V, I_{D} =32.5A V_{GS} =10V, R_{G} =1.6 Ω	-	6	-	nS
Turn-Off Delay Time	t _{d(off)}		-	20	-	nS
Turn-Off Fall Time	tf		-	4.5	-	nS
Total Gate Charge	Qg	\/ 45\/1 00.54	-	26.5	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =32.5A,	-	5.5		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	3.1		nC
Drain-Source Diode Characteristics			<u>'</u>		'	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =32.5A	-		1.2	V
Diode Forward Current (Note 2)	ls		-	-	65	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S	-	12	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	20	-	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V_DD=20V,V_G=10V,L=0.5mH,Rg=25 Ω
- 6. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of TJ(MAX)=150° C. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

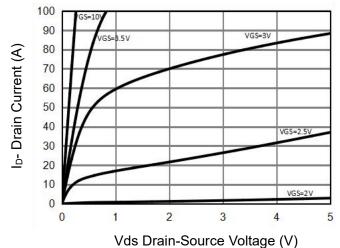


Figure 1 Output Characteristics

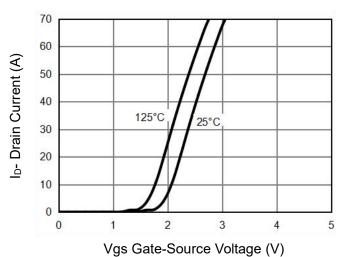


Figure 2 Transfer Characteristics

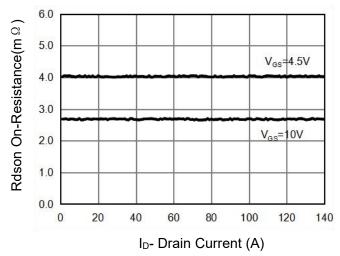


Figure 3 Rdson- Drain Current

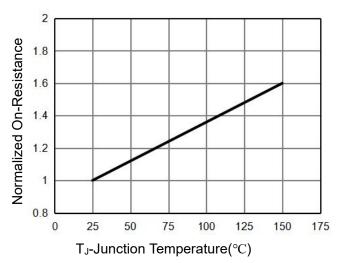


Figure 4 Rdson-Junction Temperature

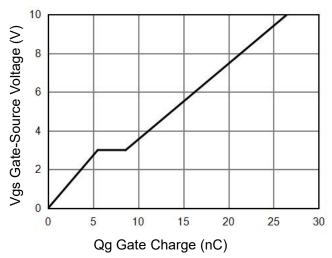


Figure 5 Gate Charge

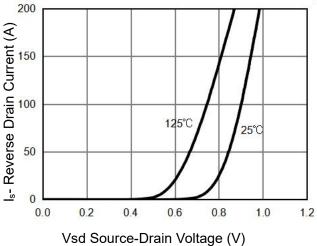


Figure 6 Source- Drain Diode Forward



Ip- Drain Current (A)

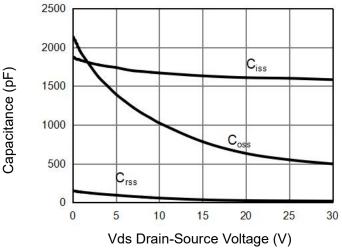
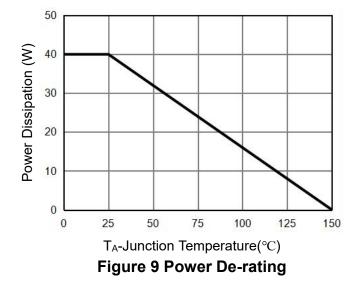


Figure 7 Capacitance vs Vds



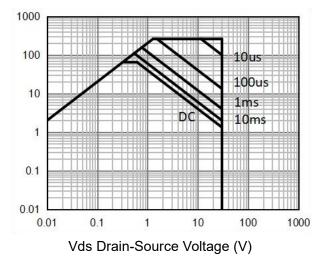


Figure 8 Safe Operation Area(Note 6)

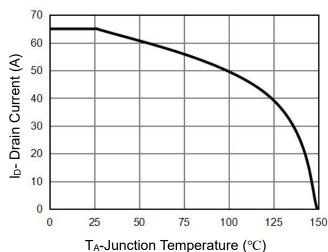


Figure 10 Current De-rating

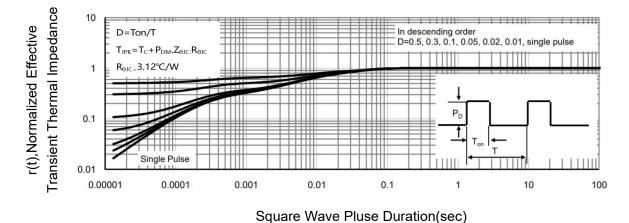
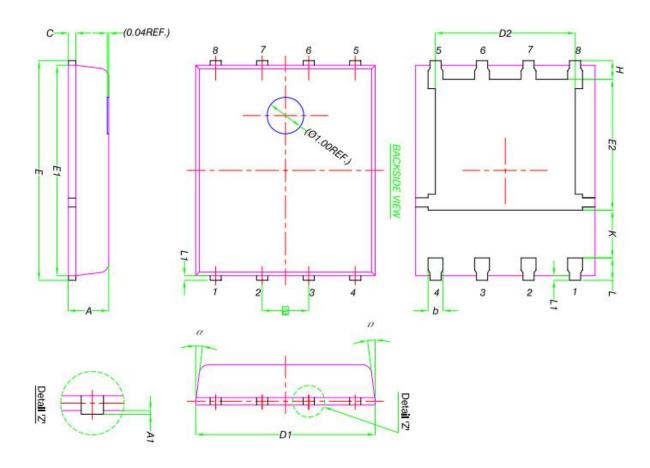
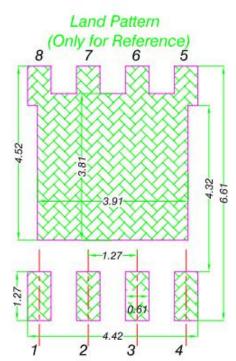


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



DIM. A A1	N	ILLIMET	ERS
DIIVI.	MIN.	NOM.	MAX.
Α	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
С	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
Ε	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
е	5.90 6.00 5.70 5.75		
Н	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
α	0°	15	12°





NCEP030N30GU

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