

NCE N-Channel Super Trench II Power MOSFET

Description

The NCEP035N60AG uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

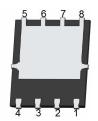
General Features

- V_{DS} =60V,I_D =90A
 - $R_{DS(ON)}$ =2.8m Ω (typical) @ V_{GS} =10V
 - $R_{DS(ON)}$ =3.5m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

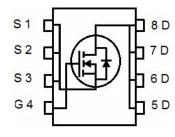
DFN 5X6





Top View

Bottom View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P035N60AG	NCEP035N60AG	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	90	А
Drain Current-Continuous(T _C =100 °C)	I _D (100°C)	63.6	Α
Pulsed Drain Current	I _{DM}	360	А
Maximum Power Dissipation	P _D	85	W
Derating factor		0.68	W/℃
Single pulse avalanche energy (Note 1)	E _{AS}	520	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance,Junction-to-Case	R _{eJC}	1.47	°C/W

NCEP035N60AG

Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	60		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.0	1.7	2.4	V
Desir Course On Otata Basistana	Б	V _{GS} =10V, I _D =45A	-	2.8	3.5	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =45A	-	3.5	4.5	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =45A	40	-	-	S
Dynamic Characteristics						
Input Capacitance	C _{lss}		-	4000	-	PF
Output Capacitance	Coss	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	605	-	PF
Reverse Transfer Capacitance	C _{rss}		-	44	-	PF
Switching Characteristics (Note 2)			•			
Turn-on Delay Time	t _{d(on)}		-	11	-	nS
Turn-on Rise Time	tr	V_{DD} =30 V , I_{D} =45 A	-	5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =4.7 Ω	-	49	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg)/ 00\/L 45A	-	73		nC
Gate-Source Charge	Qgs	$V_{DS}=30V, I_{D}=45A,$	-	12.5		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	11		nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =45A	-		1.2	V
Diode Forward Current	Is		-	-	90	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S	-	48		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/μs	-	60		nC

Notes:

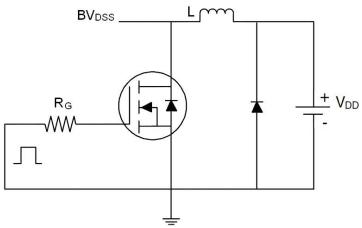
^{1.} EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=30V,VG=10V,L=0.5mH,Rg=25 Ω

^{2.} Guaranteed by design, not subject to production

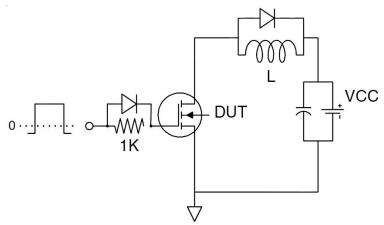
^{3.} These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of TJ(MAX)=150° C. The SOA curve provides a single pulse rating.

Test Circuit

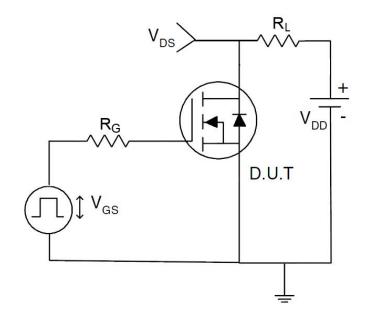
1) E_{AS} test Circuit



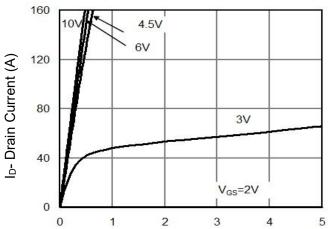
2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics

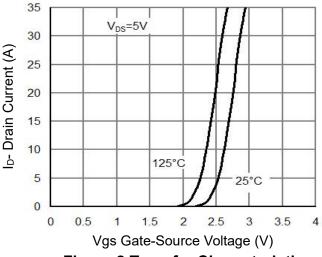


Figure 2 Transfer Characteristics

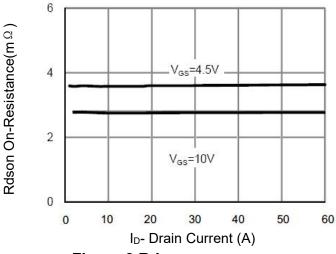
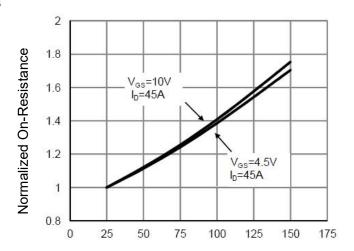


Figure 3 Rdson- Drain Current



T_J-Junction Temperature(°C)

Figure 4 Rdson-JunctionTemperature

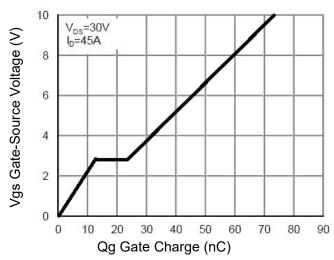


Figure 5 Gate Charge

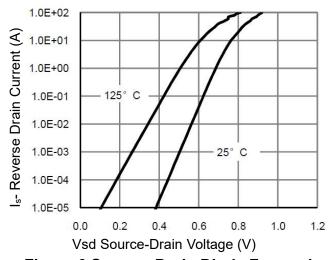


Figure 6 Source- Drain Diode Forward

1E-05

0.0001

0.001

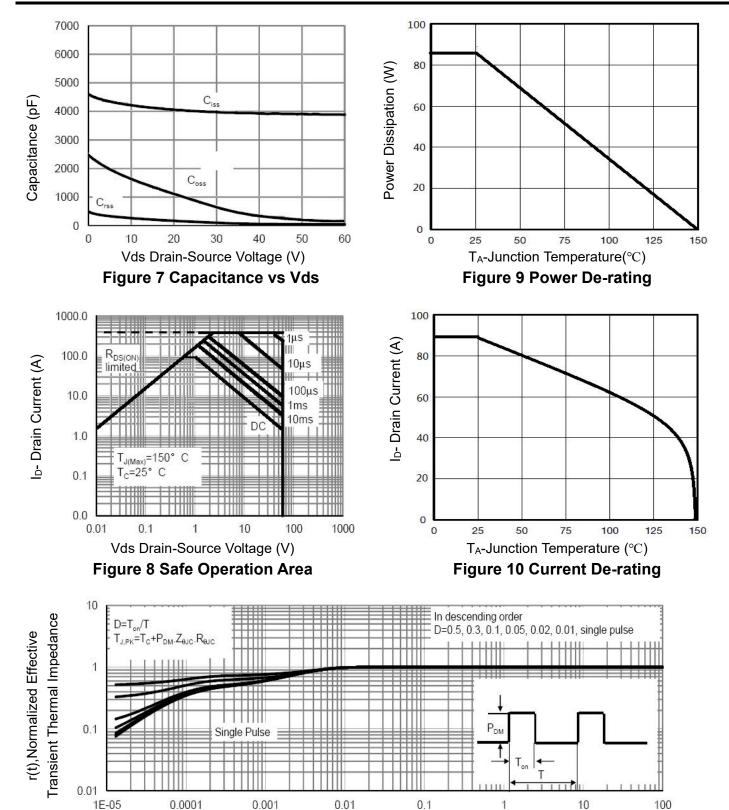
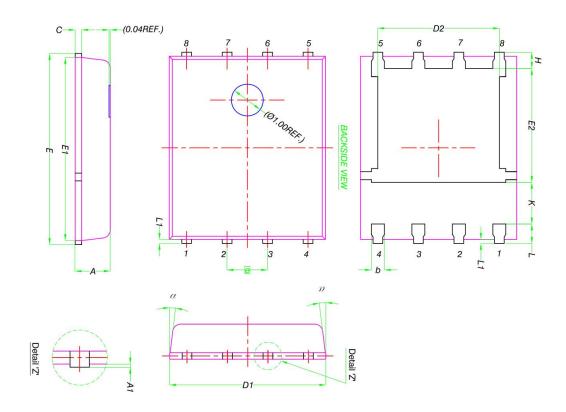


Figure 11 Normalized Maximum Transient Thermal Impedance

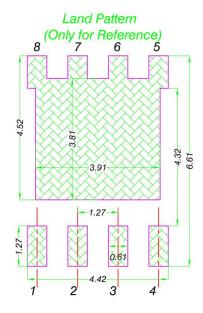
Square Wave Pluse Duration(sec)

10

DFN5X6-8L(G) Package Information



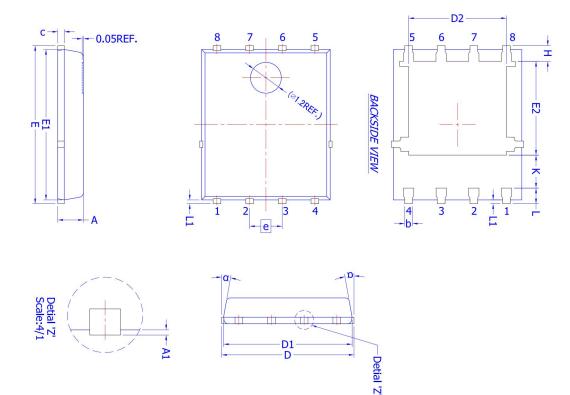
DIM.	MILLIMETERS			
	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
A1	0	8	0.05	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Ε	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	1.27 BSC			
Н	0.41	0.51	0.61	
K	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
α	0°	-	12°	



Note:

- 1. All Dimension Are In mm.
- But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.

DFN5X6-8L(P) Package Information

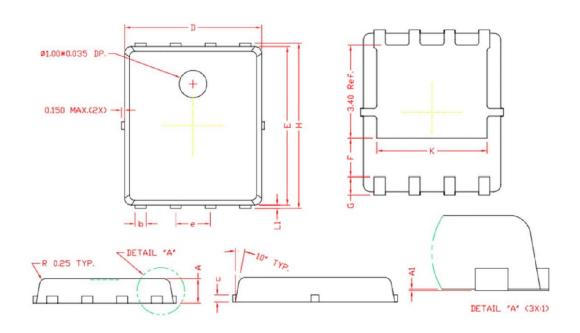


D/4	MILLIMETERS			
DIM.	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
A1	0	-	0.05	
Ь	0.30	0.40	0.50	
С	0.20	0.25	0.30	
D		5.15 BSC	7	
D1		7		
D2	3.76	3.81	3.86	
E	(6.15 BSC	?	
E1	5.80	5.85	5.90	
E2	3.45	3.65	3.85	
e	1.27 BSC			
Н	0.51	0.61	0.71	
K	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.08	0.15	0.23	
α	10°	11°	12°	

Note:

- 1. All Dimension Are In mm;
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic. Body Exclusive Of Mold Flash, Tie Bar, Tie Bar Burrs Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.

DFN5X6-8L(X) Package Information



COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX	
A	0.80	0.90	1.00	
A1	0.00	0.03	0.05	
b	0.35	0.42	0.49	
С	0. 254 REF.			
D	4.90	5.00	5. 10	
F	1.40 REF.			
E	5. 70	5. 70 5. 80		
е	1.27 BSC.			
Н	5.95	6.08	6. 20	
L1	0.10	0.14	0.18	
G	0.60 REF.			
K	4.00 REF.			

NCEP035N60AG

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