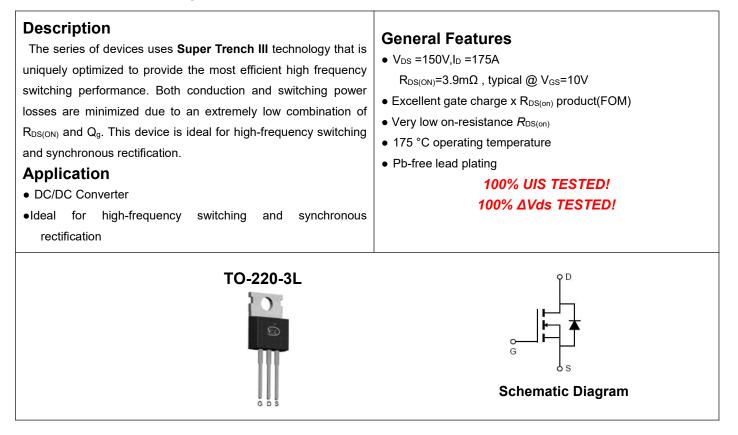


## NCE N-Channel Super Trench III Power MOSFET



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP048NH150	NCEP048NH150	TO-220-3L	-	-	-

### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	150	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	175	А
Drain Current-Continuous(Tc=100℃)	l₀ (100°C)	122	А
Pulsed Drain Current	I <sub>DM</sub>	700	A
Maximum Power Dissipation	PD	323	W
Derating factor		2.15	W/℃
Single pulse avalanche energy <sup>(Note 1)</sup>	Eas	1536	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	0.46	°C <b>/W</b>



### Electrical Characteristics (Tc=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	I					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	150	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =150V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics	I		<b>I</b>			
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	2.5	3.5	4.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =20A	-	3.9	4.8	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =10V,I <sub>D</sub> =40A	-	75	-	S
Dynamic Characteristics	I		•			
Input Capacitance	Clss	V <sub>DS</sub> =75V,V <sub>GS</sub> =0V,	-	7150	-	PF
Output Capacitance	Coss		-	2050	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	47	-	PF
Switching Characteristics (Note 2)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	30	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =75V,I <sub>D</sub> =75A	-	40	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> =10V,R <sub>G</sub> =4.7Ω	-	70	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =75V,I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	106	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	36	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	27	-	nC
Drain-Source Diode Characteristics	L					
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>F</sub> = I <sub>S</sub>	-	-	1.2	V
Diode Forward Current	Is		-	-	175	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 100A	-	108	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-	270	-	nC

#### Notes:

1. EAS condition : Tj=25 $^\circ C, V_{DD}$ =50V, V\_G=10V, L=0.5mH, Rg=25 $\Omega$ 

2. Guaranteed by design, not subject to production

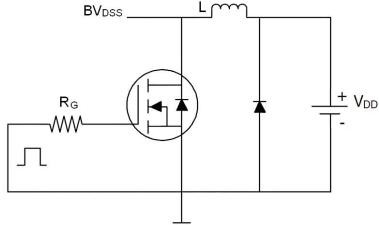
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=175°C. The SOA curve provides a single pulse rating.



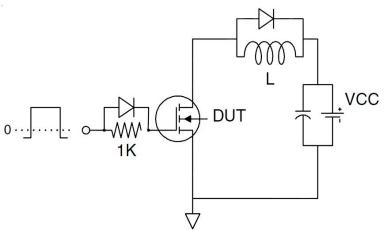
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# Test Circuit

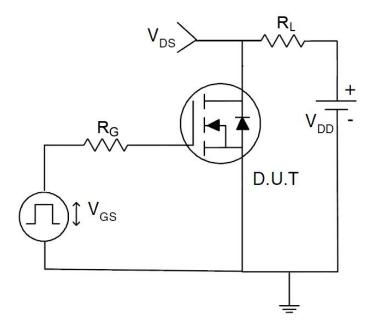
1) E<sub>AS</sub> test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





125

80

0.8

1.0

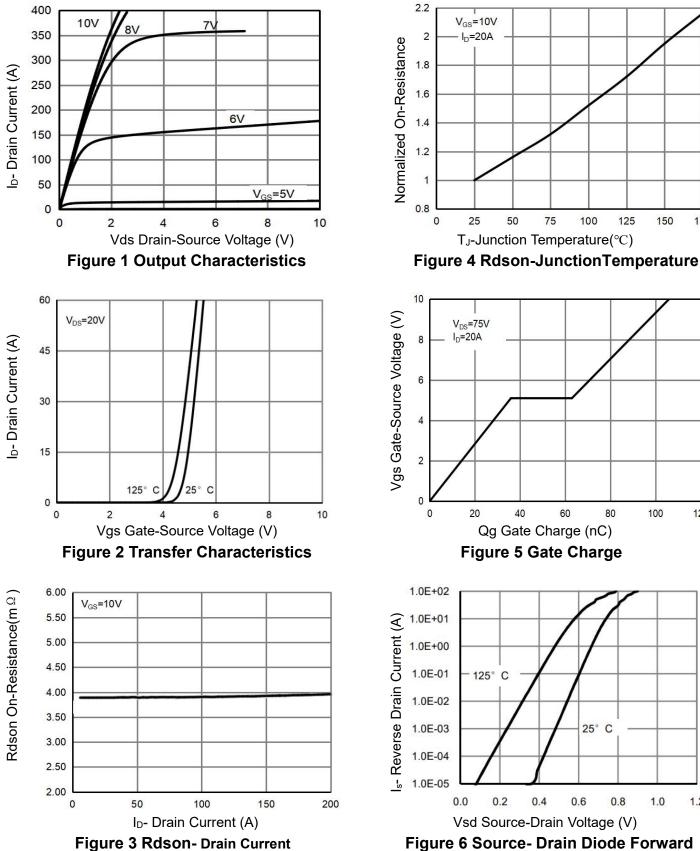
100

120

150

175



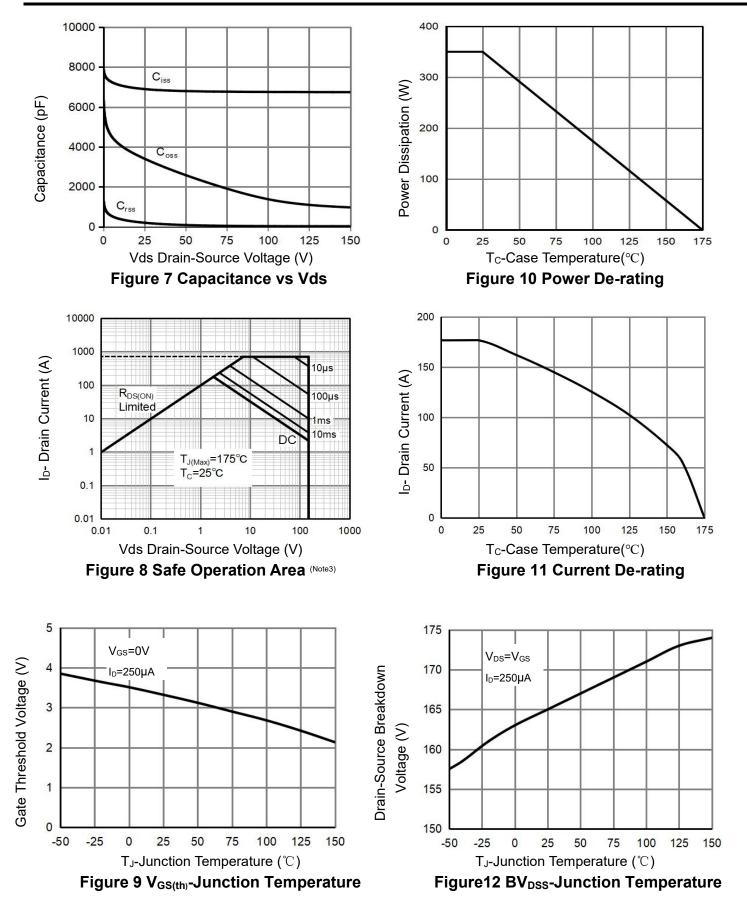


1.2



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# NCEP048NH150





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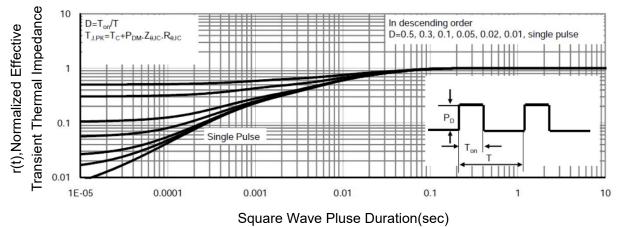
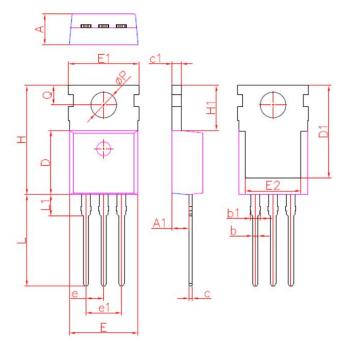


Figure 13 Normalized Maximum Transient Thermal Impedance



## TO-220-3L Package Information



		)220		
DIM.	MIN.	NOM.	MAX.	
А	4.20	4.40	4.60	
A1	2.25	2.40	2.55	
b	0.70	0.80	0.90	
b1	1.17	1.27	1.37	
с	0.33	0.50	0.65	
c1	1.20	1.30	1.40	
D	8.95	9.20	9.75	
D1	13.10	13.30	13.50	
Е	9.74	9.84	10.04	
E1	9.91	10.08	10.25	
E2	7.90	8.00	8.10	
е		2.54BSC		
e1	5.08BSC			
Н	15.45	15.65	15.85	
H1	6.30	6.45	6.60	
L	12.90	13.13	13.40	
L1	2.85	3.05	3.25	
Q	2.65	2.80	2.95	
øР	3.40	3.68	3.80	



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