

## NCE N-Channel Super Trench II Power MOSFET

### Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### General Features

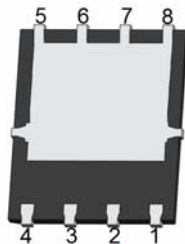
- $V_{DS} = 120V, I_D = 110A$   
 $R_{DS(ON)} = 5.2m\Omega$ , typical @  $V_{GS} = 10V$   
 $R_{DS(ON)} = 6.4m\Omega$ , typical @  $V_{GS} = 4.5V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 150°C operating temperature
- Pb-free lead plating

**100% UIS TESTED!**  
**100%  $\Delta V_{ds}$  TESTED!**

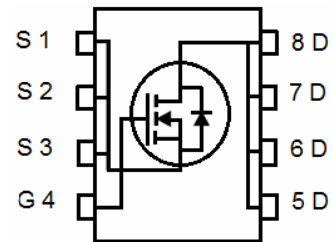
### DFN 5X6



Top View



Bottom View



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P055N12AG	NCEP055N12AG	DFN5X6-8L	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	120	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	110	A
Drain Current-Continuous( $T_C = 100^\circ C$ )	$I_D(100^\circ C)$	78	A
Pulsed Drain Current	$I_{DM}$	440	A
Maximum Power Dissipation	$P_D$	140	W
Derating factor		1.12	W/ $^\circ C$
Single pulse avalanche energy <sup>(Note 5)</sup>	$E_{AS}$	540	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

## Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	0.89	$^{\circ}C/W$
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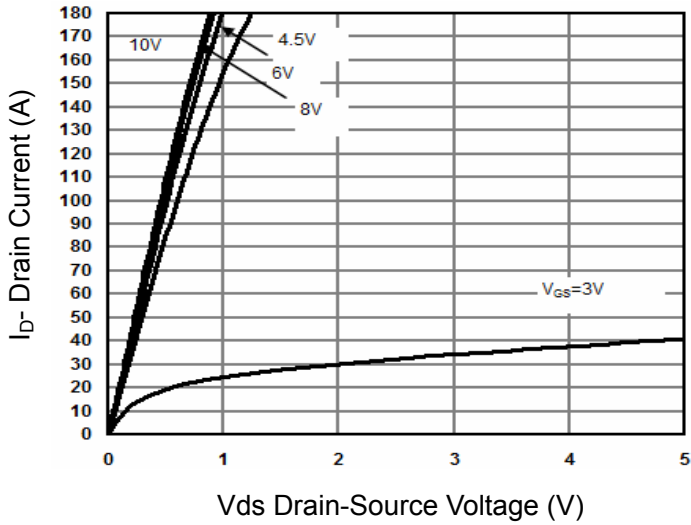
## Electrical Characteristics ( $T_C=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	120		-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=120V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.7	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=55A$	-	5.2	5.5	m $\Omega$
		$V_{GS}=4.5V, I_D=55A$	-	6.4	7.5	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=55A$		120	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{iss}$	$V_{DS}=60V, V_{GS}=0V,$ $F=1.0MHz$	-	5786	-	PF
Output Capacitance	$C_{oss}$		-	391	-	PF
Reverse Transfer Capacitance	$C_{riss}$		-	27	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=60V, I_D=55A,$ $V_{GS}=10V, R_G=3\Omega$	-	21	-	nS
Turn-on Rise Time	$t_r$		-	13	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	40	-	nS
Turn-Off Fall Time	$t_f$		-	12	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=60V, I_D=55A,$ $V_{GS}=10V$	-	96	-	nC
Gate-Source Charge	$Q_{gs}$		-	19.8		nC
Gate-Drain Charge	$Q_{gd}$		-	22.3		nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{SD}$	$V_{GS}=0V, I_S=55A$	-		1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_S$		-	-	110	A
Reverse Recovery Time	$t_{rr}$	$T_J = 25^{\circ}C, I_F = 55A$ $di/dt = 100A/\mu s$ <sup>(Note 3)</sup>	-	72	-	nS
Reverse Recovery Charge	$Q_{rr}$		-	140	-	nC

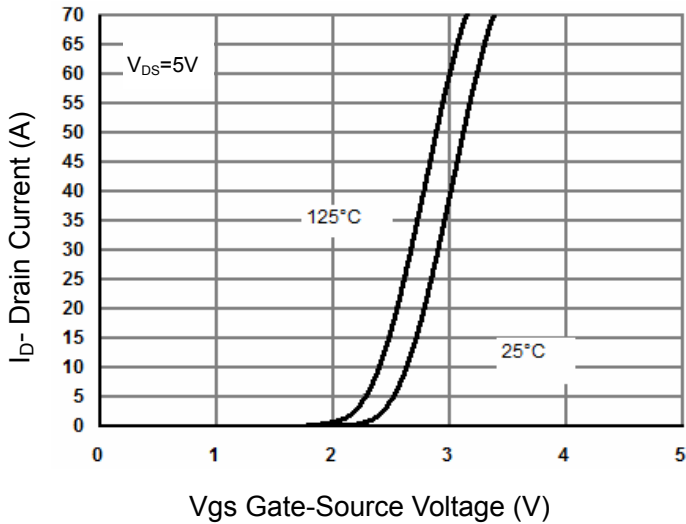
### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_J=25^{\circ}C, V_{DD}=50V, V_G=10V, L=0.5mH, R_G=25\Omega$

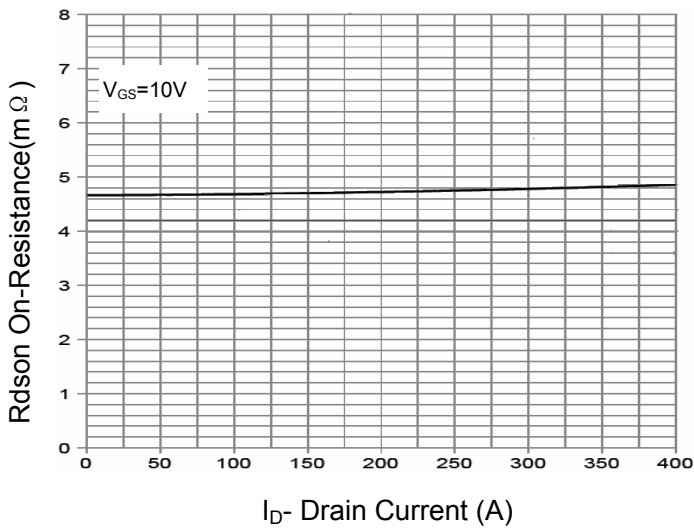
## Typical Electrical and Thermal Characteristics



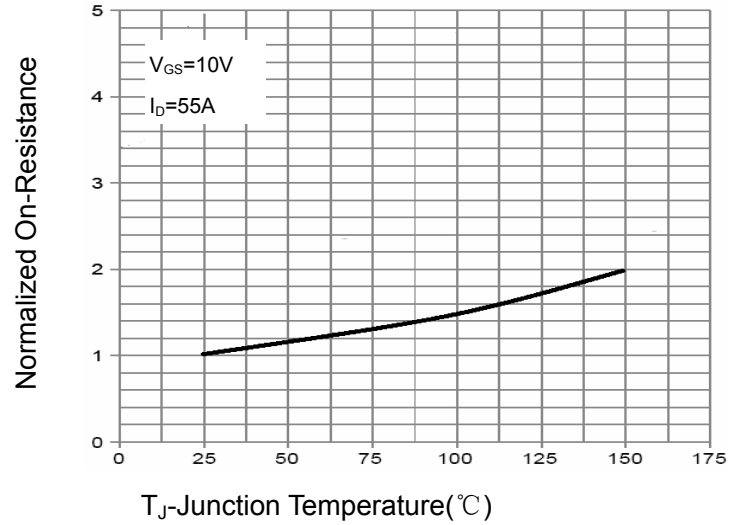
**Figure 1 Output Characteristics**



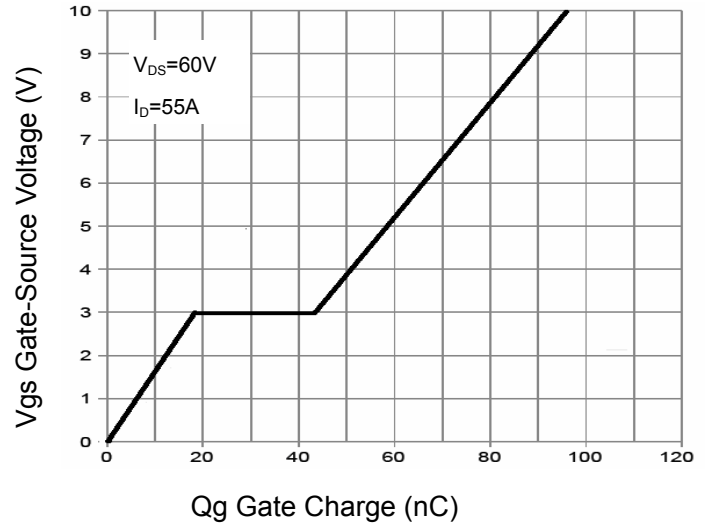
**Figure 2 Transfer Characteristics**



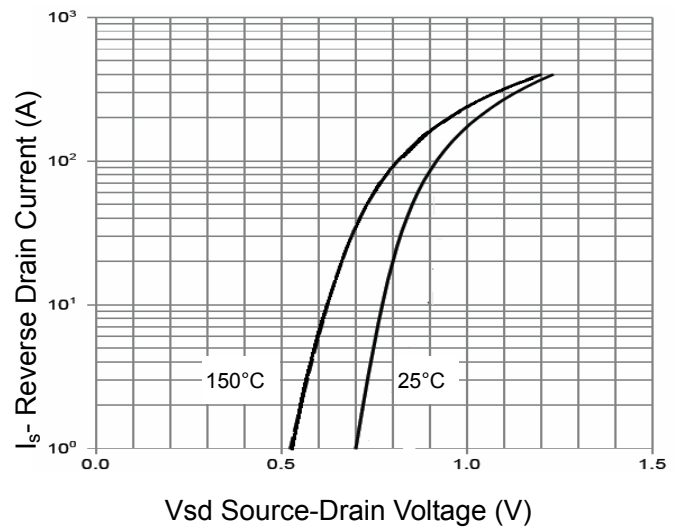
**Figure 3 Rdson- Drain Current**



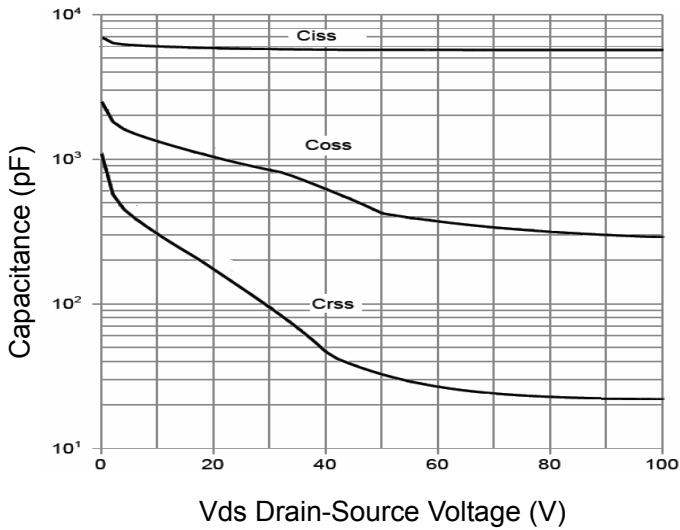
**Figure 4 Rdson-Junction Temperature**



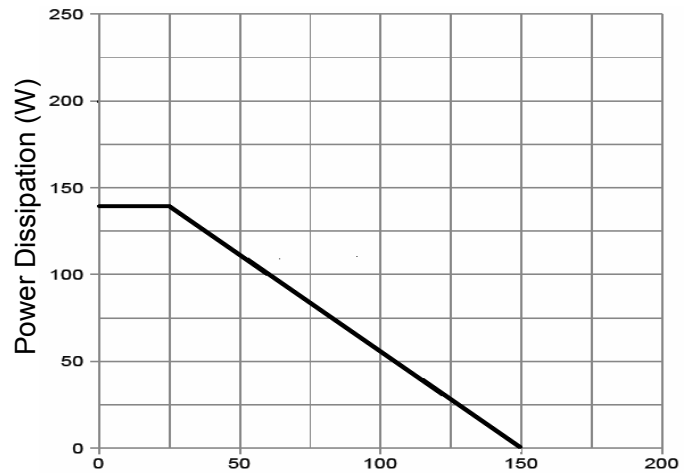
**Figure 5 Gate Charge**



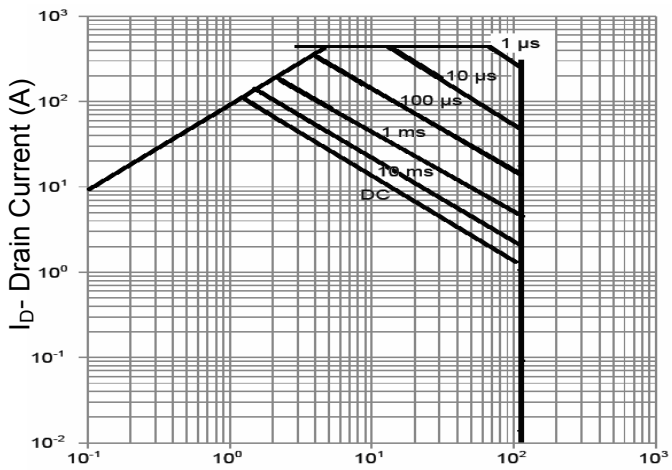
**Figure 6 Source- Drain Diode Forward**



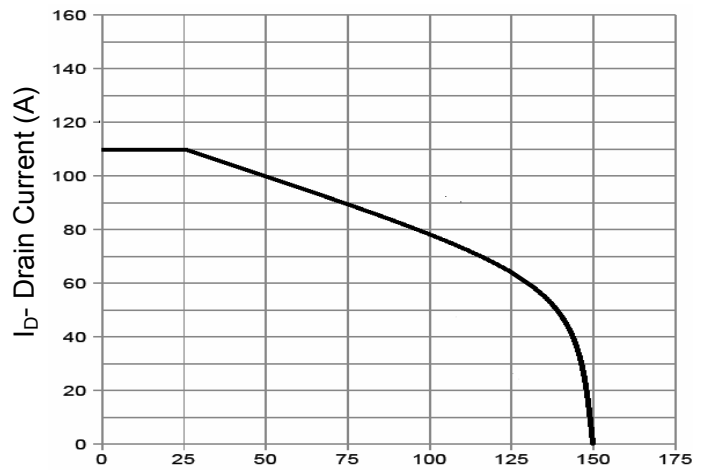
Vds Drain-Source Voltage (V)  
**Figure 7 Capacitance vs Vds**



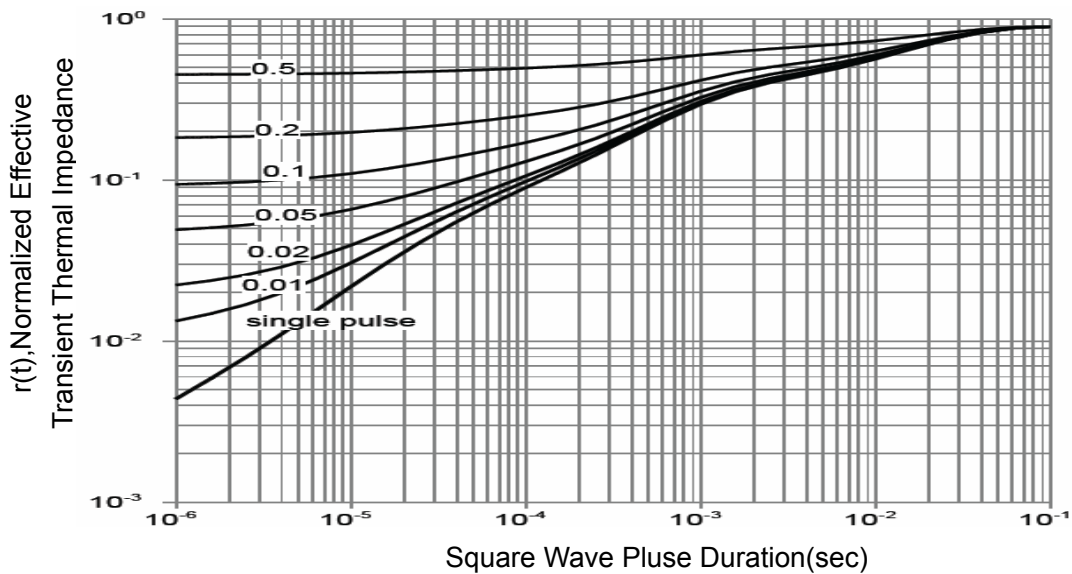
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 9 Power De-rating**



Vds Drain-Source Voltage (V)  
**Figure 8 Safe Operation Area**

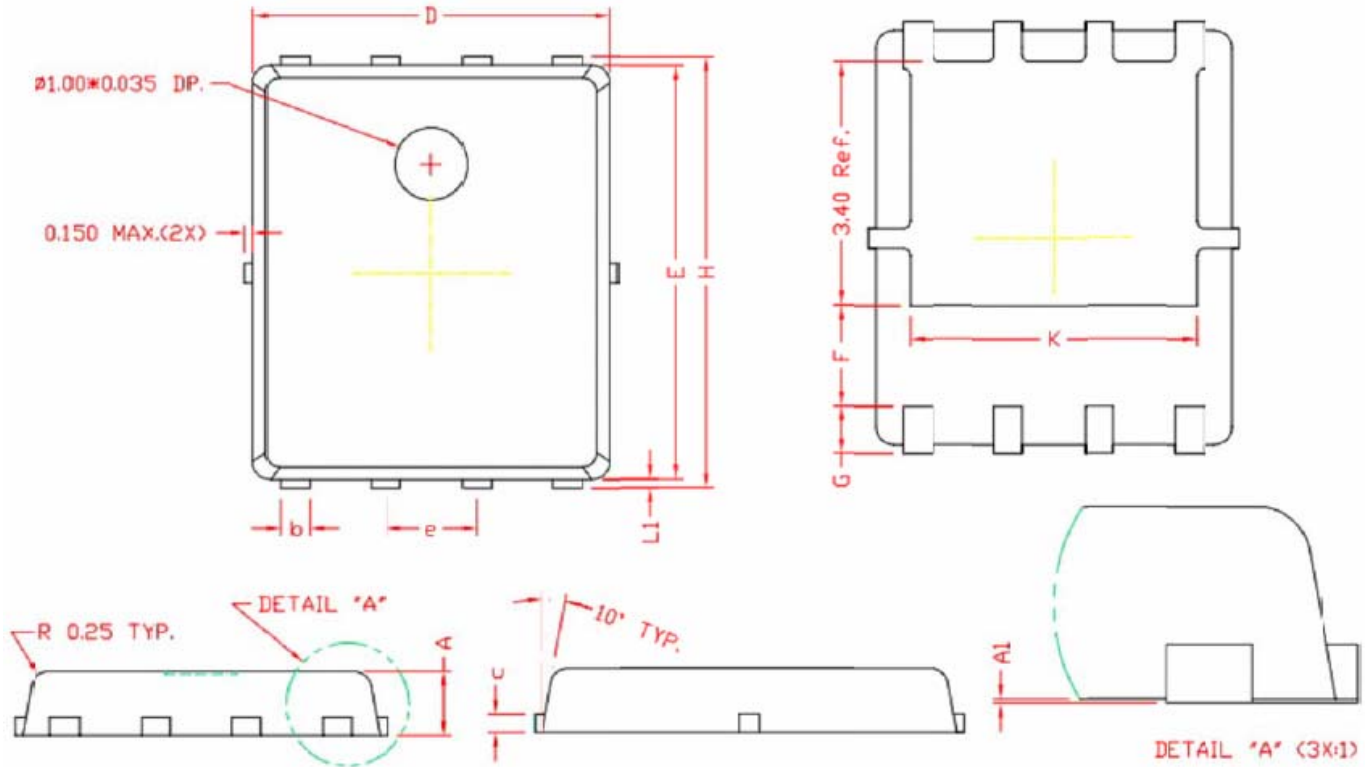


T<sub>J</sub>-Junction Temperature (°C)  
**Figure 10 Current De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## DFN5X6-8L Package Information



### COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.80	0.90	1.00
A1	0.00	0.03	0.05
b	0.35	0.42	0.49
c	0.254 REF.		
D	4.90	5.00	5.10
F	1.40 REF.		
E	5.70	5.80	5.90
e	1.27 BSC.		
H	5.95	6.08	6.20
L1	0.10	0.14	0.18
G	0.60 REF.		
K	4.00 REF.		

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