

NCE N-Channel Super Trench II Power MOSFET

Description

The NCEP072N10A uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

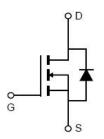
- V_{DS} =100V, I_D =88A $R_{DS(ON)}$ =6.2m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =8.4m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175°C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

TO-220



Top View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP072N10A	NCEP072N10A	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	88	А
Drain Current-Continuous(T _C =100 °C)	I _D (100°C)	63	Α
Pulsed Drain Current	I _{DM}	352	А
Maximum Power Dissipation	P _D	125	W
Derating factor		0.83	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	387	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2) R _{0JC} 1.2 °C/W

Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

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Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	100		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	·					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	1.7	2.5	V
Dunin Course On Chata Basistanas		V _{GS} =10V, I _D =44A	- 6.2 7.2			
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =44A		8.4	9.6	mΩ
Forward Transconductance	G FS	V _{DS} =5V,I _D =44A		60	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ F0\/\\ 0\/	-	4120	-	PF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V,	-	322	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	12.5	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	17	-	nS
Turn-on Rise Time	t _r	V _{DD} =50V,I _D =44A,	-	11	-	nS
Turn-Off Delay Time	$t_{\rm d(off)}$	V_{GS} =10V, R_{G} =3 Ω	-	36	-	nS
Turn-Off Fall Time	t _f		-	9	-	nS
Total Gate Charge	Qg		-	81	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =50V,I _D =44A,	-	13.9		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	21.3		nC
Drain-Source Diode Characteristics	1		'		'	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =44A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	88	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =44A	-	62	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	109	-	nC
		I .		1	L	

Notes:

- ${\it 1. Repetitive \ Rating: Pulse \ width \ limited \ by \ maximum \ junction \ temperature.}$
- 2. Surface Mounted on FR4 Board, t \leq 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V_DD=50V,V_G=10V,L=0.5mH,Rg=25 Ω



Typical Electrical and Thermal Characteristics

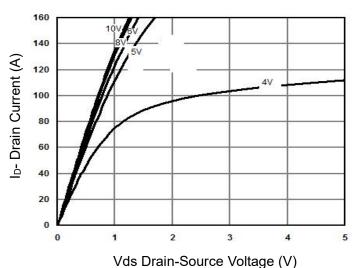


Figure 1 Output Characteristics

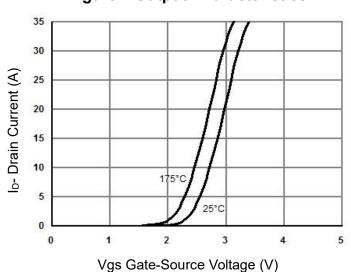


Figure 2 Transfer Characteristics

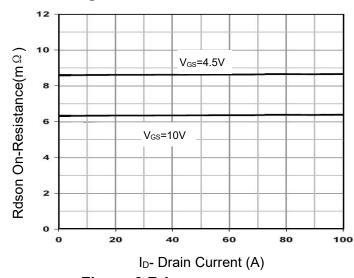
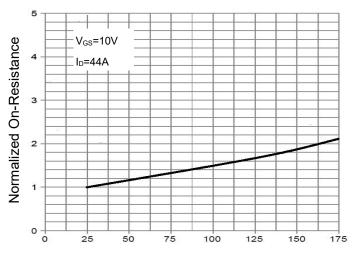
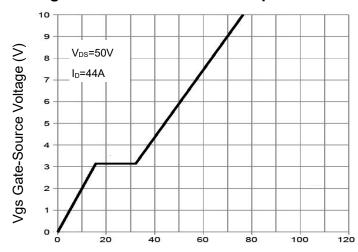


Figure 3 Rdson- Drain Current

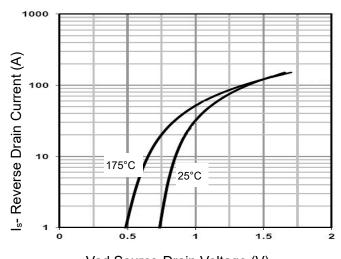


T_J-Junction Temperature(°C)

Figure 4 Rdson-Junction Temperature



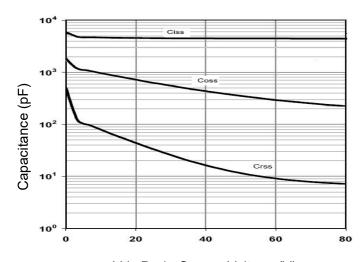
Qg Gate Charge (nC)
Figure 5 Gate Charge



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds

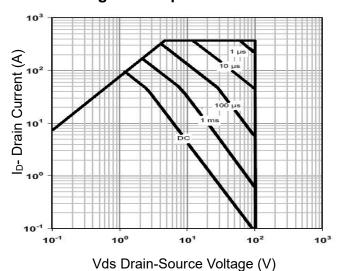
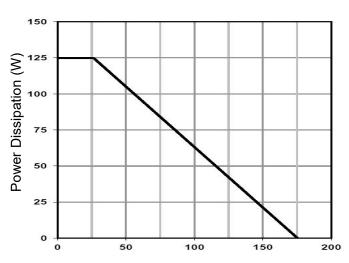


Figure 8 Safe Operation Area



T_J-Junction Temperature(°C) Figure 9 Power De-rating



T_J-Junction Temperature (°C)

100

150

200

Figure 10 Current De-rating

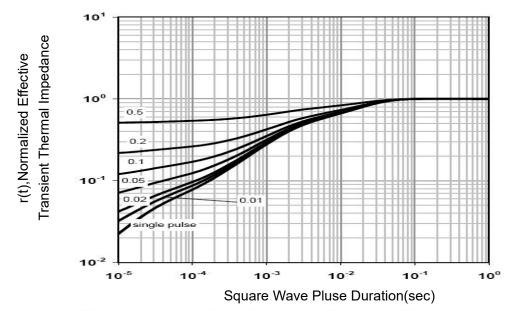
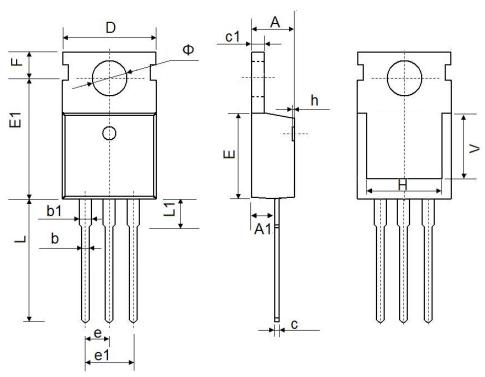


Figure 11 Normalized Maximum Transient Thermal Impedance

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TO-220-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches			
	Min.	Max.	Min.	Max.		
А	4.400	4.600	0.173	0.181		
A1	2.250	2.550	0.089	0.100		
b	0.710	0.910	0.028	0.036		
b1	1.170	1.370	0.046	0.054		
С	0.330	0.650	0.013	0.026		
c1	1.200	1.400	0.047	0.055		
D	9.910	10.250	0.390	0.404		
E	8.9500	9.750	0.352	0.384		
E1	12.650	12.950	0.498	0.510		
е	2.540	2.540 TYP.		0.100 TYP.		
e1	4.980	5.180	0.196	0.204		
F	2.650	2.950	0.104	0.116		
Н	7.900	8.100	0.311	0.319		
h	0.000	0.300	0.000	0.012		
L	12.900	13.400	0.508	0.528		
L1	2.850	3.250	0.112	0.128		
V	6.900	6.900 REF.		0.276 REF.		
Φ	3.400	3.800	0.134	0.150		

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NCEP072N10A

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