

NCE N-Channel Super Trench II Power MOSFET

Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- •Ideal for high-frequency switching and synchronous rectification

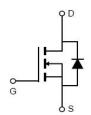
General Features

- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

TO-220F





Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP080N12F	NCEP080N12F	TO-220F	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	120	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	46.5	А
Drain Current-Continuous(T _C =100 °C)	I _D (100°C)	33	Α
Pulsed Drain Current	I _{DM}	186	А
Maximum Power Dissipation	P _D	45	W
Derating factor		0.3	W/°C
Single pulse avalanche energy (Note 1)	E _{AS}	352	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance,Junction-to-Case	R _{eJC}	3.33	°C/W	
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V1.0



Electrical Characteristics (Tc=25 $^{\circ}\mathrm{C}\,\text{unless}$ otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•		•	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA			-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =120V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics			•		•	
Gate Threshold Voltage	V _{GS(th)}	V_{DS} = V_{GS} , I_D = $250\mu A$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	8.5	9.4	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =20A		33	-	S
Dynamic Characteristics	<u>'</u>		•		•	
Input Capacitance	Clss	V _{DS} =60V,V _{GS} =0V, F=1.0MHz		3715	-	pF
Output Capacitance	Coss			275	-	pF
Reverse Transfer Capacitance	C _{rss}			18	-	pF
Switching Characteristics (Note 2)	·		•			
Turn-on Delay Time	t _{d(on)}	V_{DD} =60V, I_{D} =20A V_{GS} =10V, R_{G} =1.6 Ω		30	-	nS
Turn-on Rise Time	t _r			21	-	nS
Turn-Off Delay Time	t _{d(off)}			50	-	nS
Turn-Off Fall Time	t _f			21	-	nS
Total Gate Charge	Qg	- V _{DS} =60V,I _D =20A, - V _{GS} =10V		58	-	nC
Gate-Source Charge	Q _{gs}			21	-	nC
Gate-Drain Charge	Q_{gd}			14.5	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current	Is		-	-	46.5	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = 46.5A$		65	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/μs	-	105	-	nC

Notes:

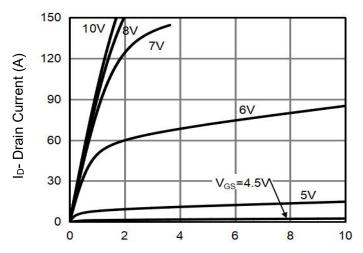
^{1.} EAS condition : Tj=25 $^{\circ}\text{C}\text{,V}_{DD}\text{=}50\text{V,V}_{G}\text{=}10\text{V,L=}0.5\text{mH,Rg=}25\Omega$

^{2.} Guaranteed by design, not subject to production

^{3.} These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=175°C. The SOA curve provides a single pulse rating.



Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)

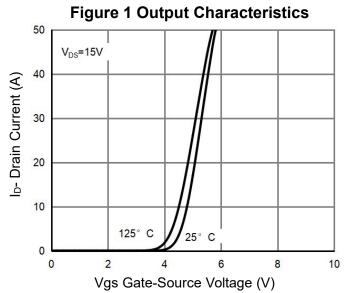


Figure 2 Transfer Characteristics

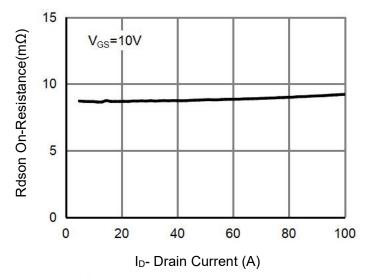


Figure 3 Rdson- Drain Current

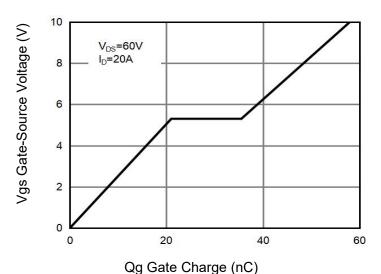


Figure 4 Gate Charge

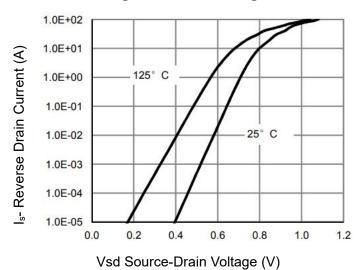


Figure 5 Source- Drain Diode Forward

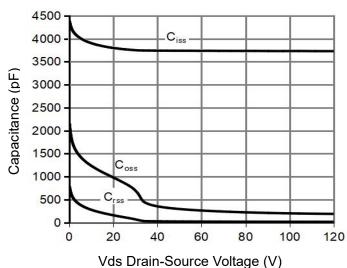
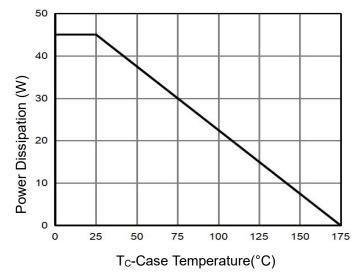


Figure 6 Capacitance vs Vds

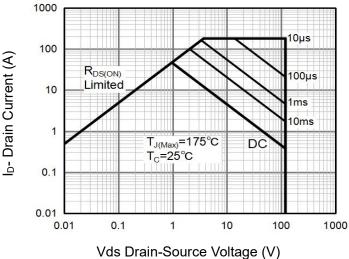




50 lp- Drain Current (A) 40 30 20 10 0 50 75 0 25 100 125 150 175 T_C-Case Temperature (°C)

Figure 7 Power De-rating

Figure 9 Current De-rating



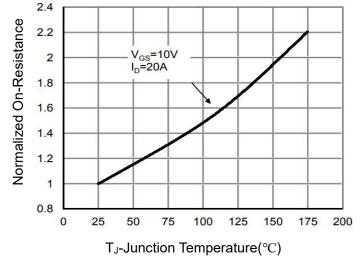
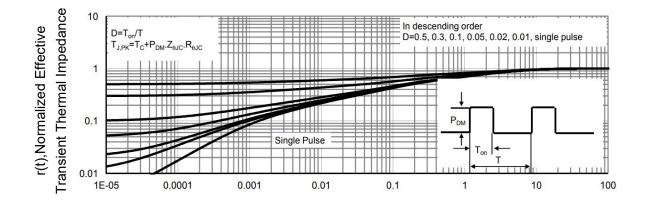


Figure 8 Safe Operation Area (Note3)

Figure 10 Rdson-Junction Temperature

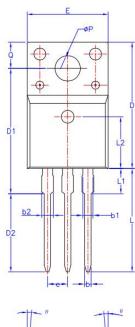


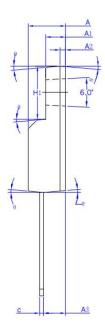
Square Wave Pluse Duration(sec)

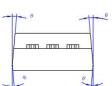
Figure 11 Normalized Maximum Transient Thermal Impedance



TO-220F-3L Package Information







SYMBOL	MIN	NOM	MAX		
Α	4.50	4.70	4.83		
A1	2.34	2.54	2.74		
A2	0.70 REF				
A3	2.56	2.93			
b	0.70	_	0.90		
b1	1.18	- <u> </u>	1.38		
b2	1 4	-	1.47		
С	0.45	0.50	0.60		
D	15.67	15.87	16.07		
D1	15.55	15.75	15.95		
D2	9.60	9.80	10.0		
E	9.96	10.16	10.36		
е	2.54BSC				
H1	6.48	6.68	6.88		
L	12.68	12.98	13.28		
L1	1-0		3.50		
L2	6.50REF				
ØΡ	3.08	3.18	3.28		
Q	3.20	<u></u>	3.40		
θ 1	1°	3°	5°		



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