

NCE N-Channel Super Trench Power MOSFET

Description

The NCEP1545AK uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(on)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

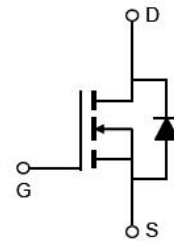
- $V_{DS} = 150V, I_D = 45A$
 $R_{DS(on)} = 26m\Omega$ (typical) @ $V_{GS} = 10V$
 $R_{DS(on)} = 30m\Omega$ (typical) @ $V_{GS} = 4.5V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!
100% ΔV_{ds} TESTED!

TO-252



Top View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP1545AK	NCEP1545AK	TO-252-2L	-	-	-

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	45	A
Drain Current-Continuous($T_C = 100^\circ C$)	$I_D(100^\circ C)$	31.8	A
Pulsed Drain Current	I_{DM}	180	A
Maximum Power Dissipation	P_D	130	W
Derating factor		0.88	W/ $^\circ C$
Single pulse avalanche energy ^(Note1)	E_{AS}	100	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Résistance, Junction-to-Case	$R_{\theta JC}$	1.15	$^\circ C/W$
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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

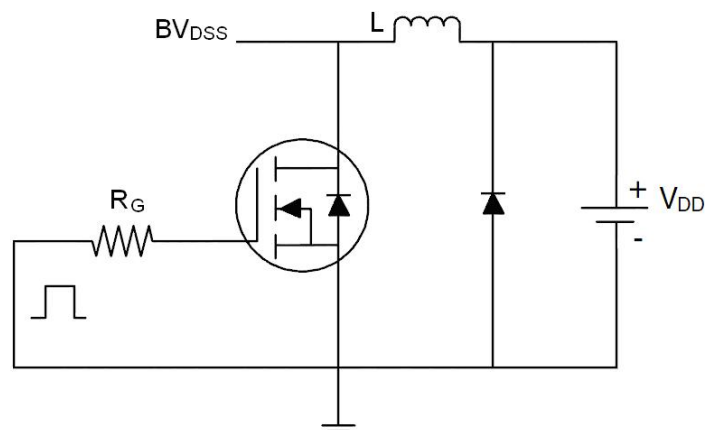
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =150V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	1.2	1.7	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	26	35	mΩ
		V _{GS} =4.5V, I _D =20A	-	30	45	mΩ
Forward Transconductance	g _{FS}	V _{DS} =5V,I _D =20A	15	-	-	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =75V,V _{GS} =0V, F=1.0MHz	-	1935		PF
Output Capacitance	C _{oss}		-	145		PF
Reverse Transfer Capacitance	C _{rss}		-	11		PF
Switching Characteristics <small>(Note 2)</small>						
Turn-on Delay Time	t _{d(on)}	V _{DD} =75V, R _L =7.5Ω V _{GS} =10V,R _G =3Ω	-	10	-	nS
Turn-on Rise Time	t _r		-	6.5	-	nS
Turn-Off Delay Time	t _{d(off)}		-	16	-	nS
Turn-Off Fall Time	t _f		-	7	-	nS
Total Gate Charge	Q _g	V _{DS} =75V,I _D =20A, V _{GS} =10V	-	33	-	nC
Gate-Source Charge	Q _{gs}		-	7.2	-	nC
Gate-Drain Charge	Q _{gd}		-	7.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current	I _S		-	-	45	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S	-	30	-	nS
Reverse Recovery Charge	Q _{rr}	di/dt = 100A/μs	-	135	-	nC

Notes:

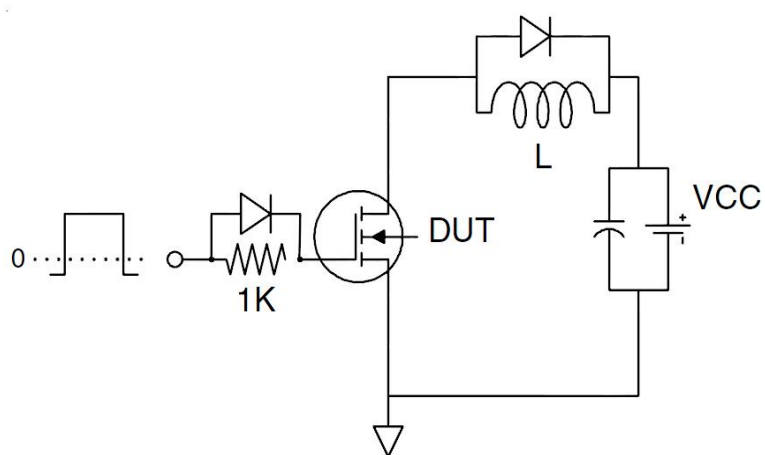
1. EAS condition : $T_J=25^{\circ}\text{C}, V_{DD}=75V, V_G=10V, L=0.5mH, R_g=25\Omega$
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_J(MAX)=175^{\circ}\text{C}$. The SOA curve provides a single pulse rating.

Test Circuit

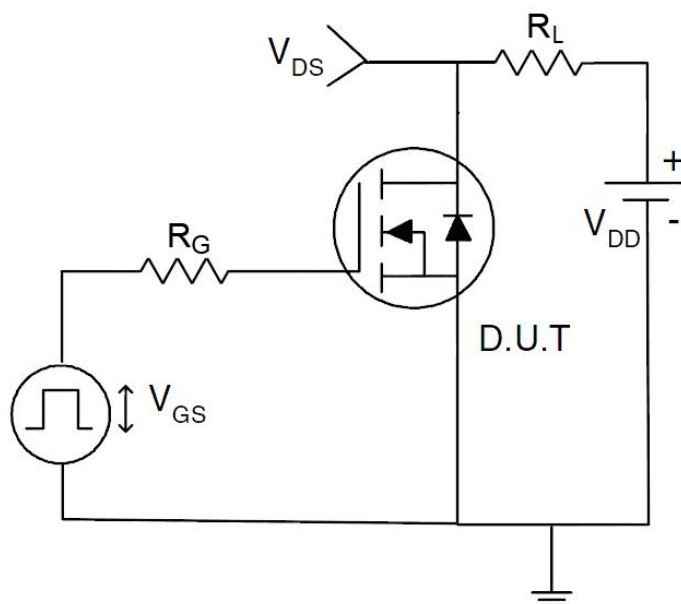
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics

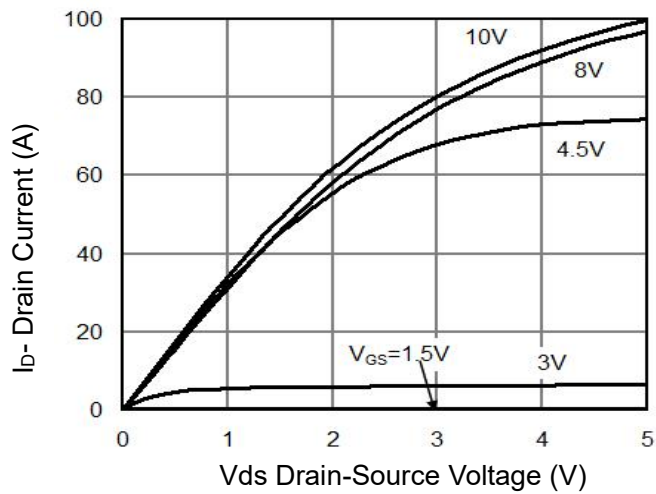


Figure 1 Output Characteristics

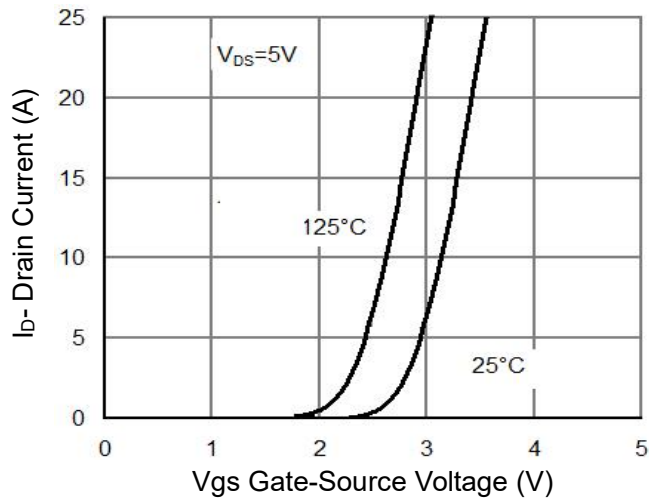


Figure 2 Transfer Characteristics

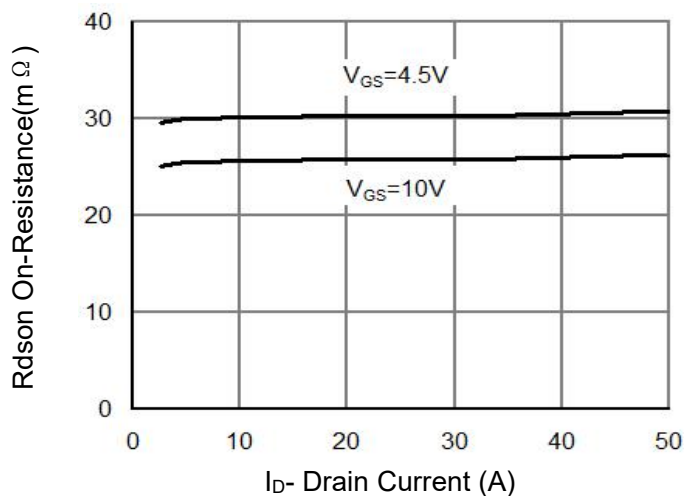


Figure 3 $R_{DS(on)}$ - Drain Current

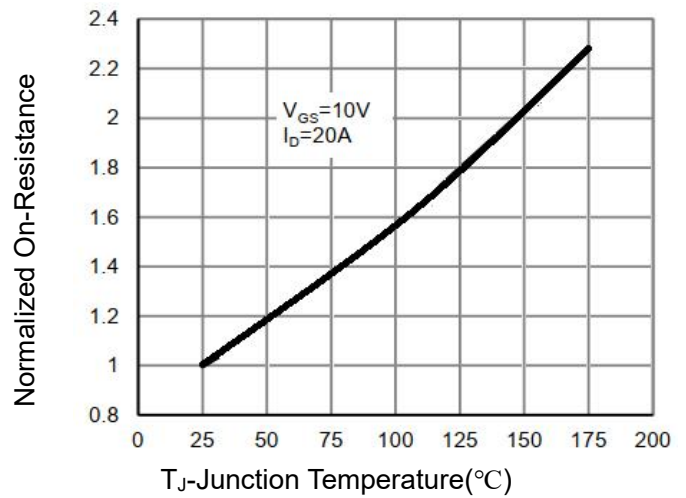


Figure 4 $R_{DS(on)}$ -Junction Temperature

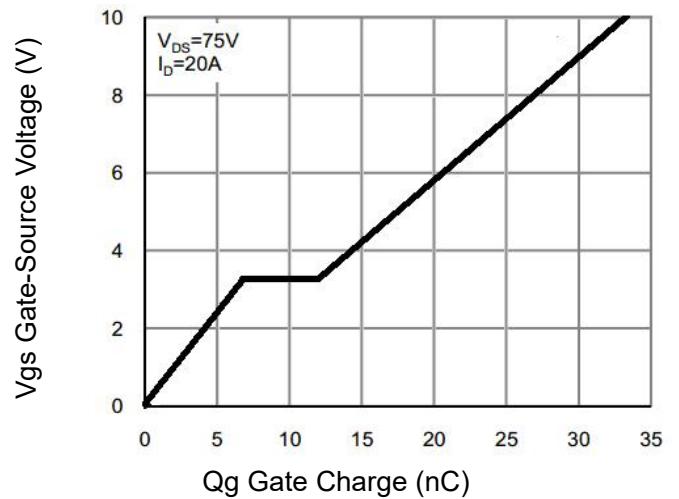


Figure 5 Gate Charge

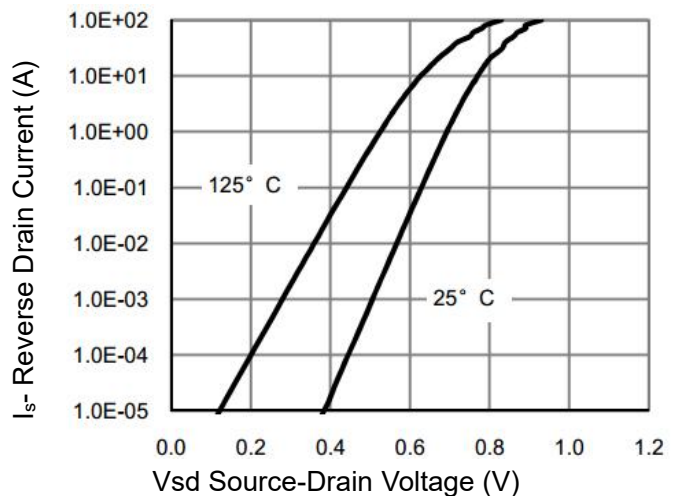


Figure 6 Source- Drain Diode Forward

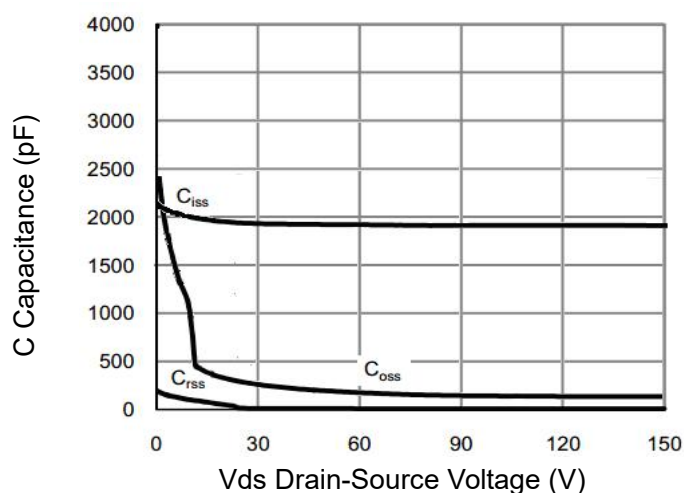


Figure 7 Capacitance vs Vds

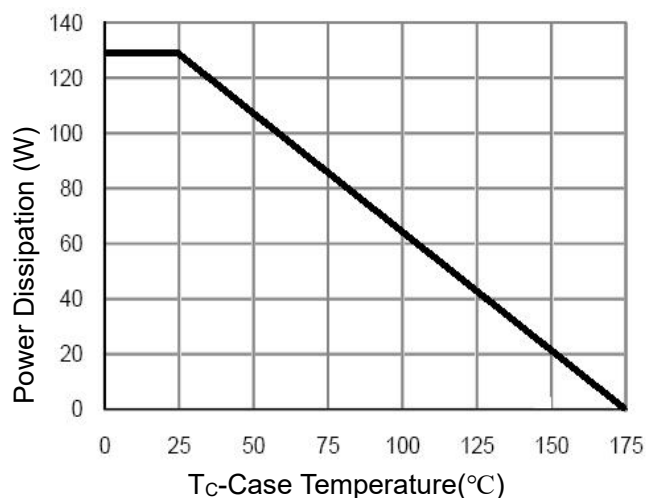


Figure 9 Power De-rating

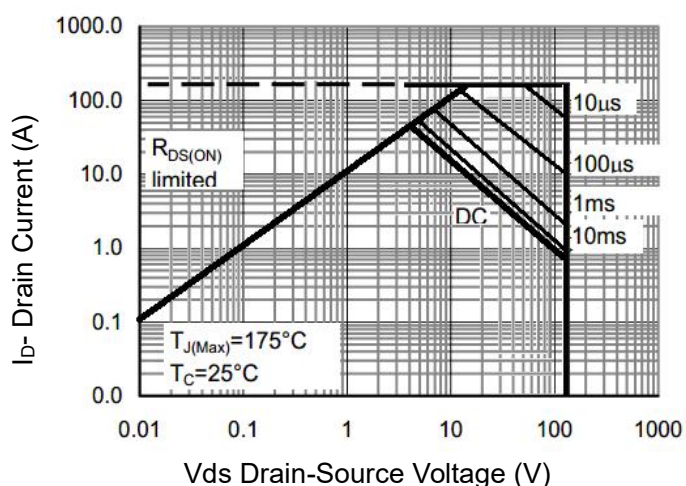


Figure 8 Safe Operation Area(Note3)

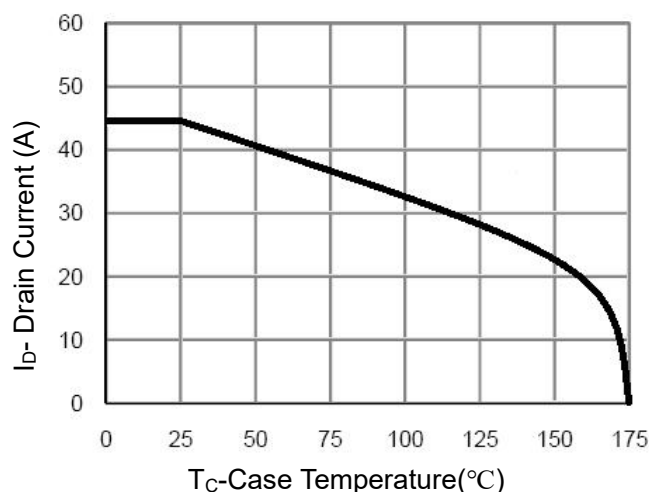


Figure 10 Current De-rating

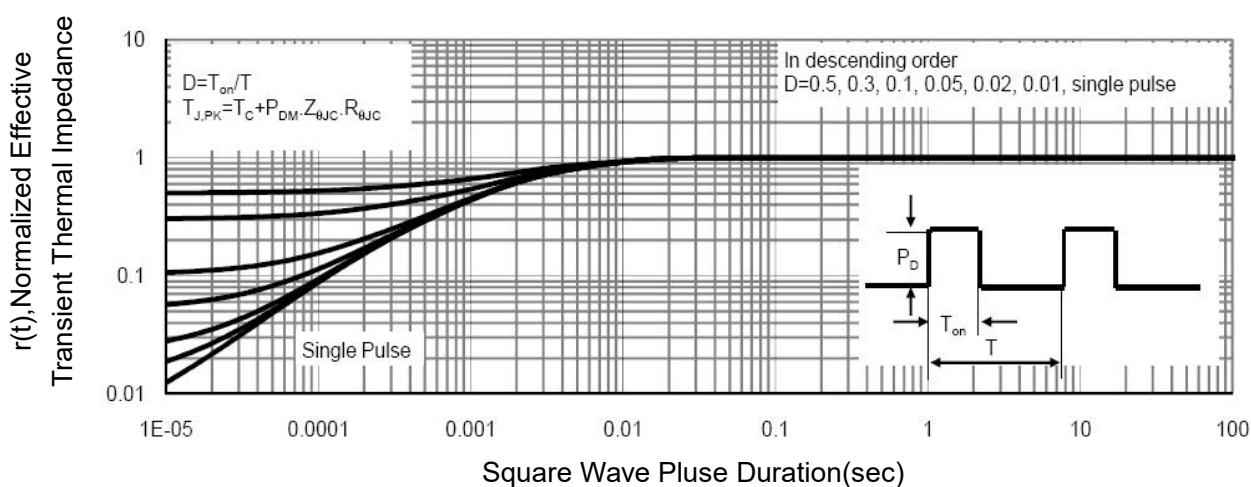
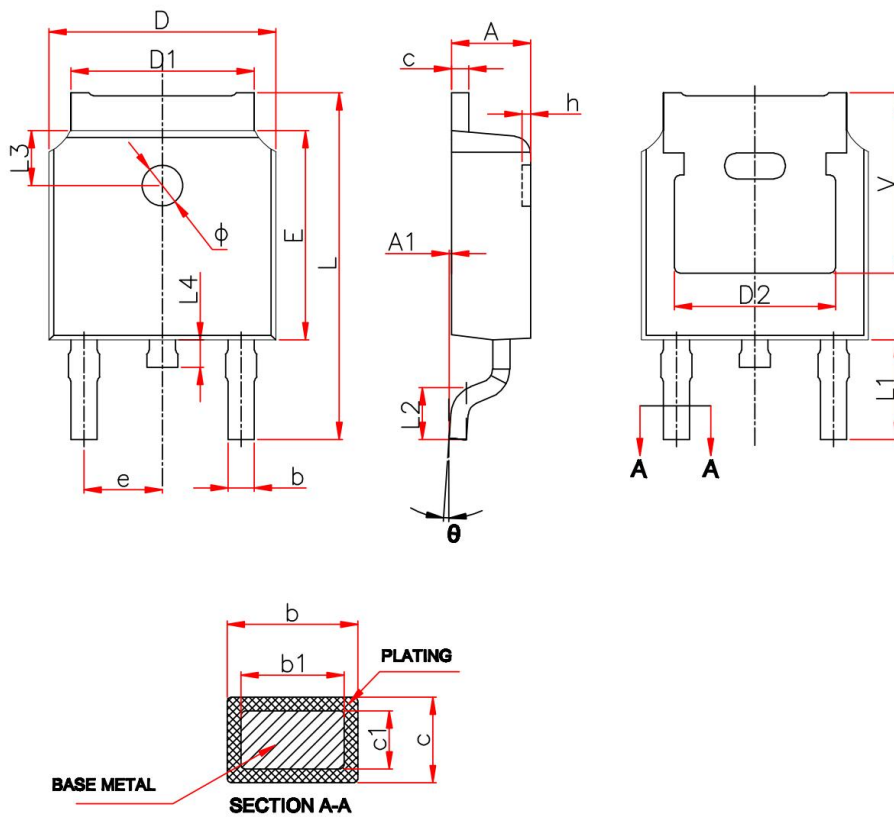


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252-2L Package Information



Symbol	Millimeters	
	Min.	Max.
A	2.20	2.40
A1	0.00	0.13
b	0.66	0.86
b1	0.73	0.79
c	0.46	0.58
c1	0.50	0.52
D	6.50	6.70
D1	5.10	5.46
D2	4.83 REF.	
E	6.00	6.20
e	2.19	2.39
L	9.80	10.40
L1	2.90 REF.	
L2	1.40	1.70
L3	1.60 REF.	
L4	0.60	1.00
ϕ	1.10	1.30
θ	0°	8°

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