

## NCE P-Channel **Super Trench** Power MOSFET

### Description

The NCEP15P30AK uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

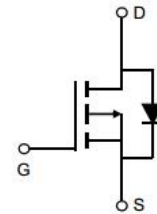
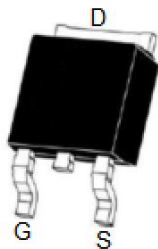
### General Features

- $V_{DS} = -150V, I_D = -29.5A$   
 $R_{DS(ON)} = 90m\Omega$  (typical) @  $V_{GS} = -10V$   
 $R_{DS(ON)} = 107m\Omega$  (typical) @  $V_{GS} = -4.5V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

**100% UIS TESTED!**

**100%  $\Delta V_{ds}$  TESTED!**

TO-252-2L



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP15P30AK	NCEP15P30AK	TO-252-2L	-	-	-

### Absolute Maximum Ratings ( $T_c = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-150	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	-29.5	A
Drain Current-Continuous( $T_c = 100^\circ\text{C}$ )	$I_D(100^\circ\text{C})$	-20.6	A
Pulsed Drain Current	$I_{DM}$	-118	A
Maximum Power Dissipation	$P_D$	235	W
Derating factor		1.57	W/ $^\circ\text{C}$
Single pulse avalanche energy <sup>(Note 1)</sup>	$E_{AS}$	207	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ\text{C}$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.64	$^\circ\text{C/W}$
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## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-150	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-150V, V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.2	-1.7	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A	-	90	105	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-15A	-	107	125	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-15A	-	30	-	S
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-75V, V <sub>GS</sub> =0V, F=1.0MHz	-	1650	-	PF
Output Capacitance	C <sub>oss</sub>		-	135	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	12	-	PF
Switching Characteristics (Note 2)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-50V, I <sub>D</sub> =-15A V <sub>GS</sub> =-10V, R <sub>G</sub> =1.6Ω	-	10	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	18	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	20	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-75V, I <sub>D</sub> =-15A, V <sub>GS</sub> =-10V	-	25	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	5.2	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3.1	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-15A	-		-1.2	V
Diode Forward Current	I <sub>S</sub>		-	-	-29.5	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = -15A	-	55	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs	-	101	-	nC

### Notes:

1. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=-50V, V<sub>G</sub>=-10V, L=0.5mH, R<sub>G</sub>=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T<sub>J</sub>(MAX)=175°C. The SOA curve provides a single pulse rating.

## Typical Electrical and Thermal Characteristics

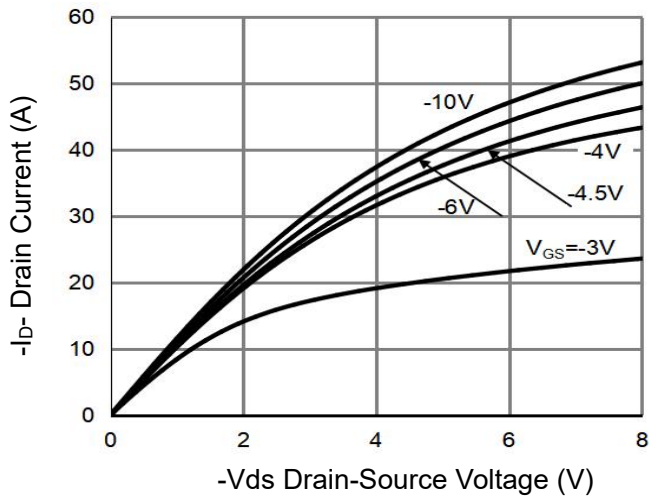


Figure 1 Output Characteristics

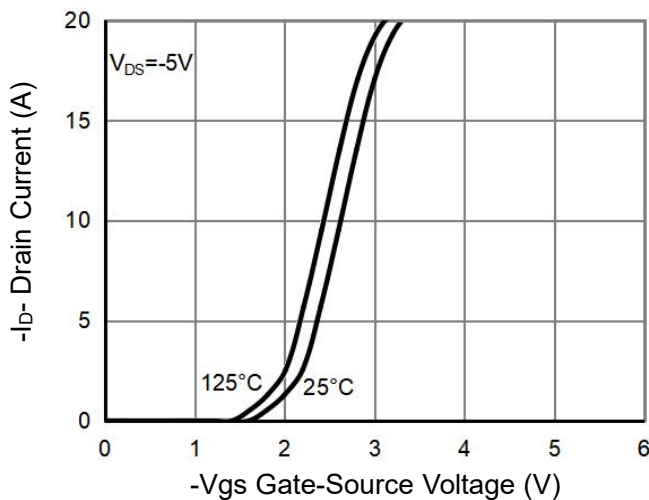


Figure 2 Transfer Characteristics

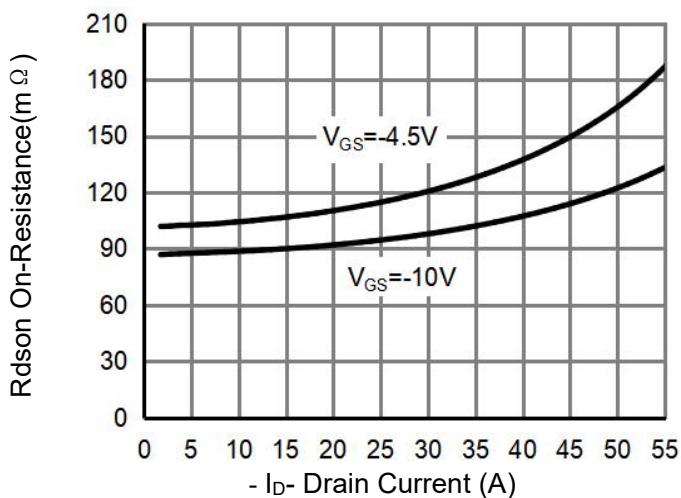


Figure 3 R\_DS(on) vs -I\_D Drain Current

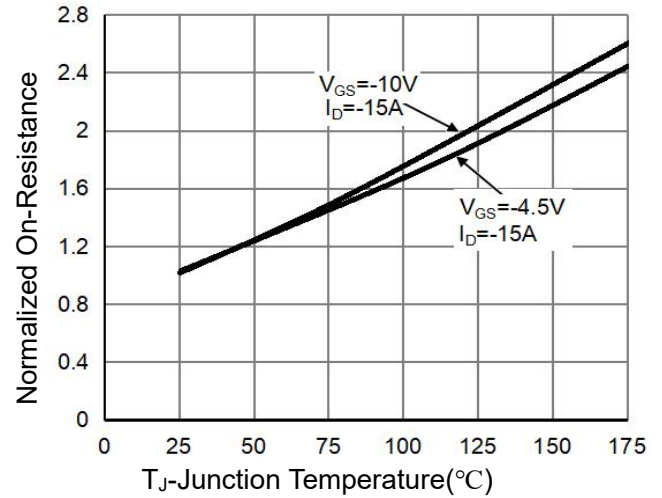


Figure 4 R\_DS(on) vs Junction Temperature

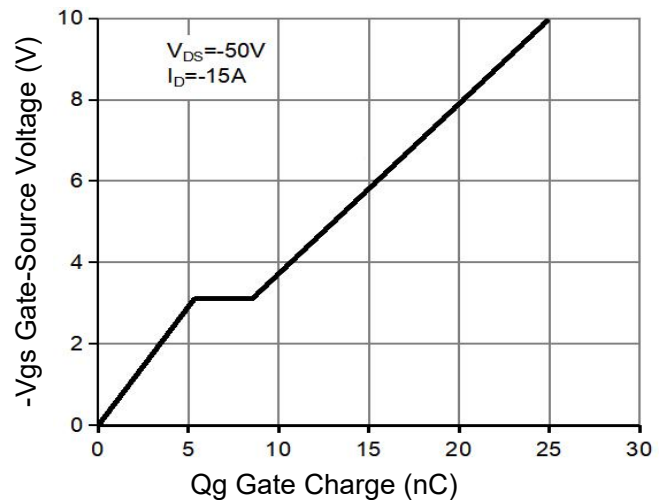


Figure 5 Gate Charge

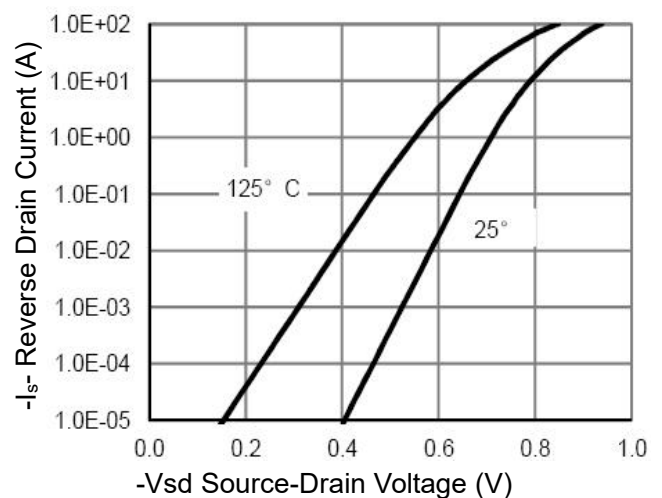


Figure 6 Source-Drain Diode Forward

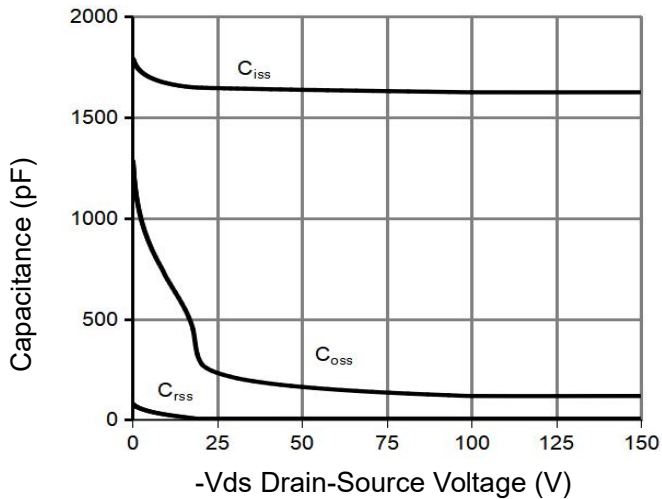


Figure 7 Capacitance vs Vds

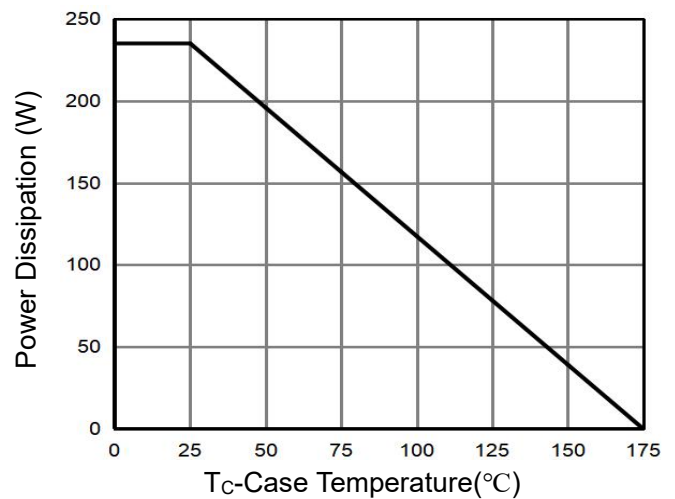


Figure 9 Power De-rating

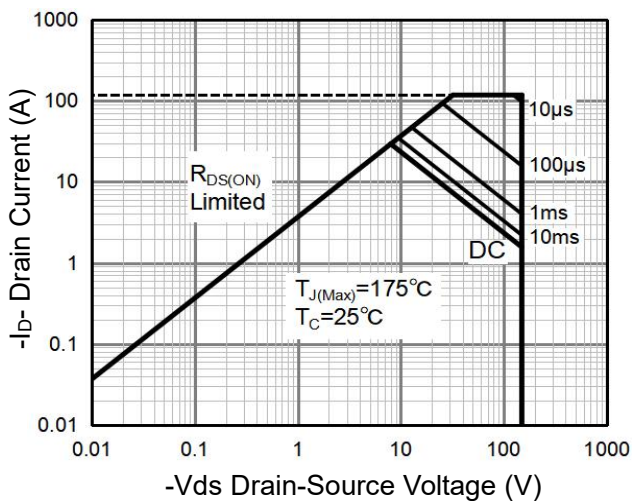


Figure 8 Safe Operation Area (Note 3)

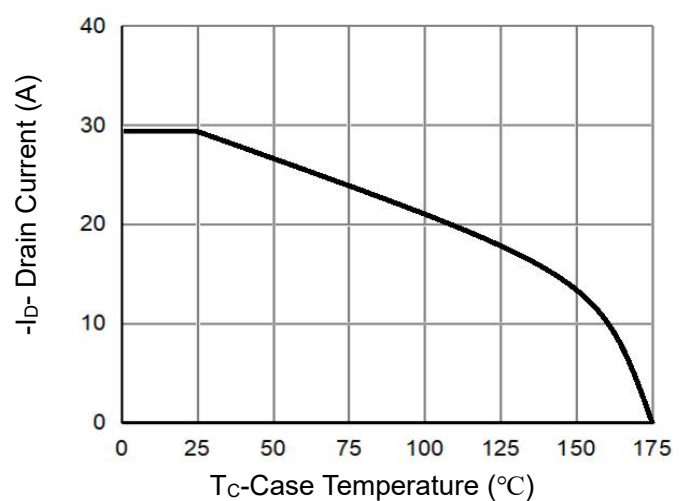


Figure 10 Current De-rating

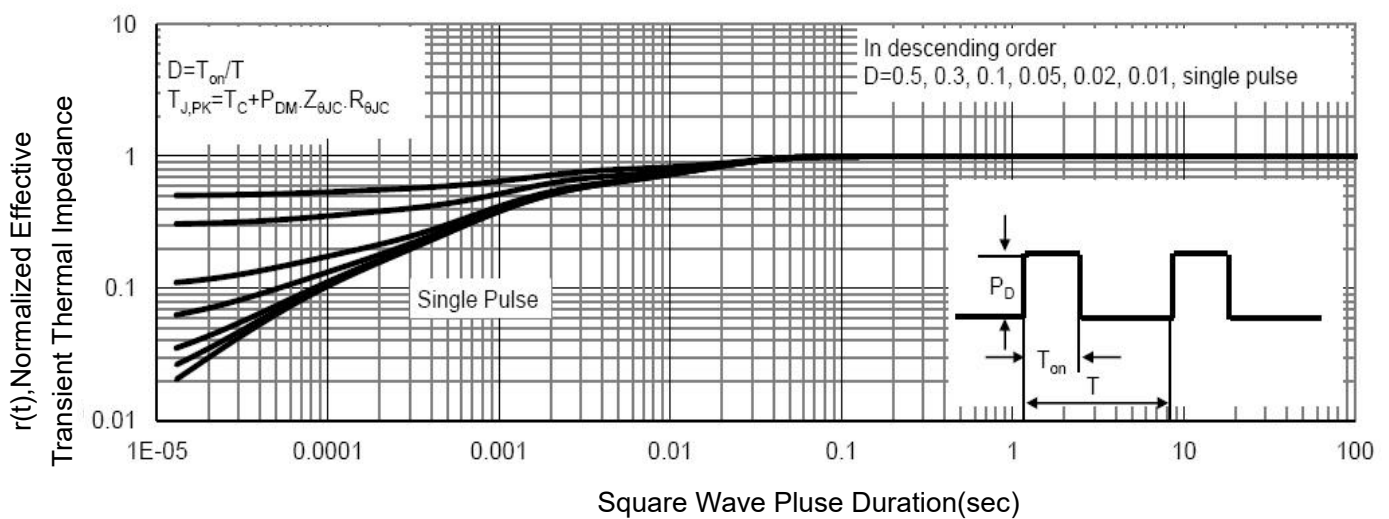
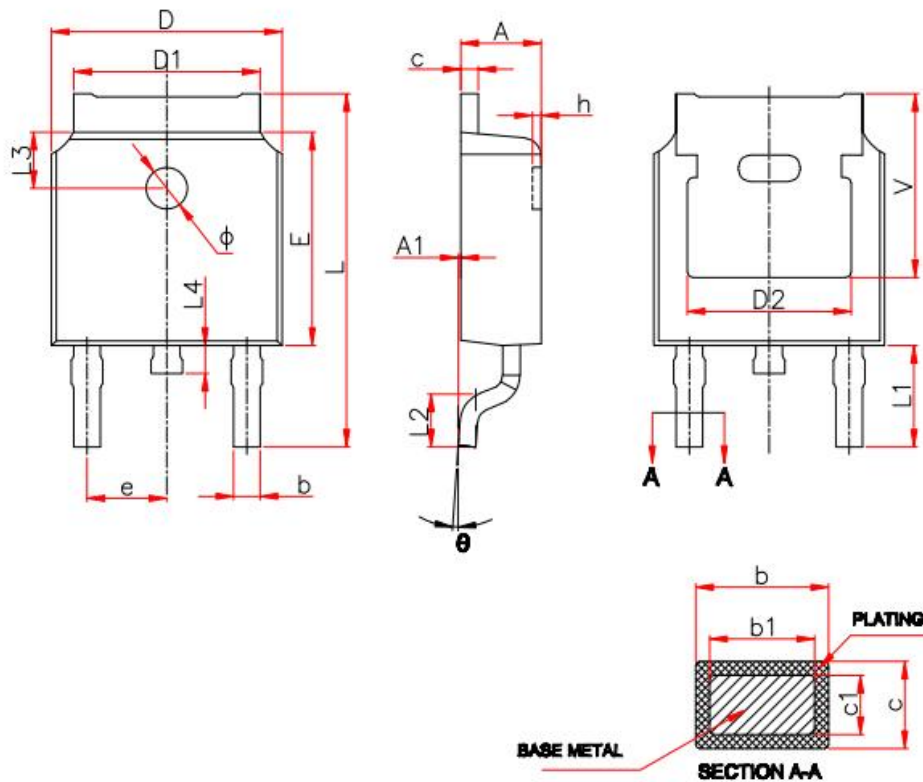


Figure 11 Normalized Maximum Transient Thermal Impedance

## TO-252-2L Package Information



Symbol	Millimeters	
	Min.	Max.
A	2.20	2.40
A1	0.00	0.13
b	0.66	0.86
b1	0.73	0.79
c	0.46	0.58
c1	0.50	0.52
D	6.50	6.70
D1	5.10	5.46
D2	4.83 REF.	
E	6.00	6.20
e	2.19	2.39
L	9.80	10.40
L1	2.90 REF.	
L2	1.40	1.70
L3	1.60 REF.	
L4	0.60	1.00
$\Phi$	1.10	1.30
$\theta$	0°	8°

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