

NCE N-Channel Super Trench Power MOSFET

Description

The NCEP3045BGU uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

V_{DS} =30V,I_D =45A

 $R_{DS(ON)}$ =4.4m Ω (typical) @ V_{GS} =10V

 $R_{DS(ON)}$ =5.3m Ω (typical) @ V_{GS} =4.5V

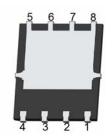
 $R_{DS(ON)}$ =7.4m Ω (typical) @ V_{GS} =2.5V

- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!
100% ΔVds TESTED!

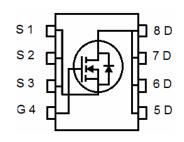
DFN 5X6





Top View

Bottom View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P3045BGU	NCEP3045BGU	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _G s	±10	V
Drain Current-Continuous	I _D	45	Α
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	31.8	Α
Pulsed Drain Current	I _{DM}	125	Α
Maximum Power Dissipation	P _D	28	W
Derating factor		0.22	W/℃
Single pulse avalanche energy (Note 5)	Eas	150	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2)	$R_{ heta JC}$	4.5	°C/W
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Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			•
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	$V_{GS}=\pm10V, V_{DS}=0V$	-	-	±100	nA
On Characteristics (Note 3)						•
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	0.5	0.7	1.0	V
		V _{GS} =10V, I _D =20A	-	4.4	5.0	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	-	5.3	6.3	mΩ
		V _{GS} =2.5V, I _D =20A	-	7.4	9.0	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A		30	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -15\/\/ -0\/	-	1125	-	PF
Output Capacitance	C _{oss}	V _{DS} =15V,V _{GS} =0V, F=1.0MHz	-	415	-	PF
Reverse Transfer Capacitance	C _{rss}	r=1.0lvlm2	-	29.7	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	6.5	-	nS
Turn-on Rise Time	t _r	V_{DD} =15 V , I_D =20 A	-	2.5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5 V , R_{G} =1.6 Ω	-	17	-	nS
Turn-Off Fall Time	t _f		-	2.5	-	nS
Total Gate Charge	Qg	\/ -45\/1 -204	-	13.8	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=15V, I_{D}=20A,$	-	2.7		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V	-	3.0		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	20	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = I_S$	-	11	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	19	-	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=20V,VG=10V,L=0.5mH,Rg=25 Ω



Typical Electrical and Thermal Characteristics

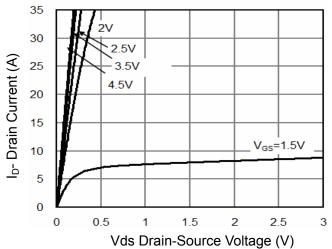


Figure 1 Output Characteristics

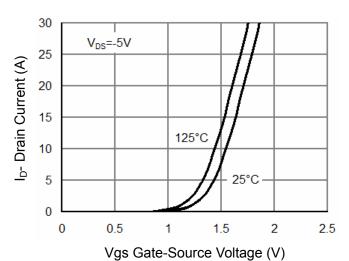


Figure 2 Transfer Characteristics

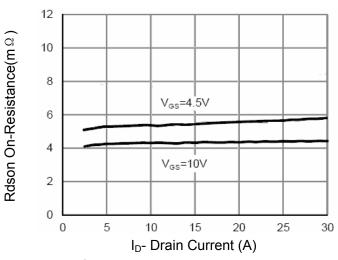


Figure 3 Rdson- Drain Current

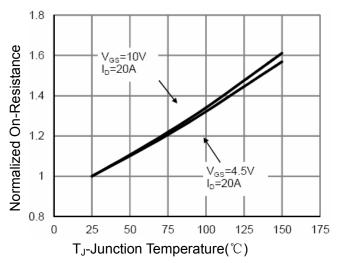


Figure 4 Rdson-Junction Temperature

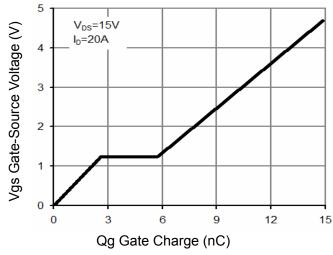


Figure 5 Gate Charge

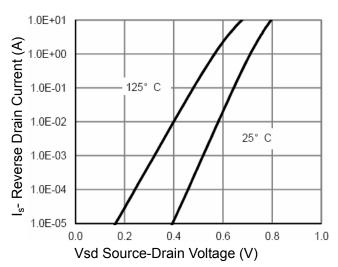


Figure 6 Source- Drain Diode Forward



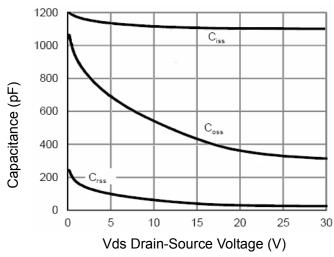


Figure 7 Capacitance vs Vds

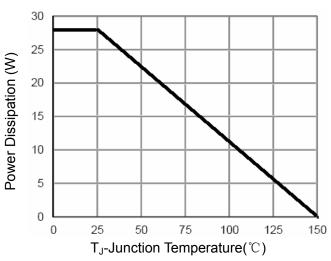


Figure 9 Power De-rating

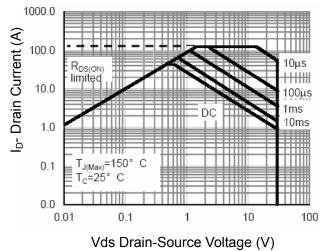


Figure 8 Safe Operation Area

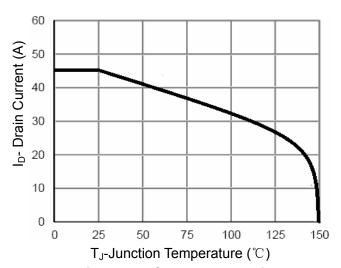


Figure 10 Current De-rating

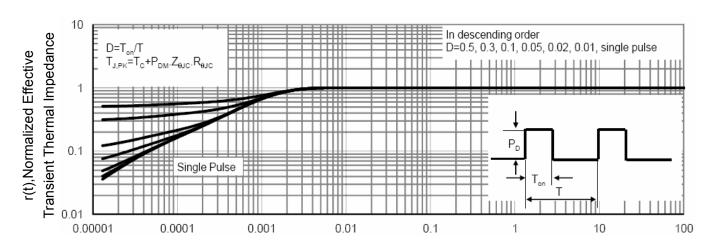
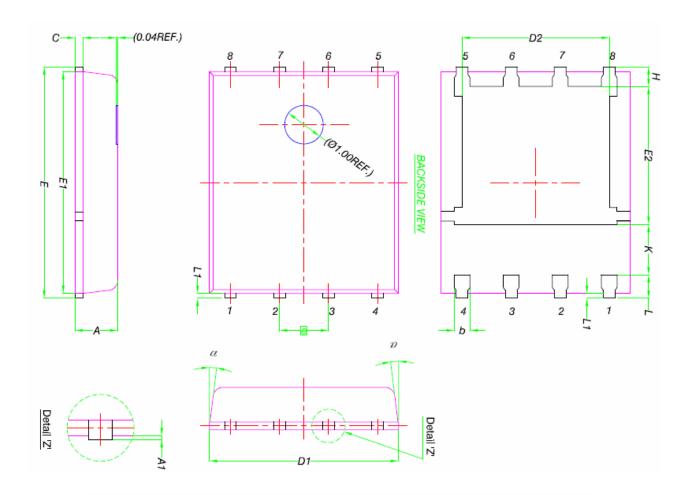


Figure 11 Normalized Maximum Transient Thermal Impedance

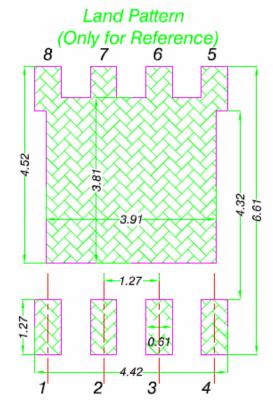
Square Wave Pluse Duration(sec)



DFN5X6-8L Package Information



DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	0.90	1.00	1.10		
A1	0	-	0.05		
b	0.33	0.41	0.51		
С	0.20	0.25	0.30		
D1	4.80	4.90	5.00		
D2	3.61	3.81	3.96		
Ε	5.90	6.00	6.10		
E1	5.70	5.75	5.80		
E2	3.38	3.58	3.78		
е	1.27 BSC				
Н	0.41	0.51	0.61		
K	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.06	0.13	0.20		
α	O°	-	12°		



Attention:



http://www.ncepower.com

NCEP3045BGU

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