

NCE N-Channel Super Trench Power MOSFET

Description

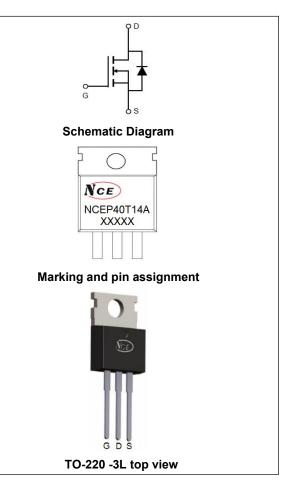
The NCEP40T14A uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

- V_{DS} =40V,I_D =140A
 R_{DS(ON)}=1.8mΩ (typical) @ V_{GS}=10V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance *R*_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested
- 100% ΔVds tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP40T14A	NCEP40T14A	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	40	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	140	A
Drain Current-Continuous	I _D (100℃)	109	A
Pulsed Drain Current	I _{DM}	560	A
Maximum Power Dissipation	PD	200	W
Derating factor		1.33	W/°C
Single pulse avalanche energy (Note 1)	E _{AS}	1036	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case	R _{eJC}	0.75	°C/W
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Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics	····					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics	i					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =10V, I _D =20A	-	1.8	2.4	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =40A	-	80	-	S
Dynamic Characteristics						
Input Capacitance	Clss	V_{DS} =20V, V_{GS} =0V,	-	4180	-	pF
Output Capacitance	Coss		-	1920	-	pF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	85	-	pF
Switching Characteristics (Note 2)	····		•			
Turn-on Delay Time	t _{d(on)}		-	40	-	nS
Turn-on Rise Time	tr	V _{DD} =20V,I _D =40A V _{GS} =10V,R _G =1.6Ω	-	35	-	nS
Turn-Off Delay Time	t _{d(off)}		-	70	-	nS
Turn-Off Fall Time	t _f		-	22	-	nS
Total Gate Charge	Qg	V _{DS} =20V,I _D =20A,	-	59.5	-	nC
Gate-Source Charge	Q _{gs}		-	20.5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	10	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current	ls		-	-	140	Α
Reverse Recovery Time	trr	T _J = 25°C, I _F =70A	-	50	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-	62	-	nC

Notes:

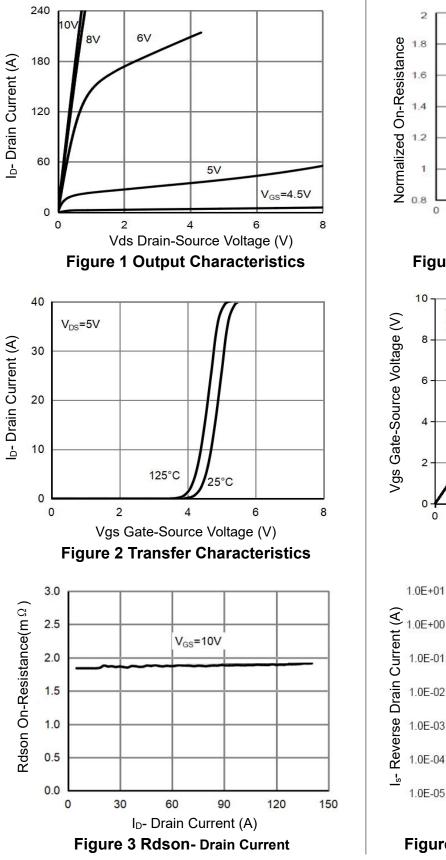
1. EAS condition : Tj=25 $^\circ \!\! \mathbb{C}$,V_DD=20V,V_G=10V,L=0.5mH,Rg=25 $\!\Omega$

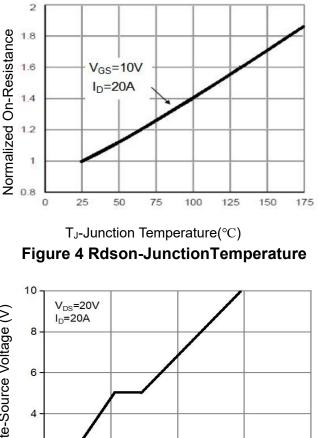
2. Guaranteed by design, not subject to production

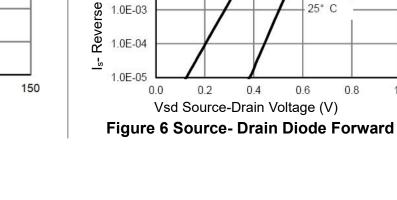
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=175°C. The SOA curve provides a single pulse rating.











2

0

0

20

125° C 40

Qg Gate Charge (nC)

Figure 5 Gate Charge

60

80

1.0

0.8



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NCEP40T14A

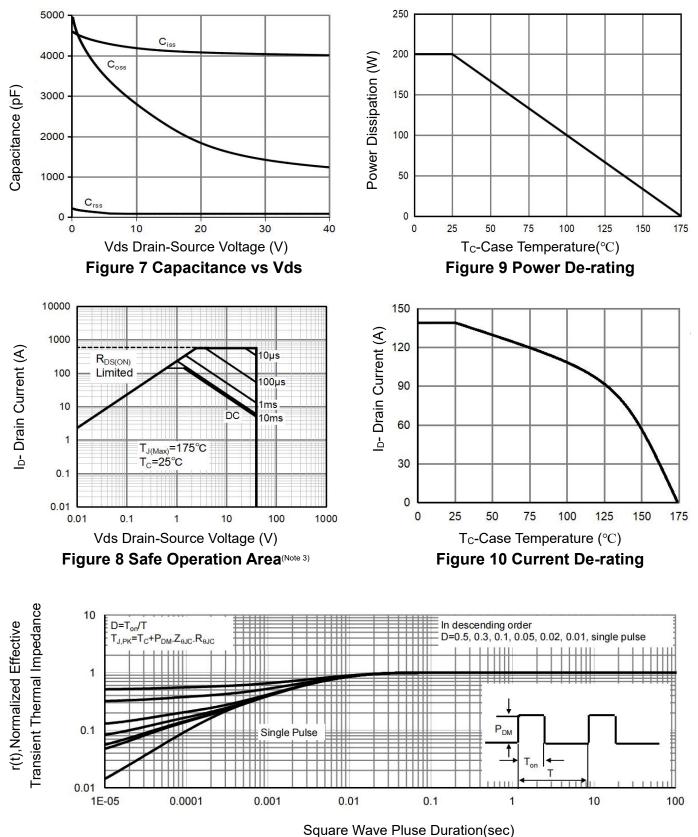
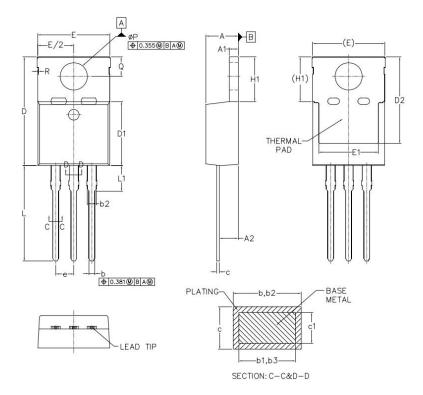


Figure 11 Normalized Maximum Transient Thermal Impedance



TO-220-3L Package Information



Ş	COMMON			
MB B	MM			
ZBOLN	MIN.	MAX.		
A	3.556	4.826		
A1	0.508	1.397		
A2	2.032	2.921		
b	0.381	1.016		
b1	0.381	0.965		
b2	1.143	1.778		
b3	1.143	1.727		
С	0.356	0.610		
c1	0.356	0.559		
D	14.224	16.510		
D1	8.382	9.017		
D2	12.042	12.878		
E	9.652	10.668		
E1	6.858	8.890		
е	2.540 BSC.			
H1	5.842	6.858		
L	12.700	14.732		
L1	3.560	4.060		
ØP	3.810 3.860			
Q	2.540 3.048			
R	0.127 BSC			



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