

## **NCE N-Channel Super Trench Power MOSFET**

### **Description**

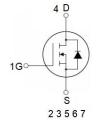
The NCEP40T35AVD uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{\text{DS(ON)}}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

## **Application**

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### **General Features**

- $V_{DS}$  =40V, $I_D$  =480A (Silicon Limited)  $R_{DS(ON)}$ =0.68m $\Omega$  (typical) @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested
- 100% ∆Vds tested







TO-263-7L top view

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP40T35AVD	NCEP40T35AVD	TO-263-7L	-	-	-

## Absolute Maximum Ratings (T<sub>c</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	40	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous(Silicon Limited)(Note 1)	I <sub>D</sub>	480	А
Drain Current-Continuous(Silicon Limited)(Note 1)	I <sub>D</sub> (100℃)	343	А
Drain Current-Continuous(Package Limited)	I <sub>D</sub>	350	А
Pulsed Drain Current	I <sub>DM</sub>	1400	Α
Maximum Power Dissipation	P <sub>D</sub>	380	W
Derating factor		2.53	W/°C
Single pulse avalanche energy (Note 2)	E <sub>AS</sub>	3000	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	$^{\circ}$ C

### **Thermal Characteristic**

Thermal Resistance,Junction-to-Case	R <sub>θJC</sub>	0.39	°C/W
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# NCEP40T35AVD

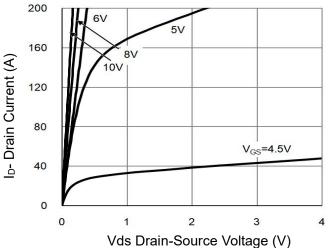
## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	43	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	0.68	0.88	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	100	-	-	S
Dynamic Characteristics						
Input Capacitance	C <sub>lss</sub>	.,	-	12850	-	pF
Output Capacitance	Coss	$V_{DS}=20V, V_{GS}=0V,$	-	4641	-	pF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	205	-	pF
Switching Characteristics (Note 1)	<u>,                                      </u>					
Turn-on Delay Time	t <sub>d(on)</sub>		-	26	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =20 $V$ , $I_D$ =20 $A$	-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =1.6 $\Omega$	-	110	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	17	-	nS
Total Gate Charge	Qg	\/ 00\/ L 00 A	-	156.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =20V,I <sub>D</sub> =20A,	-	59	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	23.5	-	nC
Drain-Source Diode Characteristics			<u> </u>			
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current	Is		-	-	480	Α
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = I <sub>S</sub>	-	66	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-	240	-	nC

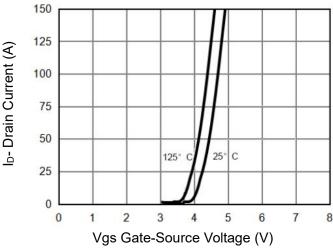
### Notes:

- 1. Defined by design.Not Subject to production test
- 2. EAS condition : Tj=25  $^{\circ}\text{C}$  ,V\_DD=20V,V\_G=10V,L=0.5mH,Rg=25 $\Omega$
- 3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=175° C. The SOA curve provides a single pulse rating.

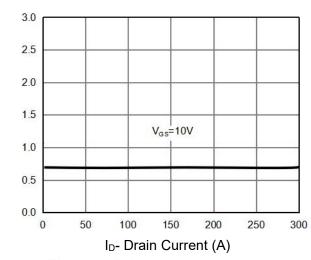




**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 



Rdson On-Resistance(m 🛭 )

Figure 3 Rdson- Drain Current

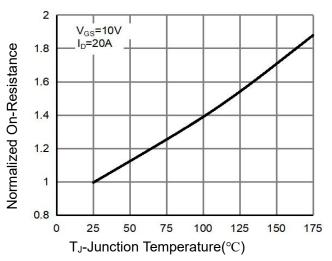


Figure 4 Rdson-Junction Temperature

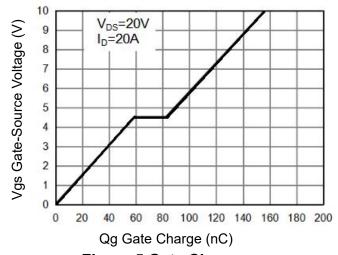


Figure 5 Gate Charge

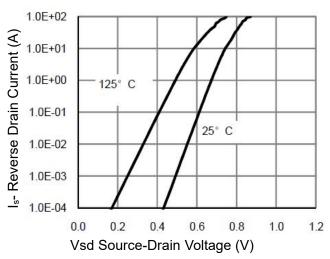
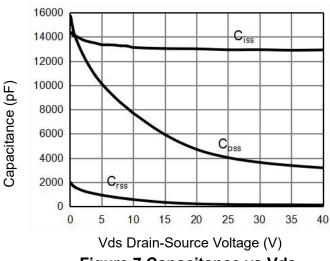


Figure 6 Source- Drain Diode Forward



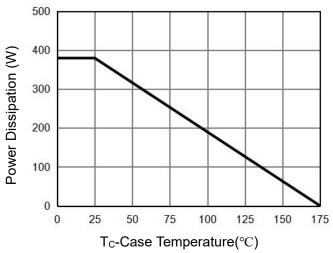
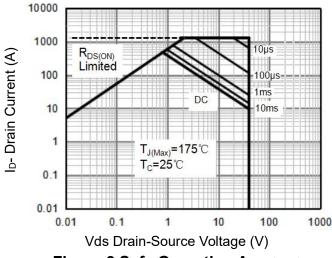


Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



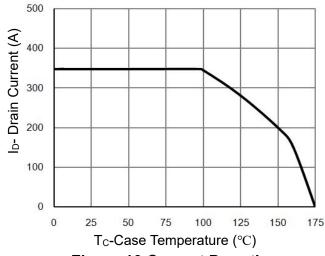


Figure 8 Safe Operation Area(Note 3)

Figure 10 Current De-rating

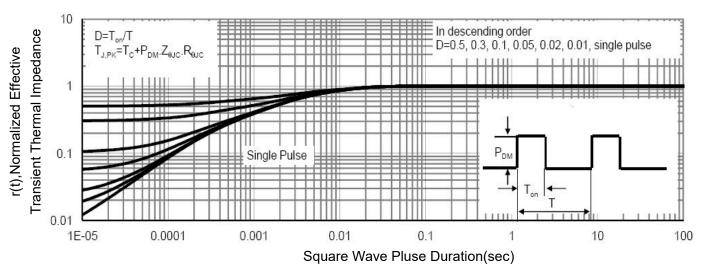
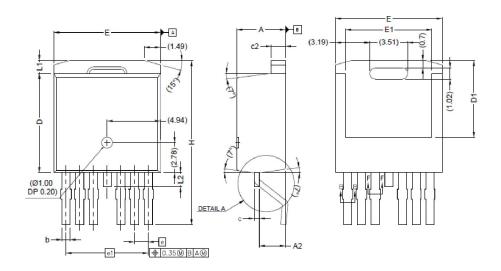
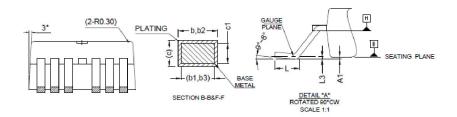


Figure 11 Normalized Maximum Transient Thermal Impedance

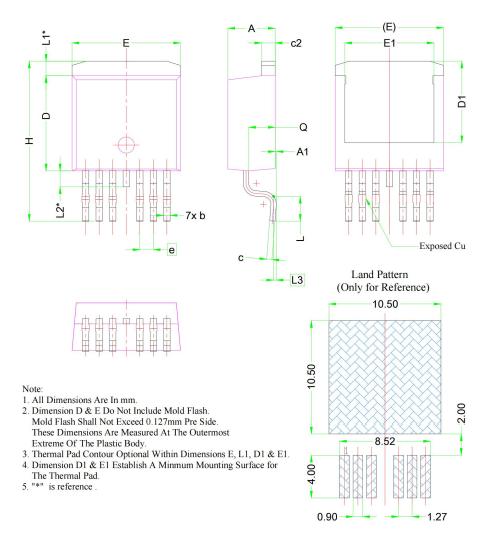
# TO-263-7L (B) Package Information





SYMBOL	MIN	MAX	
Α	4.30	4.70	
A1	-	0.25	
A2	2.20	2.60	
b	0.65	0.85	
b1	0.65	0.80	
b2	0.80	1.00	
b3	0.80	0.95	
C	0.45	0.60	
c1	0.45	0.55	
c2	1.25	1.40	
D	9.00	9.40	
D1	6.86	7.42	
E	9.68	10.08	
E1	7.70	8.30	
е	1.27	7 BSC	
e1	7.62	2 BSC	
L	1.78	2.79	
L1	-	1.60	
L2	-	1.78	
L3	0.25BSC		
Н	14.61	15.88	

# TO-263-7L (G) Package Information



SYMBOL	DIMENSIONS				
STIMBUL	MIN.	NOM.	MAX.		
Α	4.24	4.44	4.64		
A1	0.00	0.10	0.25		
b	0.50	0.60	0.70		
С	0.40	0.50	0.60		
c2	1.15	1.27	1.40		
D	8.82	8.92	9.02		
D1	6.86	7.65			
E	9.96	10.16	10.36		
E1	8.20	8.35	8.50		
е		1.27 BSC			
Н	14.61	15.00	15.88		
L	1.78	2.32	2.79		
L1	1.36 REF.				
L2	1.50 REF.				
L3	0.25 BSC				
Q	2.30	2.48	2.70		

# NCEP40T35AVD

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