

## NCE N-Channel Super Trench Power MOSFET

### Description

The NCEP6080G uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(on)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

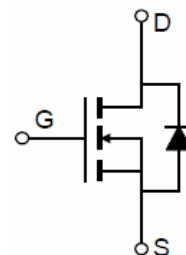
### General Features

- $V_{DS} = 60V, I_D = 80A$   
 $R_{DS(on)} < 4.3m\Omega @ V_{GS} = 10V$  (Typ: 3.8m $\Omega$ )
- Excellent gate charge x  $R_{DS(on)}$  product
- Very low on-resistance  $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

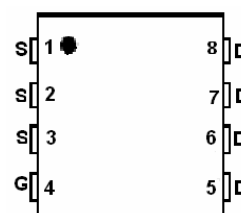
### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

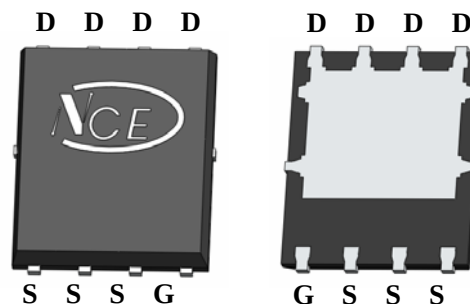
**100% UIS TESTED!**  
**100%  $\Delta V_{ds}$  TESTED!**



Schematic diagram



Marking and pin assignment



Top View

Bottom View

### Package Marking and Ordering Information

| Device Marking | Device    | Device Package | Reel Size | Tape width | Quantity |
|----------------|-----------|----------------|-----------|------------|----------|
| NCEP6080G      | NCEP6080G | DFN5X6-8L      | -         | -          | -        |

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

| Parameter                                        | Symbol              | Limit      | Unit          |
|--------------------------------------------------|---------------------|------------|---------------|
| Drain-Source Voltage                             | $V_{DS}$            | 60         | V             |
| Gate-Source Voltage                              | $V_{GS}$            | $\pm 20$   | V             |
| Drain Current-Continuous (Silicon Limited)       | $I_D$               | 80         | A             |
| Drain Current-Continuous ( $T_C = 100^\circ C$ ) | $I_D (100^\circ C)$ | 58         | A             |
| Pulsed Drain Current                             | $I_{DM}$            | 320        | A             |
| Maximum Power Dissipation                        | $P_D$               | 85         | W             |
| Derating factor                                  |                     | 0.68       | W/ $^\circ C$ |
| Single pulse avalanche energy (Note 5)           | $E_{AS}$            | 400        | mJ            |
| Operating Junction and Storage Temperature Range | $T_J, T_{STG}$      | -55 To 150 | $^\circ C$    |

## Thermal Characteristic

|                                                          |                 |      |                      |
|----------------------------------------------------------|-----------------|------|----------------------|
| Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup> | $R_{\theta JC}$ | 1.47 | $^{\circ}\text{C/W}$ |
|----------------------------------------------------------|-----------------|------|----------------------|

## Electrical Characteristics ( $T_C=25^{\circ}\text{C}$ unless otherwise noted)

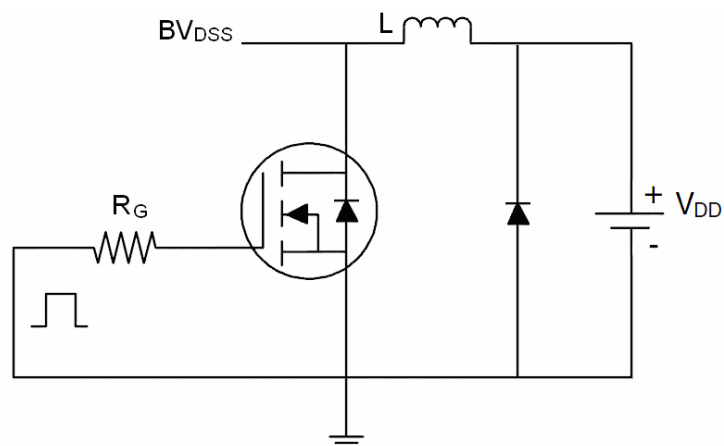
| Parameter                          | Symbol       | Condition                                                      | Min | Typ  | Max       | Unit       |
|------------------------------------|--------------|----------------------------------------------------------------|-----|------|-----------|------------|
| Off Characteristics                |              |                                                                |     |      |           |            |
| Drain-Source Breakdown Voltage     | $BV_{DSS}$   | $V_{GS}=0V, I_D=250\mu A$                                      | 60  |      | -         | V          |
| Zero Gate Voltage Drain Current    | $I_{DSS}$    | $V_{DS}=60V, V_{GS}=0V$                                        | -   | -    | 1         | $\mu A$    |
| Gate-Body Leakage Current          | $I_{GSS}$    | $V_{GS}=\pm 20V, V_{DS}=0V$                                    | -   | -    | $\pm 100$ | nA         |
| On Characteristics (Note 3)        |              |                                                                |     |      |           |            |
| Gate Threshold Voltage             | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$                                  | 2.0 | 3.0  | 4.0       | V          |
| Drain-Source On-State Resistance   | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=40A$                                          | -   | 3.8  | 4.3       | m $\Omega$ |
| Forward Transconductance           | $g_{FS}$     | $V_{DS}=5V, I_D=40A$                                           | 40  | -    | -         | S          |
| Dynamic Characteristics (Note4)    |              |                                                                |     |      |           |            |
| Input Capacitance                  | $C_{iss}$    | $V_{DS}=30V, V_{GS}=0V,$<br>$F=1.0MHz$                         | -   | 3400 | -         | PF         |
| Output Capacitance                 | $C_{oss}$    |                                                                | -   | 650  | -         | PF         |
| Reverse Transfer Capacitance       | $C_{rss}$    |                                                                | -   | 20   | -         | PF         |
| Switching Characteristics (Note 4) |              |                                                                |     |      |           |            |
| Turn-on Delay Time                 | $t_{d(on)}$  | $V_{DD}=30V, I_D=40A$<br>$V_{GS}=10V, R_G=4.7\Omega$           | -   | 11   | -         | nS         |
| Turn-on Rise Time                  | $t_r$        |                                                                | -   | 5    | -         | nS         |
| Turn-Off Delay Time                | $t_{d(off)}$ |                                                                | -   | 56   | -         | nS         |
| Turn-Off Fall Time                 | $t_f$        |                                                                | -   | 12   | -         | nS         |
| Total Gate Charge                  | $Q_g$        | $V_{DS}=30V, I_D=40A,$<br>$V_{GS}=10V$                         | -   | 51   |           | nC         |
| Gate-Source Charge                 | $Q_{gs}$     |                                                                | -   | 12   |           | nC         |
| Gate-Drain Charge                  | $Q_{gd}$     |                                                                | -   | 7.3  |           | nC         |
| Drain-Source Diode Characteristics |              |                                                                |     |      |           |            |
| Diode Forward Voltage (Note 3)     | $V_{SD}$     | $V_{GS}=0V, I_S=80A$                                           | -   |      | 1.2       | V          |
| Diode Forward Current (Note 2)     | $I_S$        |                                                                | -   | -    | 80        | A          |
| Reverse Recovery Time              | $t_{rr}$     | $T_J = 25^{\circ}C, I_F = I_S$<br>$di/dt = 100A/\mu s$ (Note3) | -   | 47   |           | nS         |
| Reverse Recovery Charge            | $Q_{rr}$     |                                                                | -   | 59   |           | nC         |

## Notes:

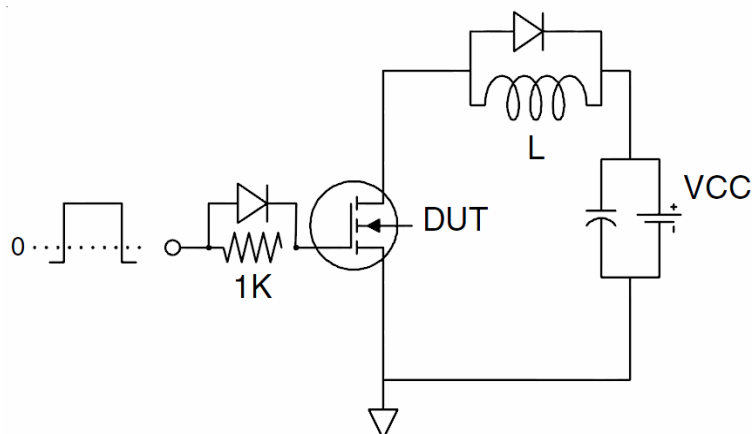
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_J=25^{\circ}\text{C}, V_{DD}=30V, V_G=10V, L=0.5mH, R_g=25\Omega$

## Test Circuit

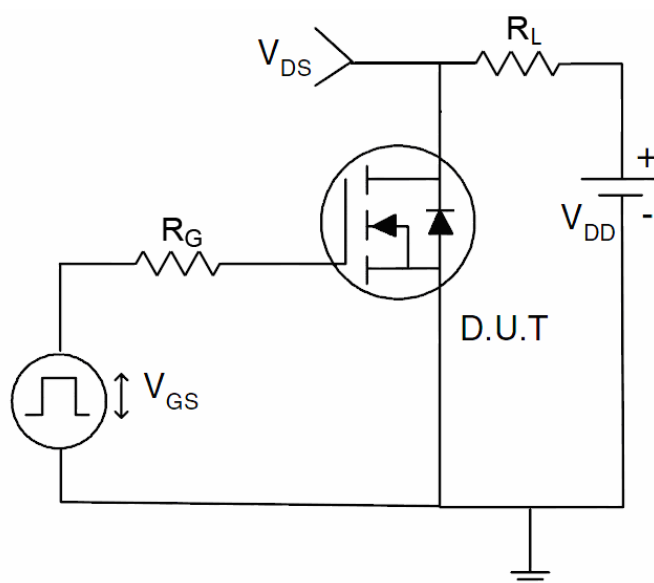
### 1) $E_{AS}$ test Circuit



### 2) Gate charge test Circuit



### 3) Switch Time Test Circuit



## Typical Electrical and Thermal Characteristics

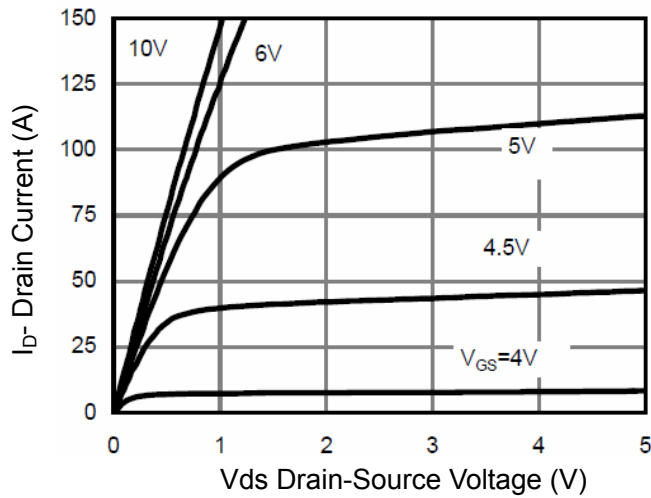


Figure 1 Output Characteristics

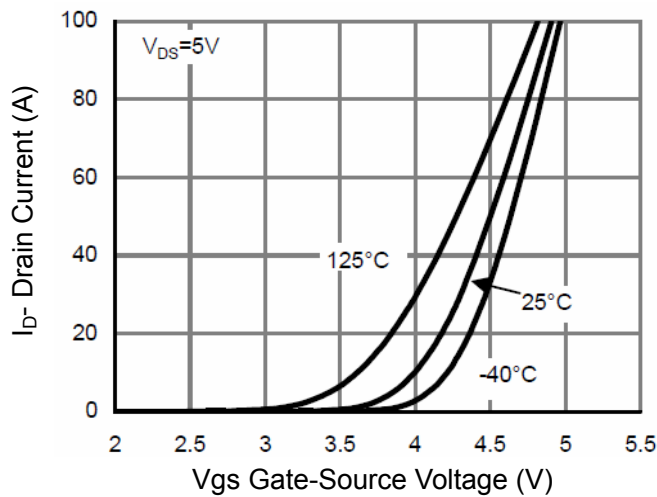


Figure 2 Transfer Characteristics

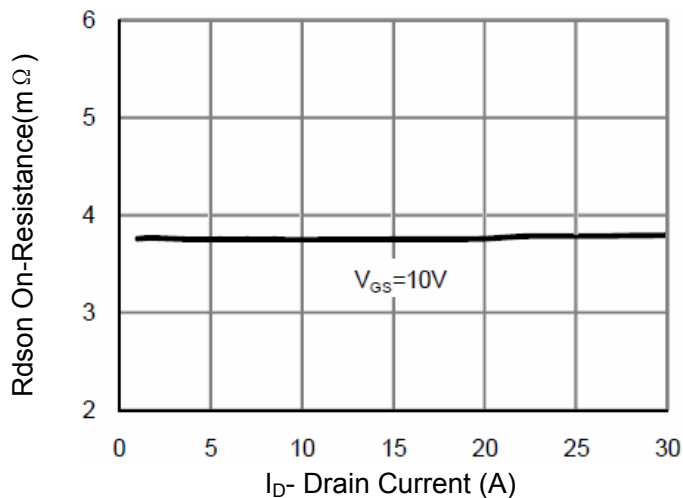


Figure 3 Rdson- Drain Current

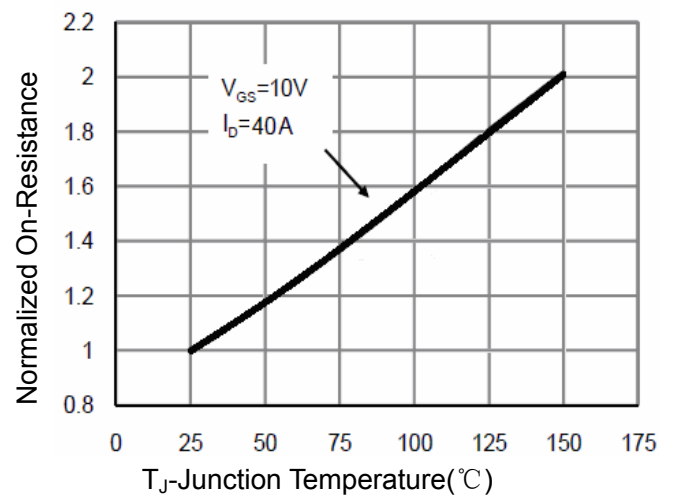


Figure 4 Rdson-Junction Temperature

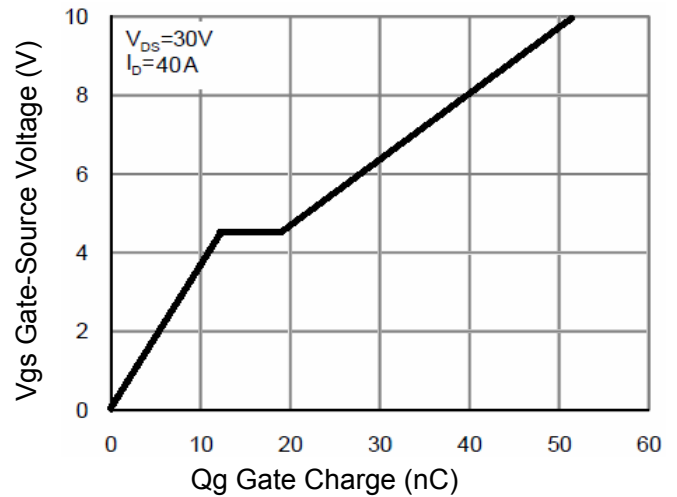


Figure 5 Gate Charge

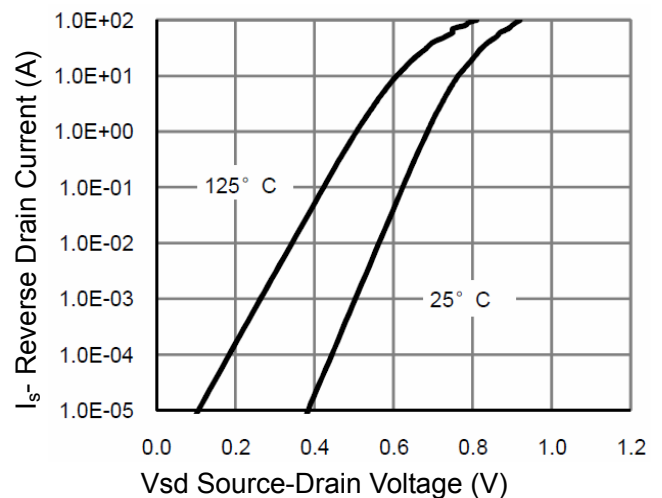


Figure 6 Source- Drain Diode Forward

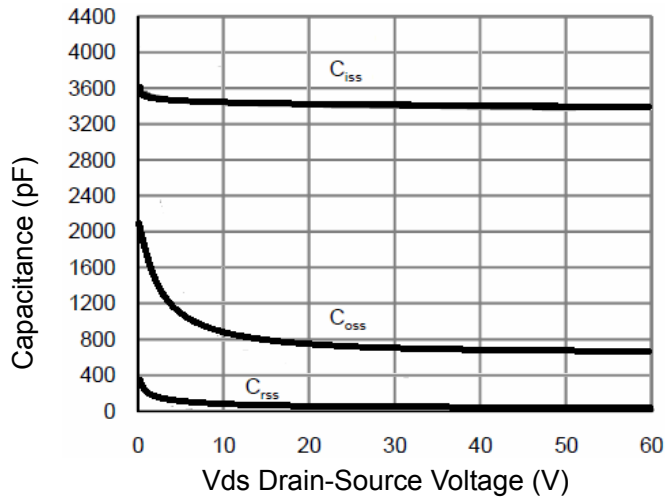


Figure 7 Capacitance vs Vds

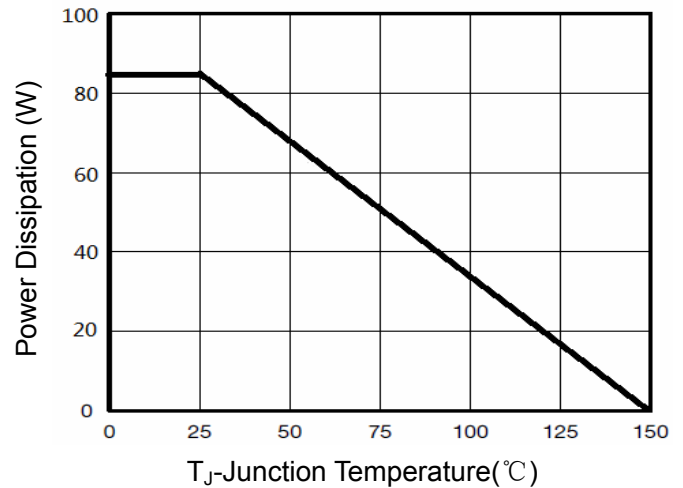


Figure 9 Power De-rating

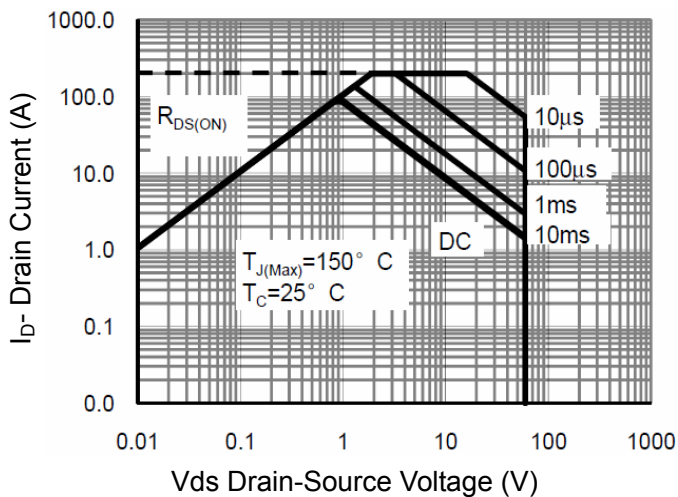


Figure 8 Safe Operation Area

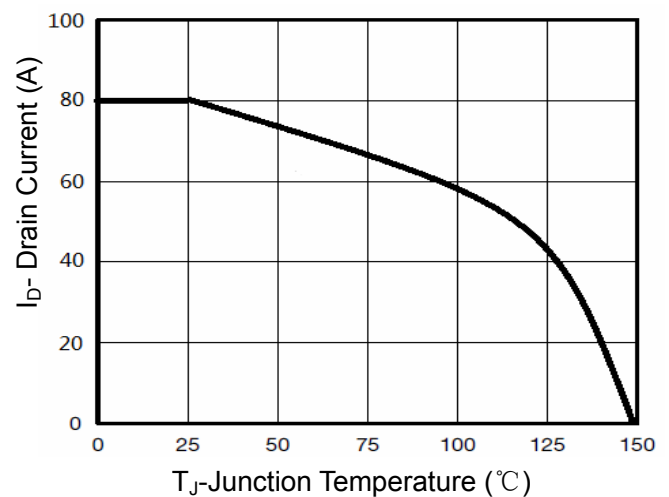


Figure 10 Current De-rating

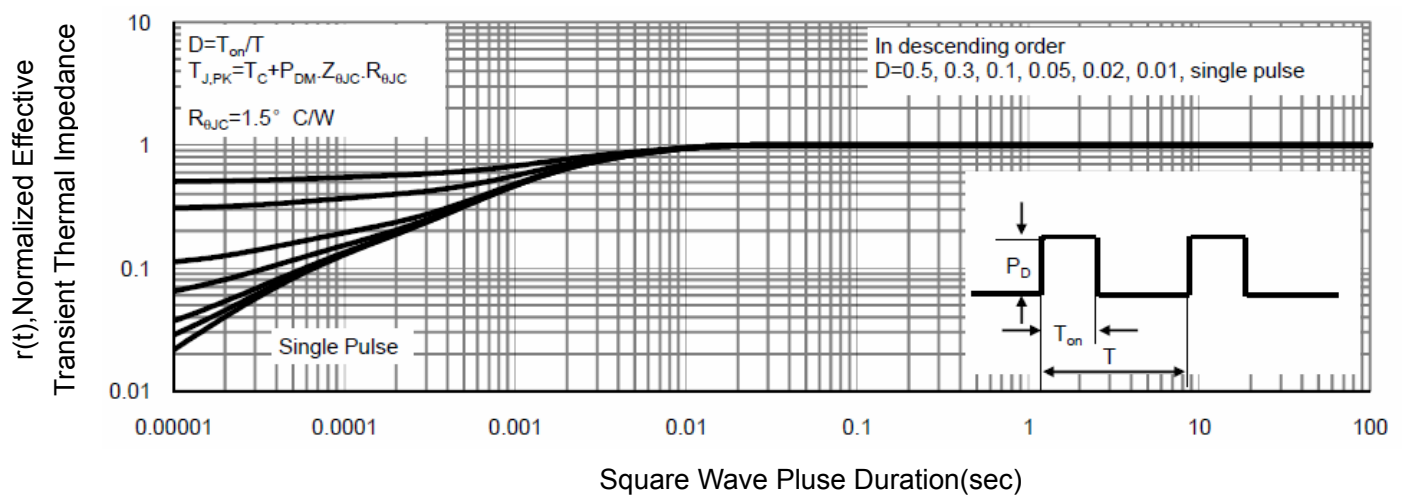
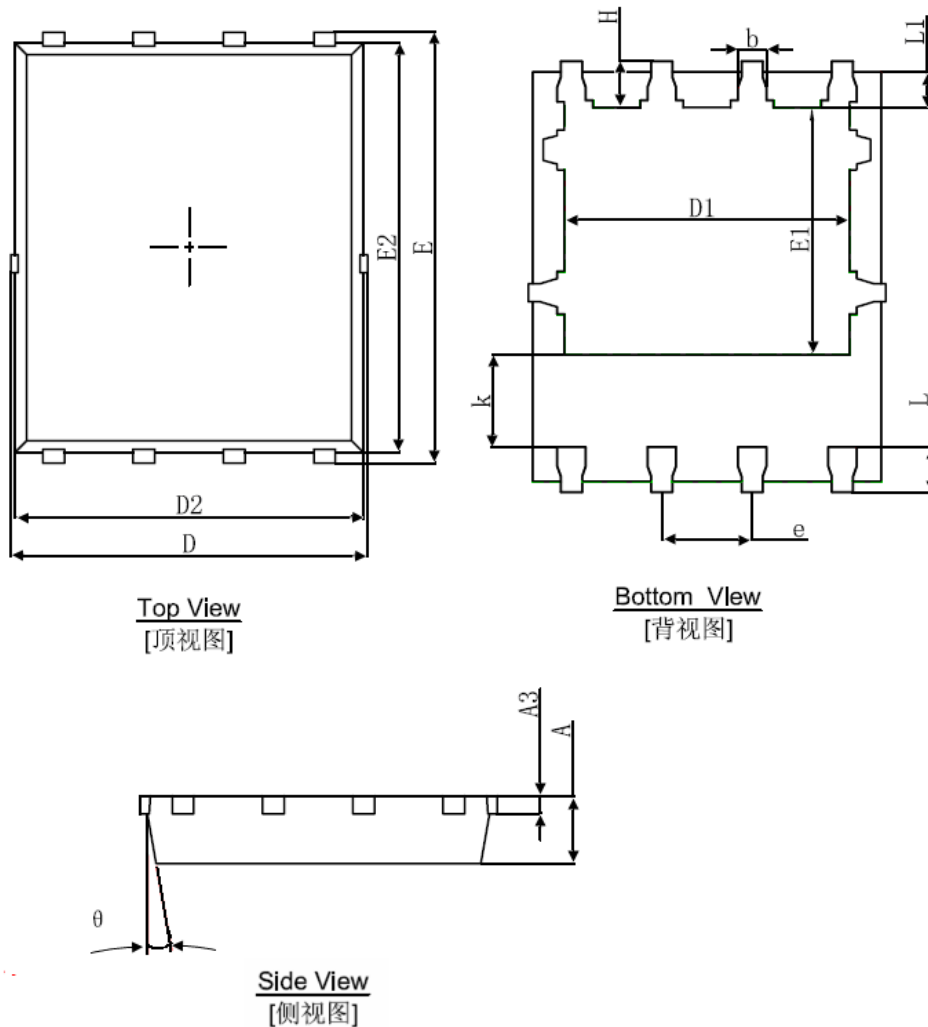


Figure 11 Normalized Maximum Transient Thermal Impedance

## DFN5X6-8L Package Information



| Symbol | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|---------------------------|-------|----------------------|-------|
|        | Min.                      | Max.  | Min.                 | Max.  |
| A      | 0.900                     | 1.000 | 0.035                | 0.039 |
| A3     | 0.254REF.                 |       | 0.010REF.            |       |
| D      | 4.944                     | 5.096 | 0.195                | 0.201 |
| E      | 5.974                     | 6.126 | 0.235                | 0.241 |
| D1     | 3.910                     | 4.110 | 0.154                | 0.162 |
| E1     | 3.375                     | 3.575 | 0.133                | 0.141 |
| D2     | 4.824                     | 4.976 | 0.190                | 0.196 |
| E2     | 5.674                     | 5.826 | 0.223                | 0.229 |
| k      | 1.190                     | 1.390 | 0.047                | 0.055 |
| b      | 0.350                     | 0.450 | 0.014                | 0.018 |
| e      | 1.270TYP.                 |       | 0.050TYP.            |       |
| L      | 0.559                     | 0.711 | 0.022                | 0.028 |
| L1     | 0.424                     | 0.576 | 0.017                | 0.023 |
| H      | 0.574                     | 0.726 | 0.023                | 0.029 |
| θ      | 8°                        | 12°   | 8°                   | 12°   |

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