Innovative Service Around the Globe YAGEO

DATA SHEET

THICK FILM LEAD FREE CHIP RESISTORS

SR_P series

0.5%, 1%, 5%, 10%, 20%

sizes 0201/0402/0603/0805/1206

RoHS compliant & Halogen free

YAGEO

Product specification – January 20, 2022 V.1

SCOPE

This specification describes SR0201 to SR1206 chip resistors made by thick film process.

APPLICATIONS

- Total lead free without RoHS exemption
- Telecommunications
- Power supplies

FEATURES

- Superior to RC series in pulse withstanding voltage and surge withstanding voltage.
- MSL class: MSL 1
- Halogen free epoxy
- Reduce environmentally hazardous waste
- High component and equipment reliability

ORDERING INFORMATION - GLOBAL PART NUMBER

Part number is identified by the series name, size, tolerance, packaging type, temperature coefficient, taping reel and resistance value.

GLOBAL PART NUMBER

SR	XXXX	X	X	X	XX	XXXX	P
	(1)	(2)	(3)	(4)	(5)	(6)	(7)

(1) SIZE

0201/ 0402/ 0603/ 0805/ 1206

(2) TOLERANCE

D = ±0.5%
F = ±1%
J = ± 5%
K=±10%
M=±20%

(3) PACKAGING TYPE

R = Paper taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

– = Based on spec.

(5) TAPING REEL & POWER

07 = 7 inch dia. Reel
7W = 7 inch dia. Reel & 2 x standard power
7T = 7 inch dia. Reel & 3 x standard power
47 = 7 inch dia. Reel & 4 x standard power

(6) RESISTANCE VALUE

1 Ω ≤ R ≤ 1MΩ

There are 2~4 digits indicated the resistance value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g.1K2, not 1K20.

Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter P is lead free (without RoHS exemption).

Resistance rule of global part number

Resistance coding rule	Example
XRXX (1 to 9.76 Ω)	1R = 1 Ω 1R5 = 1.5 Ω 9R76 = 9.76 Ω
XXRX (10 to 97.6 Ω)	10R = 10 Ω 97R6 = 97.6 Ω
XXXR (100 to 976 Ω)	100R = 100 Ω
XKXX (1 to 9.76 KΩ)	1K = 1,000 Ω 9K76 = 9760 Ω
XXKX (10 to 97.6 KΩ)	10K = 10,000 Ω 97K6= 97,600 Ω
XXXK (100 KΩ)	100K = 100,000 Ω

ORDERING EXAMPLE

The ordering code for an SR0805 chip resistor, value 10 KΩ with ±5% tolerance, supplied in 7-inch tape reel is: SR0805JR-0710KP.

MARKING

SR0201/ 0402

Fig. 1

No Marking

SR0603

Fig. 2 Value = 24 Ω

1%, 0.5%,E24 exception values 10/11/13/15/20/75 of E24 series

Fig. 3 Value = 806 Ω

1%, 0.5%, E96 refer to EIA-96 marking method, including values 10/11/13/15/20/75 of E24 series

SR0805 /1206

Fig. 4 Value = 10 K Ω

Both E-24 and E-96 series: 4 digits, ±0.5% & ±1%

First three digits for significant figure and 4th digit for number of zeros

NOTE

For further marking information, please refer to data sheet "Chip resistors marking".

TAPING REEL & POWER

Table 1

TYPE	POWER, W (P70)			
	CODING			
	07	7W	7T	47
0201	1/20	1/10	-	1/5
0402	1/16	1/8	1/5	-
0603	1/10	1/5	1/4	-
0805	1/8	1/4	1/3	1/2
1206	1/4	1/2	3/4	-

CONSTRUCTION

The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive glaze. The resistive glaze is covered by a lead-free glass. The composition of the glaze is adjusted to give the approximately required resistance value. The whole element is covered by a protective overcoat. The top of overcoat is marked with the resistance value. Finally, the two external terminations (Ni/matte tin) are added, as shown in Fig.5.

OUTLINES

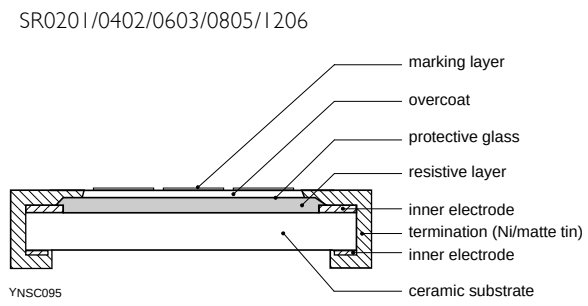


Fig. 5 Chip resistor outlines

DIMENSIONS

Table 2

TYPE	L (mm)	W (mm)	H (mm)	I_1 (mm)	I_2 (mm)
SR0201	0.60±0.03	0.30±0.03	0.23±0.03	0.12±0.05	0.15±0.05
SR0402	1.00±0.05	0.50±0.05	0.35±0.05	0.20±0.10	0.25±0.10
SR0603	1.60±0.10	0.80±0.10	0.45±0.10	0.25±0.15	0.25±0.15
SR0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.20
SR1206	3.10±0.10	1.60±0.10	0.55±0.10	0.45±0.20	0.45±0.20

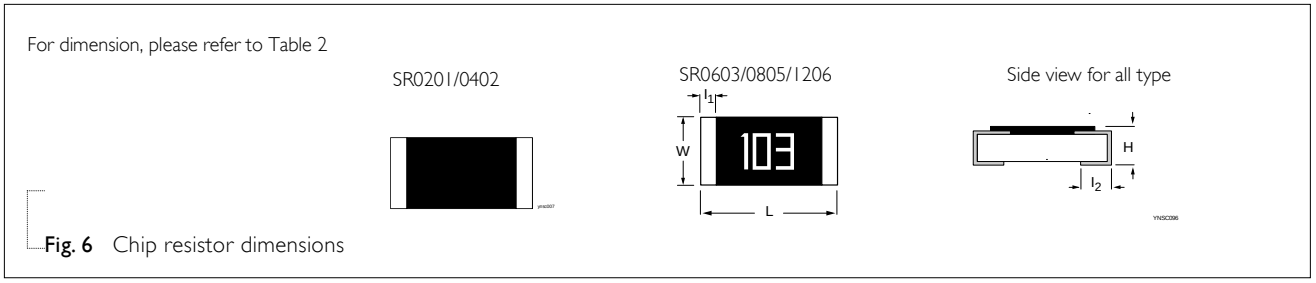


Fig. 6 Chip resistor dimensions

ELECTRICAL CHARACTERISTICS

Table 3

TYPE	POWER	RESISTANCE RANGE	CHARACTERISTICS				
			Operating Temperature Range	Max. Working Voltage	Max. Overload Voltage	Dielectric Withstanding Voltage	Temperature Coefficient of Resistance
SR0201	1/20W 1/10W 1/5W	E24 5%, 10%, 20% 1Ω ≤ R ≤ 1MΩ E24/E96 0.5%, 1% 1 Ω ≤ R ≤ 1M Ω	–55 °C to +155 °C	25V	50V	50V	1Ω ≤ R ≤ 10Ω -100~+350ppm°C 10Ω < R ≤ 1MΩ ± 200 ppm°C
SR0402	1/16W 1/8W 1/5W			75V	100V	100V	1Ω ≤ R ≤ 10Ω ± 200 ppm°C 10Ω < R ≤ 1MΩ ± 100 ppm°C
SR0603	1/10W 1/5W 1/4W			75V	150V	150V	
SR0805	1/8W 1/4W 1/3W 1/2W			150V	300V	300V	
SR1206	1/4W 1/2W 3/4W			200 V	400 V	500V	

FOOTPRINT AND SOLDERING PROFILES

Recommended footprint and soldering profiles, please refer to data sheet “Chip resistors mounting”.

PACKING STYLE AND PACKAGING QUANTITY

Table 4 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	SR0201/0402	SR0603/0805/1206
Paper taping reel (R)	7" (178 mm)	10,000	5,000

NOTE

1. For paper/embossed tape and reel specification/dimensions, please refer to data sheet “Chip resistors packing”.

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

Range: –55 °C to +155 °C

POWER RATING

Each type rated power at 70 °C:
SR0201: 1/20W, 1/10W, 1/5W
SR0402: 1/16W, 1/8W, 1/5W
SR0603: 1/10W, 1/5W, 1/4W
SR0805: 1/8W, 1/4W, 1/3W, 1/2W
SR1206: 1/4W, 1/2W, 3/4W

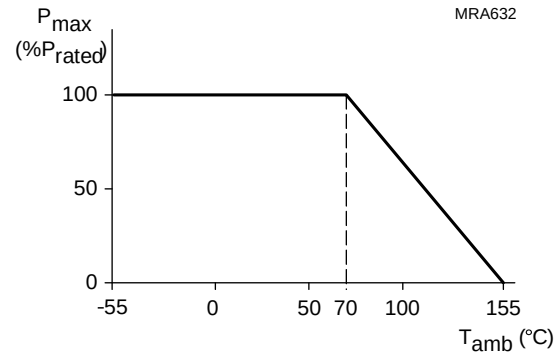


Fig. 7 Maximum dissipation (P_{max}) in percentage of rated power as a function of the operating ambient temperature (T_{amb})

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{(P \times R)}$$

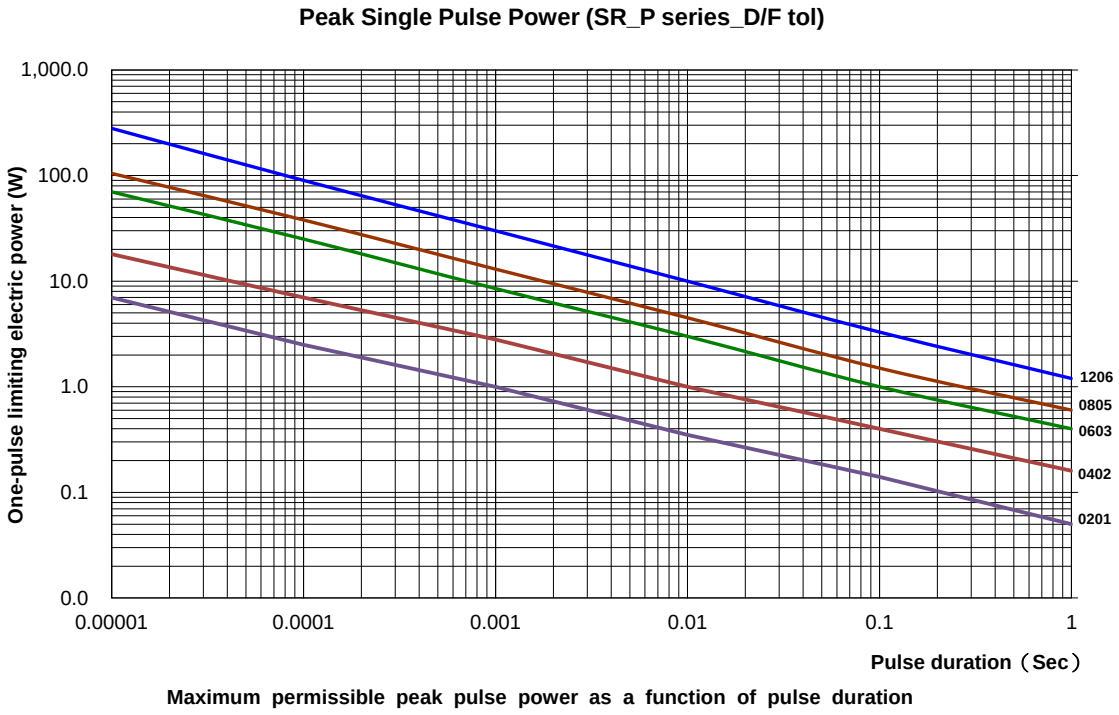
Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

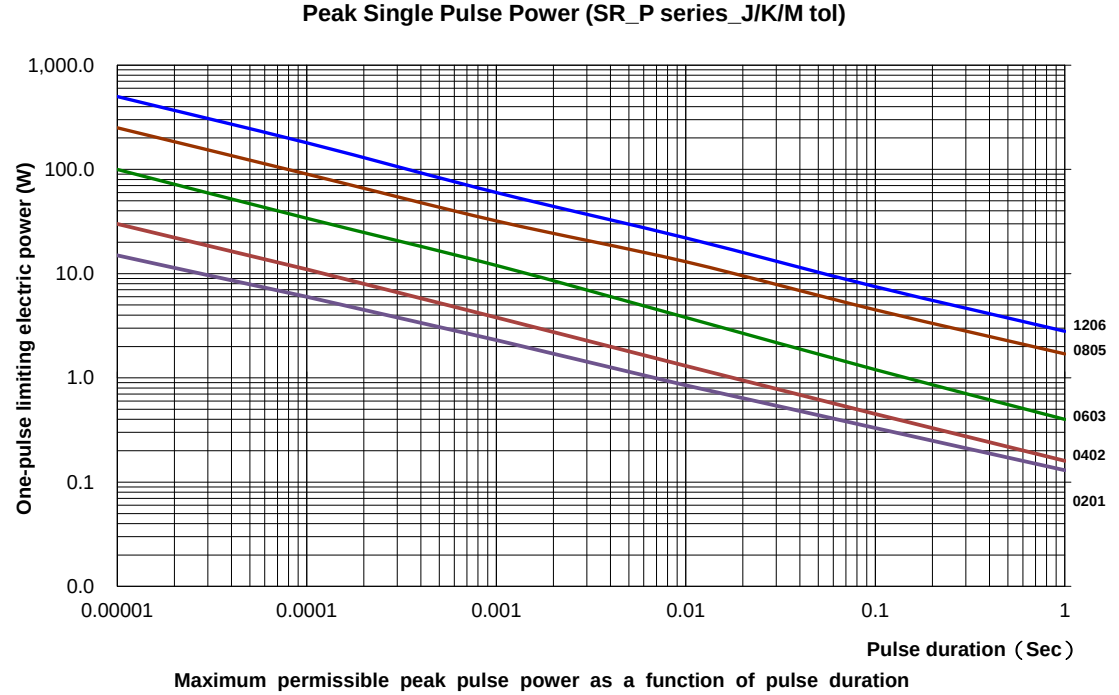
R = Resistance value (Ω)

PULSE LOAD BEHAVIOR



Maximum permissible peak pulse power as a function of pulse duration

Fig. 8 Pulse-Load behavior



Maximum permissible peak pulse power as a function of pulse duration

Fig. 9 Pulse-Load behavior

TESTS AND REQUIREMENTS

Table 5 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Temperature Coefficient of Resistance (T.C.R.)	MIL-STD-202 Method 304	At +25/–55 °C and +25/+125 °C **Formula:** $T.C.R = \frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6$ (ppm/°C) Where t_1 = +25 °C or specified room temperature t_2 = –55 °C or +125 °C test temperature R_1 =resistance at reference temperature in ohms R_2 =resistance at test temperature in ohms	Refer to table 3
Short Time Overload	IEC60115-1 4.13	2.5 times of rated voltage or maximum overload voltage whichever is less for 5 sec at room temperature	±(2.0%+0.05 Ω)
High Temperature Exposure	IEC 60068-2-2	1,000 hours at T_A = 155 °C ±5 °C, unpowered	± (2.0%+0.05Ω) for D/F tol ± (3.0%+0.05Ω) for J tol
Humidity	IEC 60115-1 4.24.2	Steady state for 1,000 hours at 40 °C / 95% R.H. RCWV applied for 1.5 hours on and 0.5 hour off	±(3.0%+0.05 Ω)
Life	IEC 60115-1 4.25.1 MIL-STD-202 Method 108	1,000 hours at 70±2 °C, RCWV applied for 1.5 hours on, 0.5 hour off, still-air required	± (2.0%+0.05Ω) for D/F tol ± (3.0%+0.05Ω) for J tol
Resistance to Soldering Heat	IEC 60115-1 4.18 MIL-STD- 202 Method 210	Condition B, no pre-heat of samples Lead-free solder, 260±5 °C, 10±1 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	±(1.0%+0.05 Ω) No visible damage
Temperature Cycling	JESD22-A104C	-55/+125 °C for 1 cycle per hour, with 1,000 cycles. Devices mounted	±(1.0%+0.05 Ω)

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability Wetting	J-STD-002	Electrical Test not required Magnification 50X SMD conditions: Immerse the specimen into the solder pot at 245±3°C for 2±0.5 seconds.	Well tinned (≥95% covered) No visible damage
Board Flex	IEC 60115-1 4.33	Chips mounted on a 90mm glass epoxy resin PCB (FR4) Bending for 0402: 5mm 0603 & 0805: 3mm 1206 and above: 2mm Holding time: minimum 60 seconds	±(1.0%+0.05 Ω)

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 1	Jan. 20, 2022	-	- Add size 0201
Version 0	Feb. 03, 2021	-	- New product datasheet