Clock OSC SG5032CCN

Product name SG5032CCN 25.000000 MHz HJGA Product Number / Ordering code X1G0044710007xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

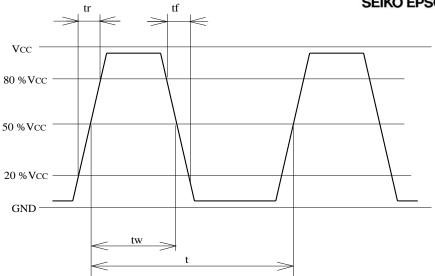
Pb free / Complies with EU RoHS directive

Reference weight Typ. 52 mg

1.Absolute maximum ratings									
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks			
Maximum supply voltage	Vcc-GND	-0.3	-	7	V	-			
Storage temperature	T_stg	-40	-	125	°C	Storage as single product			
Input voltage	Vin	-0.5	-	Vcc+0.5	V	OE terminal			

2.Specifications(charac	teristics)						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks	
Output frequency	f0		25.0000		MHz		
Supply voltage	Vcc	4.5	5	5.5	V	-	
Operating temperature	T_use	-40	-	85	٥C	-	
requency tolerance	f_tol	-50	-	50	x10 ⁻⁶	T_use	
Current consumption	Icc	-	-	20	mA	No load condition	
Stand-by current	I_std	•	-	-	μΑ	-	
Disable current	I_dis	-	-	10.0	mA	OE = GND	
Symmetry	SYM	40	-	60	%	50% Vcc Level L_CMOS=<50pF	
Output voltage	V _{OH}	Vcc-0.4	-	-		-	
	V_{OL}	-	-	0.4		-	
Output load condition	L_CMOS	-	-	50	pF	CMOS Load	
nput voltage	V _{IH}	0.8Vcc	-	-		OE terminal	
	V_{IL}	-	-	0.2Vcc		OE terminal	
Rise time	t _r	-	-	5	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=50pF	
all time	tf	-	-	5	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=50pF	
Start-up time	t_str	-	-	5	ms	t = 0 at 0.9Vcc	
itter	t _{DJ}		TBD	-	ps	Deterministic Jitter	
	T_{RJ}	-	TBD	-	ps	Random Jitter	
	t _{RMS}	-	TBD	-	ps	δ(RMS of total distribution)	
	t _{p-p}	-	TBD	-	ps	Peak to Peak	
	t _{acc}	-	TBD	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles	
Phase jitter	t _{PJ}	-	TBD	-	ps	Off set Frequency: 12kHz to 20MHz	
Phase noise	L(f)	-	TBD	-	dBc/Hz	Off set 1Hz	
		-	TBD	-	dBc/Hz	Off set 10Hz	
		-	TBD	-	dBc/Hz	Off set 100Hz	
		-	TBD	-	dBc/Hz	Off set 1kHz	
		-	TBD	-	dBc/Hz	Off set 10kHz	
		-	TBD	-	dBc/Hz	Off set 100kHz	
		-	TBD	-	dBc/Hz	Off set 1MHz	
requency aging	f_age	-5	-	5	x10 ⁻⁶	@+25°C first year	
		-	-	_		-	

3.Timing chart



4.Test circuit

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1) Waveform observation

VCC

ST
OF
OE
OE

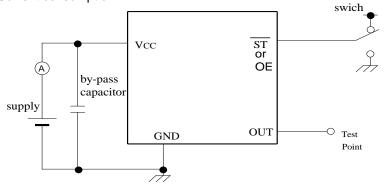
GND

OUT

Test Point
OUT

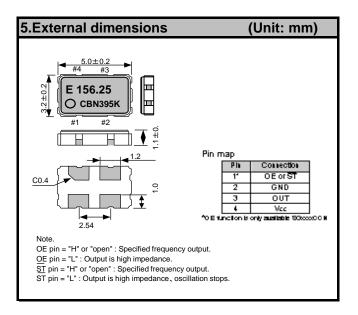
L_CMOS

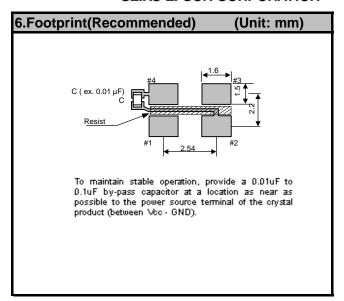
2) Current consumption

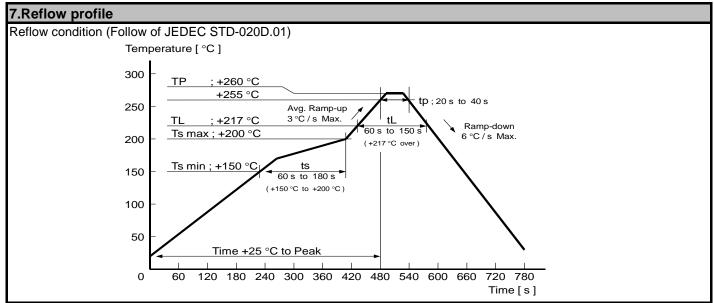


*Current consumption under the disable function should be = GND.

- 3) Condition
- (1) Oscilloscope
- Band width should be minimum 5 times higher (wider) than measurement frequency.
- · Probe earth should be placed closely from test point and lead length should be as short as possible
- * Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01 mF to 0.1 mF) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- · Start up time (0 %VCC ® 90 %VCC) of power source should be more than 150 ms.
- · Impedance of power supply should be as lowest as possible.







8.Packing information [1]Product number last 2 digits code(xx) description The recommended code is "00" X1G0044710007xx Condition Condition Code Code 01 Any Q'ty vinyl bag(Tape cut) 13 500pcs / Reel 1000pcs / Reel 11 Any Q'ty / Reel 00 12 250pcs / Reel

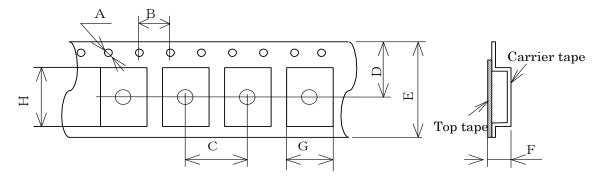
[2] Taping specification

Subject to EIA-481 & IEC-60286

(1) Tape dimensions

Material of the Carrier Tape : PS
Material of the Top Tape : PET+PE

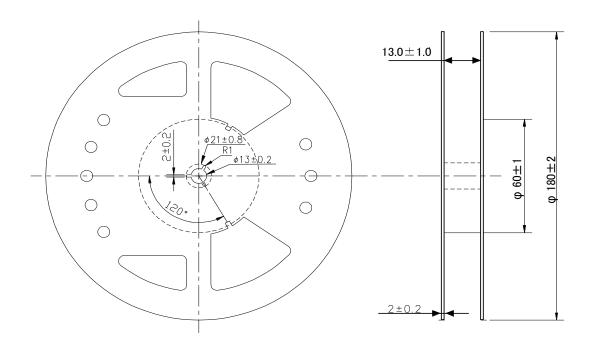
Unit: mm



Symbol	А	В	С	D	Е	F	G	Н
Value	φ1.5	4.0±0.1	8.0±0.1	7.25±0.2	12.0±0.2	1.40±0.1	3.5±0.1	5.4±0.1
	+0.1/-0							

(2) Reel dimensions

Center material : PS Material of the Reel : PS



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