# Clock OSC

# SG5032CCN

Product name SG5032CCN 7.175000 MHz HJGA Product Number / Ordering code X1G0044710043xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

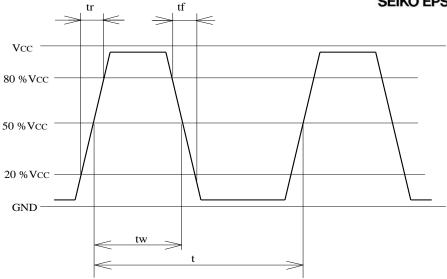
Pb free / Complies with EU RoHS directive

Reference weight Typ. 52 mg

1.Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	-	+7	V	-
Storage temperature	T_stg	-40	-	+125	°C	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	OE terminal

2.Specifications(charac		Min	T. #2	Max	Llmit	Conditions / Domorko
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	f0		7.175000		MHz	
Supply voltage	Vcc	4.5	5	5.5	V	-
Operating temperature	T_use	-40	-	+85	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	T_use
Current consumption	Icc	-	-	20	mA	No load condition
Stand-by current	I_std	-	-	-	μΑ	-
Disable current	I_dis	-	-	10.0	mA	OE = GND
Symmetry	SYM	40	ı	60	%	50% Vcc Level L_CMOS=<50pF
Output voltage	$V_{OH}$	Vcc-0.4	-	-		-
	V <sub>OL</sub>	_	-	0.4		-
Output load condition	L_CMOS	-	-	50	pF	CMOS Load
Input voltage	$V_{IH}$	0.8Vcc	-	-		OE terminal
	$V_{IL}$	-	-	0.2Vcc		OE terminal
Rise time	t <sub>r</sub>	-	-	5	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=50pF
Fall time	tf	-	-	5	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=50pF
Start-up time	t_str	-	-	5	ms	t = 0 at 0.9Vcc
Jitter	t <sub>DJ</sub>	=	0	-	ps	Deterministic Jitter
	t <sub>RJ</sub>	-	TBD	-	ps	Random Jitter
	t <sub>RMS</sub>	-	TBD	-	ps	δ(RMS of total distribution)
	t <sub>p-p</sub>	-	TBD	-	ps	Peak to Peak
	t <sub>acc</sub>	-	-	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles
Phase jitter	t <sub>PJ</sub>	-	TBD	_	ps	Off set Frequency: 12kHz to 20MHz
Phase noise	L(f)	-	-	-	dBc/Hz	Off set 1Hz
		-	TBD	-	dBc/Hz	Off set 10Hz
		-	TBD	_	dBc/Hz	Off set 100Hz Vcc=3.3V
		-	TBD	_	dBc/Hz	Off set 1kHz
		_	TBD	_	dBc/Hz	Off set 10kHz
		_	TBD	_	dBc/Hz	Off set 100kHz Vcc=3.3V
		_	TBD	_	dBc/Hz	Off set 1MHz
Frequency aging	f_age	-5	-	5	x10 <sup>-6</sup>	@+25°C first year
	i_age	9			^10	C. 25 5 mot your

# 3.Timing chart



#### 4.Test circuit

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1) Waveform observation

VCC

ST

OF

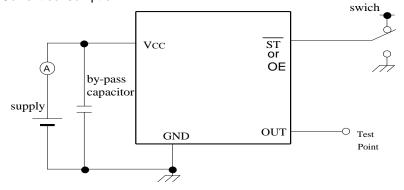
Supply

GND

OUT

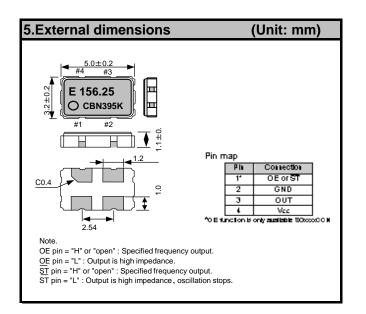
L\_CMOS

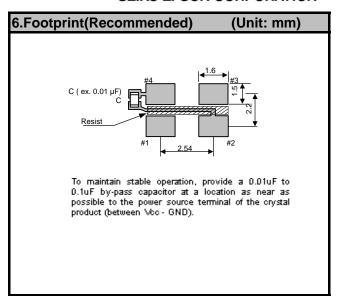
#### 2) Current consumption

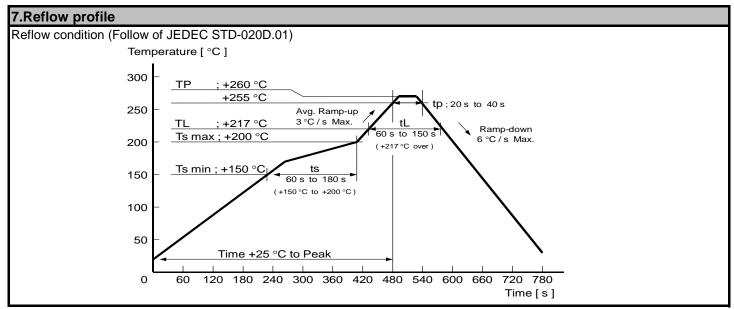


\*Current consumption under the disable function should be = GND.

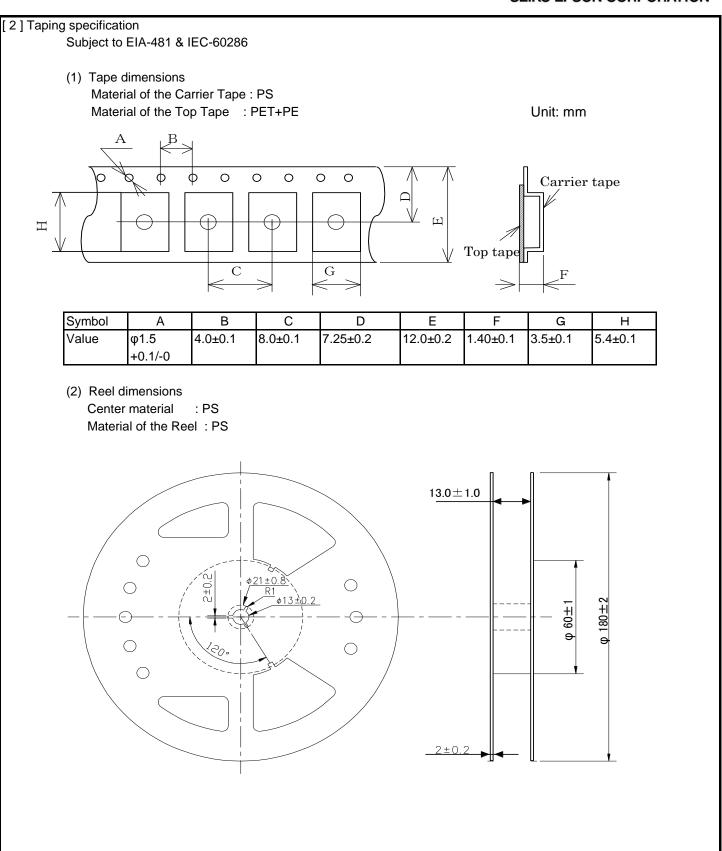
- 3) Condition
- (1) Oscilloscope
- · Band width should be minimum 5 times higher (wider) than measurement frequency.
- · Probe earth should be placed closely from test point and lead length should be as short as possible
- \* Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L\_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01  $\mu$ F to 0.1  $\mu$ F) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- · Start up time (0 %VCC to 90 %VCC) of power source should be more than 150 µs.
- · Impedance of power supply should be as lowest as possible.







#### 8.Packing information [ 1 ]Product number last 2 digits code(xx) description The recommended code is "00" X1G0044710043xx Condition Condition Code Code 01 Any Q'ty vinyl bag(Tape cut) 13 500pcs / Reel 1000pcs / Reel 11 Any Q'ty / Reel 00 12 250pcs / Reel



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