# Clock OSC

## SG5032CCN

Product name SG5032CCN 45.000000 MHz HJGA
Product Number / Ordering code X1G0044710069xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

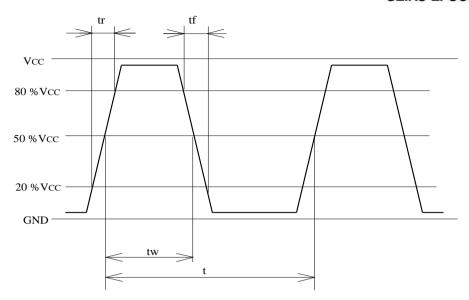
Pb free / Complies with EU RoHS directive

Reference weight Typ. 52 mg

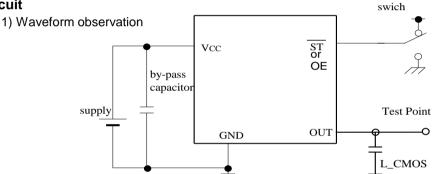
1. Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	1	+7	V	-
Storage temperature	T_stg	-40	1	+125	°C	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	OE terminal

2.Specifications(charac	teristics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	f0		45.000000		MHz	
Supply voltage	Vcc	4.5	5	5.5	V	-
Operating temperature	T_use	-40	-	+85	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	T_use
Current consumption	Icc	-	-	20	mA	No load condition
Stand-by current	I_std	-	-	-	μΑ	-
Disable current	I_dis	=	-	10.0	mA	OE = GND
Symmetry	SYM	40	-	60	%	50% Vcc Level L_CMOS=<50pF
Output voltage	V <sub>OH</sub>	Vcc-0.4	-	-		-
	$V_{OL}$	-	-	0.4		-
Output load condition	L_CMOS	-	-	50	pF	CMOS Load
Input voltage	$V_{IH}$	0.8Vcc	-	-		OE terminal
	$V_{IL}$	-	-	0.2Vcc		OE terminal
Rise time	t <sub>r</sub>	-	-	5	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=50pF
Fall time	tf	-	-	5	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=50pF
Start-up time	t_str	-	-	5	ms	t = 0 at 0.9Vcc
Jitter	t <sub>DJ</sub>	-	-	-	ps	Deterministic Jitter
	t <sub>RJ</sub>	-	-	-	ps	Random Jitter
	t <sub>RMS</sub>	=	-	-	ps	σ(RMS of total distribution)
	t <sub>p-p</sub>	-	-	-	ps	Peak to Peak
	t <sub>acc</sub>	-	-	-	ps	Accumulated Jitter( $\sigma$ ), n = 2 to 50 000 cycles
Phase jitter	t <sub>PJ</sub>	-	-	-	ps	Offset Frequency: 12 kHz to 20 MHz
Phase noise	L(f)	-	-	-	dBc/Hz	Offset 1 Hz
		-	-	-	dBc/Hz	Offset 10 Hz
		-	-	-	dBc/Hz	Offset 100 Hz
		-	-	-	dBc/Hz	Offset 1 kHz
		-	-	-	dBc/Hz	Offset 10 kHz, Vcc = 3.3 V
		-	-	-	dBc/Hz	Offset 100 kHz
		-	-	-	dBc/Hz	Offset 1 MHz
Frequency aging	f_age	-5	-	5	x10 <sup>-6</sup>	@+25°C first year
		-	-	-		-

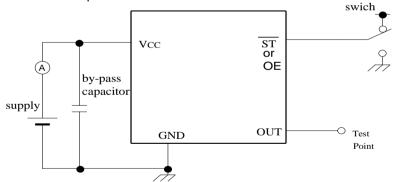
## 3.Timing chart



### 4.Test circuit

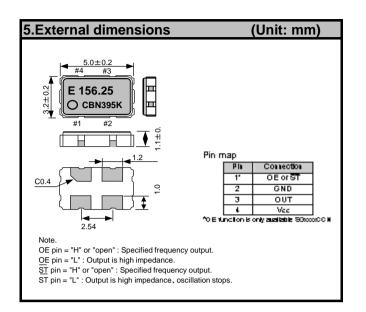


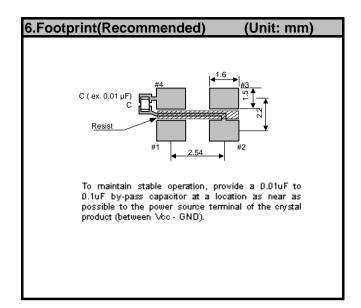
## 2) Current consumption

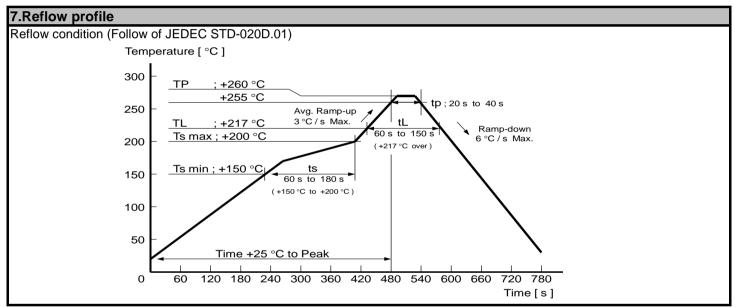


\*Current consumption under the disable function should be = GND.

- 3) Condition
- (1) Oscilloscope
- $\cdot$  Band width should be minimum 5 times higher (wider) than measurement frequency.
- · Probe earth should be placed closely from test point and lead length should be as short as possible
- \* Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L\_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01  $\mu F$  to 0.1  $\mu F$ ) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- · Start up time (0 %VCC to 90 %VCC) of power source should be more than 150 µs.
- · Impedance of power supply should be as lowest as possible.







1 ]Product number last 2 digits code(xx) description			The recommended code is "00"		
	X1G0044	1710069xx			
	Code	Condition	Code	Condition	
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel	
	11	Any Q'ty / Reel	00	1000pcs / Reel	
	12	250pcs / Reel			

# [ 2 ] Taping specification Subject to EIA-481 & IEC-60286 (1) Tape dimensions Material of the Carrier Tape: PS Material of the Top Tape : PET+PE Unit: mm User direction of feed 0 0 0 0 0 Carrier tape MARKING Ы Top tape Symbol В С D Ε F G Н 4.0±0.1 7.25±0.2 12.0±0.2 1.40±0.1 3.5±0.1 Value φ1.5 8.0±0.1 5.4±0.1 +0.1/-0 (2) Reel dimensions Center material : PS Material of the Reel: PS $13.0 \pm 1.0$ $\bigcirc$ $\phi$ 60 $\pm$ 1 $\bigcirc$ $\bigcirc$ $2 \pm 0.2$

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  - / Traffic control equipment
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