Temperature Compensated Crystal Oscillator (TCXO)

· Package size (7.0 mm × 5.0 mm × 1.5 mm)
· Ultra high stability TCXO
· Output waveform : CMOS

[1] Product Number / Product Name

(1-1) Product Number / Ordering Code

X1G0060610090xx

last 2 digits code(xx) define Quantity.

The standard is "14", 1 000 pcs/Reel.

(1-2) Product Name / Model Name

TG-5510CB-90N 25.000000 MHz

[2] Operating range

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Supply voltage	V_{CC}	3.135	3.3	3.465	V	Vcc = 3.3 V ± 5 %
	GND	0	-	0	V	-
Operating temperature range	T_use	-40	-	+85	°C	-
Output load	Load_C	13.5	15	16.5	pF	-

[3] Frequency characteristics   (Vcc = 3.3 V, GND = 0.0 V, Load = 15 pF, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Output Frequency	fo	-	25	-	MHz	-
Frequency tolerance *1	f_tol	-1.0	-	+1.0	$\times 10^{-6}$	T_use = +25 °C ±2 °C After 2 reflows *2
Frequency / temperature characteristics	fo-Tc	-0.28	-	+0.28	$\times 10^{-6}$	T_use = -40 °C to +105 °C (Reference to (fmax+fmin)/2)
Frequency / load coefficient	fo-Load	-0.1	-	+0.1	$\times 10^{-6}$	Load ±10 %
Frequency / voltage coefficient	fo-V_{CC}	-0.1	-	+0.1	$\times 10^{-6}$	Vcc ±5 %
Frequency aging	f_age	-0.5	-	+0.5	$\times 10^{-6}$	T_use = +25 °C first year
		-3.0	-	+3.0	$\times 10^{-6}$	T_use = +25 °C 20 years
Holdover stability (Free-run accuracy)	-	-4.6	-	+4.6	$\times 10^{-6}$	*3

*1 Include initial frequency tolerance and frequency deviation after reflow cycles.

*2 Measured in the elapse of 24 hours after reflow soldering.

*3 This includes initial frequency tolerance, frequency / temperature characteristics, frequency / load coefficient, frequency/voltage coefficient and frequency aging (+25 °C, 20 years) .

[4] Electrical characteristics   (Vcc = 3.3 V, GND = 0.0 V, Load = 15 pF, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Current consumption	I_{CC}	-	-	7.0	mA	-
Output voltage	VOH	90 %	-	-	V	-
	VOL	-	-	10 %	V	-
Rise time	tr	-	-	8.0	ns	10 % Vcc to 90 % Vcc level
Fall time	tf	-	-	8.0	ns	90 % Vcc to 10 % Vcc level
Start-up time	t_str	-	-	5.0	ms	t = 0 % at 90 % Vcc
Symmetry	SYM	45	-	55	%	50 % Vcc Level

[5] Enable/disable function

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Enable voltage	VIH	70 % Vcc	-	Vcc	V	OE terminal (Enable voltage)
Disable voltage	VIL	0	-	30 % Vcc	V	OE terminal (Disable voltage)
Input impedance	Zin	50	-	-	kΩ	-
Output resistance of disable	-	High impedance			-	

**[For other general specifications, please refer to the attached Full Data Sheet below]**

High Stability Temperature Compensated Crystal Oscillator (TCXO)
Supports $\pm 0.28 \times 10^{-6}$ over -40 ºC to +85 ºC

TG-5510CB

Key Specifications

Frequency Range**:**	10 MHz to 54 MHz
Supply Voltage**:**	3.3 V Typ.
Operation Temperature**:**	-40 ºC to +85 ºC (+105 ºC option)
Freq. / Temp characteristic**:**	$\pm 0.28 \times 10^{-6}$ Max. over temperature
Frequency Slope**:**	$\pm 0.2 \times 10^{-6}$ / ºC Max. over temperature
Frequency Aging**:**	$\pm 3.0 \times 10^{-6}$ Max. / 20 years (meets Stratum3)
Package**:**	5.0 × 3.2 × 1.45 mm (10 pins)

With optional on-chip filter capacitor for lower phase noise
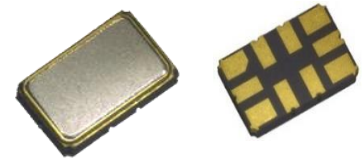
TG-5510CB
(10 pins)

Description - TG-5510CB is a high stability TCXO available with either CMOS or Clipped Sine output

• Specified to +85 ºC for 5G Base Stations and Edge Computing
  needs outdoor installation, miniaturization, and fan-less operation
• Compared to other TCXOs it offers a variety of improvements such
  as low temperature slope and phase noise
• Complies with GR-1244-CORE Stratum3 and G.8262.1, G.8273.2 (Class A, B)
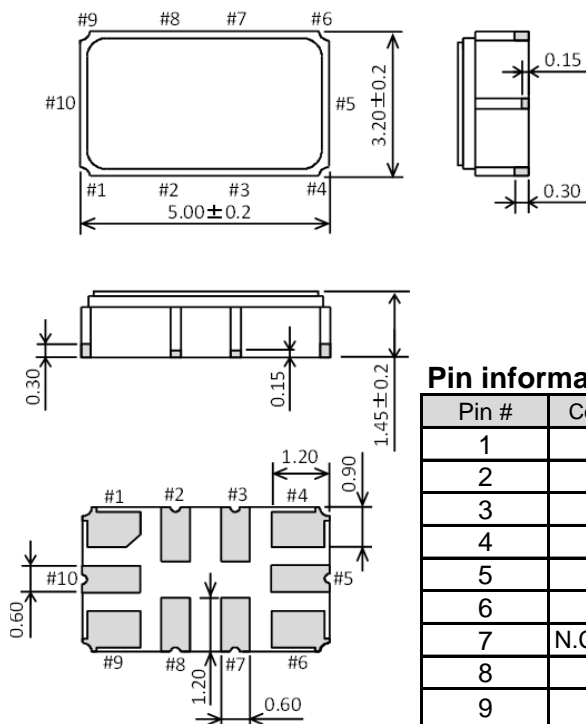• Note: this product does not have a voltage control (Vc) function

Applications

Network equipment
 • Base station
 • Microwave
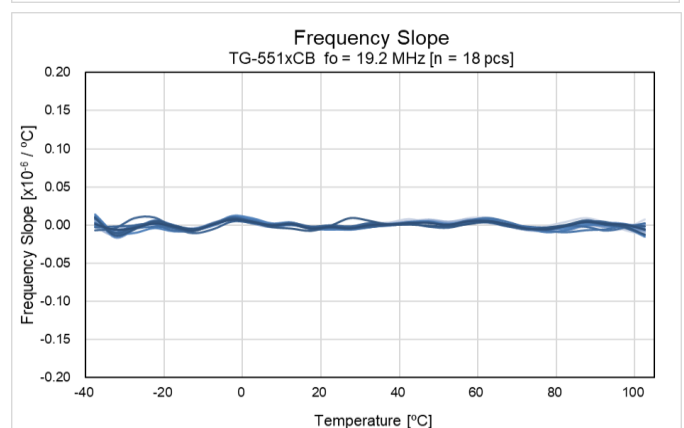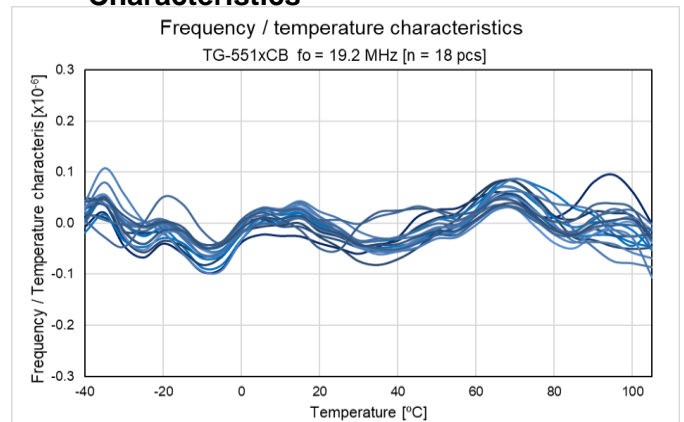Sync compliance standards
 • Stratum3
 • SyncE
 • IEEE1588

Outline dimensions

0.15
3.20±0.2
0.30
5.00±0.2
0.30
0.15
1.45±0.2
1.20
0.90
0.60
0.60
1.20

Pin information

Pin #	Connection
1	N.C.
2	N.C.
3	OE
4	GND
5	N.C.
6	OUT
7	N.C. or Filter
8	N.C.
9	V_{CC}
10	N.C.

Characteristics

Frequency / temperature characteristics
TG-551xCB fo = 19.2 MHz [n = 18 pcs]

Frequency Slope
TG-551xCB fo = 19.2 MHz [n = 18 pcs]

[1] Product Number / Product Name

(1) Product Name

TG-5510CB - *** 38.880000MHz

①   ② ③   ④

①Model ②Package type (5.0 × 3.2 × 1.45 mm) ③Spec segment (Please contact Epson) ④Frequency

Selectable specs.: Output (CMOS, Clipped sine), Frequency/temperature (±0.28 × 10^{-6} Max., ±0.25 × 10^{-6} Max.)

Oparating tenterature (-40 °C to +85 °C, -40 °C to +105 °C), OE function (Active High)

Filter function (Filter-ON, NO-Filter)

(2) Product Number / Ordering Code

TG-5510CB: X1G006061xxxx14

The standard product name & number are as follows:

Product name	Freq. [MHz]	Product number	Selectable specs
TG-5510CB-98N	10.000	X1G006061009814	Output: CMOS Frequency/temperature: ±0.28 × 10^{-6} Max. Oparating tenterature: -40 °C to +85 °C OE function: Active High Filter function: NO-Filter
TG-5510CB-94N	12.800	X1G006061009414	
TG-5510CB-81N	19.200	X1G006061008114	
TG-5510CB-91N	19.440	X1G006061xxxx14	
TG-5510CB-95N	20.000	X1G006061009514	
TG-5510CB-79N	24.000	X1G006061xxxx14	
TG-5510CB-77N	24.576	X1G006061007714	
TG-5510CB-90N	25.000	X1G006061009014	
TG-5510CB-72N	25.175	X1G006061007214	
TG-5510CB-92N	25.600	X1G006061009214	
TG-5510CB-87N	26.000	X1G006061xxxx14	
TG-5510CB-76N	30.720	X1G006061007614	
TG-5510CB-81N	38.400	X1G006061008214	
TG-5510CB-90N	38.880	X1G006061009014	
TG-5510CB-93N	40.000	X1G006061009314	
TG-5510CB-80N	48.000	X1G006061xxxx14	
TG-5510CB-78N	49.152	X1G006061007814	
TG-5510CB-97N	50.000	X1G006061009714	
TG-5510CB-73N	52.000	X1G006061xxxx14	
TG-5510CB-74N	54.000	X1G006061xxxx14	
TG-5510CB-41N	10.000	X1G006061004114	Output: CMOS Frequency/temperature: ±0.28 × 10^{-6} Max. Oparating tenterature: -40 °C to +105 °C OE function: Active High Filter function: NO-Filter
TG-5510CB-42N	19.200	X1G006061004214	
TG-5510CB-43N	20.000	X1G006061004314	
TG-5510CB-44N	24.576	X1G006061004414	
TG-5510CB-45N	25.000	X1G006061004514	
TG-5510CB-46N	38.880	X1G006061004614	
TG-5510CB-47N	40.000	X1G006061004714	
TG-5510CB-48N	50.000	X1G006061004814	

* Please contact Epson for detail of product number

Please also contact Epson for other frequencies and specifications

[2] Absolute Maximum Ratings

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Supply voltage	V_{CC}-GND	-0.5	-	+4.0	V	
Input voltage	V_{IN}	-0.5		Vcc + 0.5	V	OE Terminal
Storage temperature range	T_stg	-40	-	+105	°C	Storage as single product

[3] Operating Conditions

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Supply voltage	V_{CC}	3.135	3.3	3.465	V	V_{CC} = 3.3 V ± 5 %
Supply voltage	GND	0	-	0	V	
Input voltage	V_{IN}	GND	-	Vcc	V	OE Terminal
Operating temperature range	T_use	-40	+25	+85	°C	+105 °C Max. (Option)
CMOS load condition	L_CMOS	13.5	15	16.5	pF	
Output load (Clipped sine)	Load_R	9	10	11	kΩ	
Output load (Clipped sine)	Load_C	9	10	11	pF	
Output load (Clipped sine)	Cc	0.01	-	-	µF	AC coupling capacitor*

* An AC coupling capacitor is not included in this product, please attach an external AC coupling capacitor to the OUT pin

[4] Frequency Characteristics

(V_{CC} = 3.3 V, GND = 0.0 V, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Output Frequency	fo	10	-	54	MHz	Standard frequency
		10, 12.8, 19.2, 20, 24.576, 25, 25.175, 25.6, 30.72, 38.4, 38.88, 40, 49.152, 50				
Frequency tolerance *1	f_tol	-1.0	-	+1.0	x10⁻⁶	T_use = +25 °C ± 2 °C After 3 reflows *2
Frequency / temperature characteristics	fo-Tc	-0.28 (-0.25)	- -	+0.28 (+0.25)	x10⁻⁶	T_use = -40 °C to +85 °C, Reference to (fmax+fmin) / 2 (Option)
Frequency slope	f_slp	-0.2 (-0.05)	- -	+0.2 (+0.05)	x10⁻⁶ / °C	T_use = -40 °C to +85 °C, One frequency reading every 5 °C Min. (Option)
Frequency / load coefficient	fo-Load	-0.1	-	+0.1	x10⁻⁶	Load_R // Load_C ± 10 % L_CMOS ± 10 %
Frequency / voltage coefficient	fo-V_{CC}	-0.1	-	+0.1	x10⁻⁶	V_{CC} = 3.3 V ± 5 %
Frequency aging *3	f_age	-0.5	-	+0.5	x10⁻⁶	T_use = +25 °C, 1 year
Frequency aging *3	f_age	-3.0	-	+3.0	x10⁻⁶	T_use = +25 °C, 20 years
Holdover stability (Free-run accuracy) *4	f_hos	-4.6	-	+4.6	x10⁻⁶	T_use = +25 °C, 20 years

*1 Includes initial frequency tolerance and frequency deviation after reflow cycles.

*2 Measured 24 hours after reflow soldering.

*3 Aging is estimated from environmental reliability tests and the expected amount of frequency variation over time. It is not intended as a guarantee of performance over the product-life cycle.

*4 This includes initial frequency tolerance, frequency / temperature characteristics, frequency / load coefficient, frequency/voltage coefficient and frequency aging (+25 °C, 20 years).

[5] Electrical Characteristics
1) Electrical Characteristics

(V_{CC}= 3.135 V to 3.465 V, GND = 0.0 V, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Current consumption (CMOS)	Icc	-	-	7 9 10	mA	10 MHz ≤ fo ≤ 26 MHz 26 MHz < fo ≤ 40 MHz 40 MHz < fo ≤ 54 MHz
Current consumption (Clipped sine)	Icc	-	-	6	mA	
Output voltage (CMOS)	V_{OH}	90 % V_{CC}	-	-	V	
Output voltage (CMOS)	V_{OL}	-	-	10 % V_{CC}	V	
Output voltage (Clipped sine)	Vpp	0.8	-	-	V	Peak to peak voltage
Rise time (CMOS)	tr	-	-	8	ns	10 % V_{CC} to 90 % V_{CC} level
Fall time (CMOS)	tf	-	-	8	ns	90 % V_{CC} to 10 % V_{CC} level
Start-up time	t_str	-	-	5	ms	Until output signal has been reached min 90 % of final amplitude. Applies to both Filter-ON and NO-filter options.
Symmetry (CMOS)	SYM	45	50	55	%	Measured at Vcc/2
Phase noise (fo = 19.2 MHz) * Filter-ON	L(f)	-	-69	-	dBc/Hz	1 Hz offset
		-	-100	-		10 Hz offset
		-	-126	-		100 Hz offset
		-	-145	-		1 kHz offset
		-	-155	-		10 kHz offset
		-	-158	-		100 kHz offset
		-	-158	-		1 MHz offset

* For other frequencies, please refer to Charts (7-10), Phase noise

2) Enable/Disable Function

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Input voltage	V_{IH}	70 % V_{CC}	-	V_{CC}	V	OE terminal (Enable voltage)
Input voltage	V_{IL}	0	-	30 % V_{CC}	V	OE terminal (Disable voltage)
Input impedance	Zin	50	-	-	kΩ	

OE input level	Oscillation	Outputs
"H" or "Open"	Enable	Enable: specified frequency
"L"	Enable	Disable: high impedance

* OE input voltage must be lower than V_{CC}. Note that rise-up time of OE input voltage must not be shorter than the rise-up time of supply voltage.
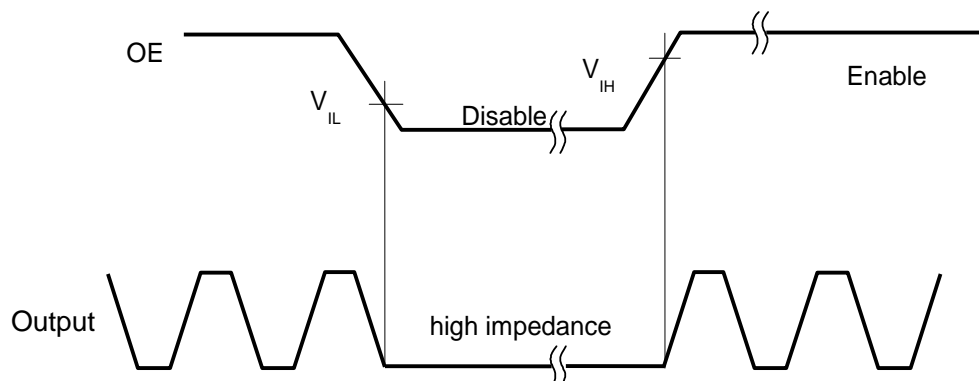
[6] Test Circuit: CMOS Output

1) Output Waveform
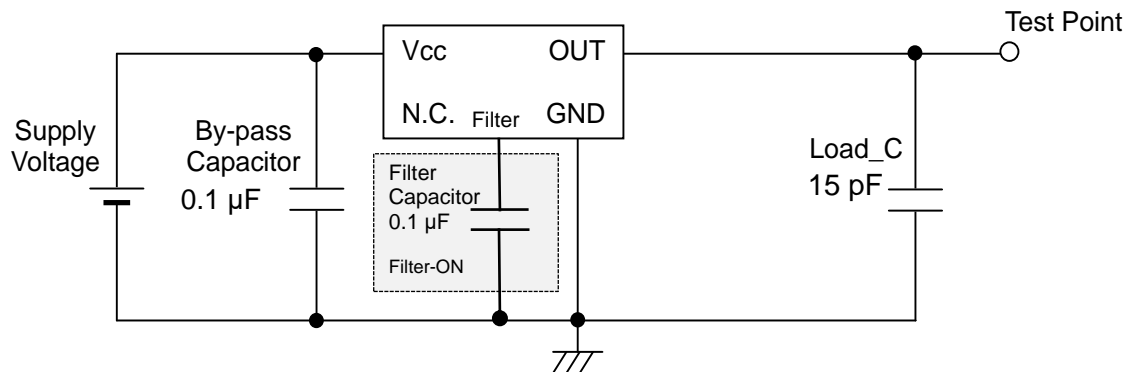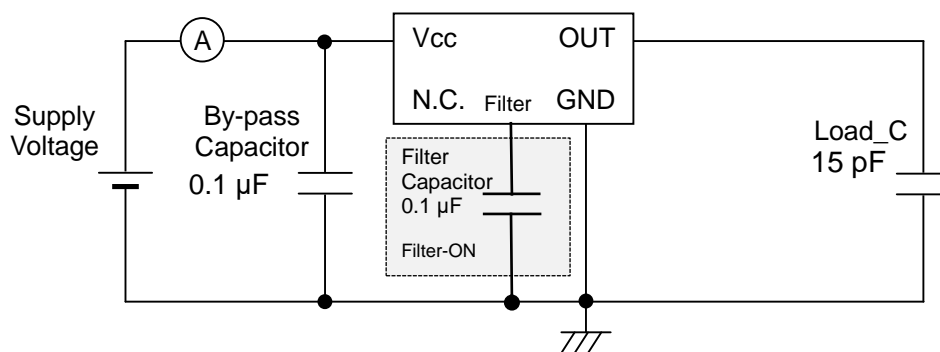
2) Current Consumption

### 3) Conditions

1. Oscilloscope: Impedance Min. 1 MΩ
   Input capacitance Max. 10 pF
   Bandwidth Min. 300 MHz
2. L_CMOS includes probe capacitance
3. A capacitor (By-pass: 0.1 µF) is placed between V_{CC} and GND close to the TCXO
4. Use the current meter whose internal impedance value is small
5. Power supply connections should be as low impedance as possible
6. GND pin should be connected to low impedance GND

[6] Test Circuit: Clipped Sine Output

1) Output Waveform
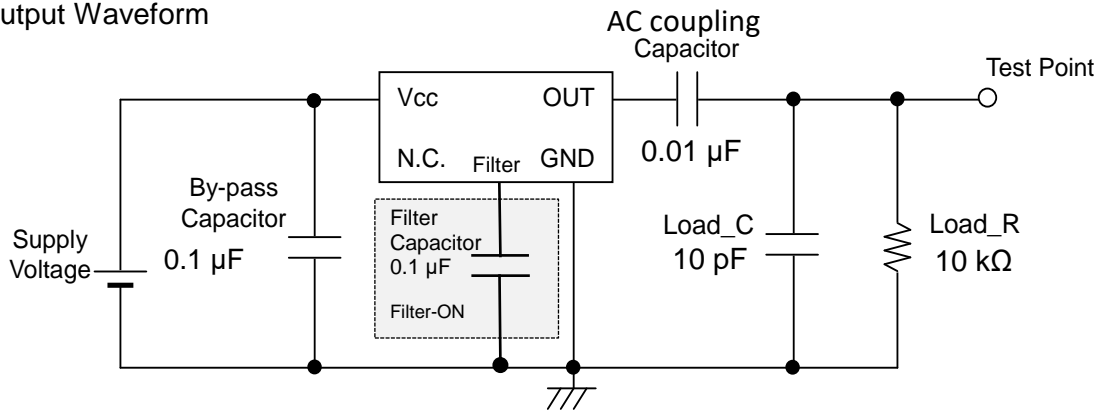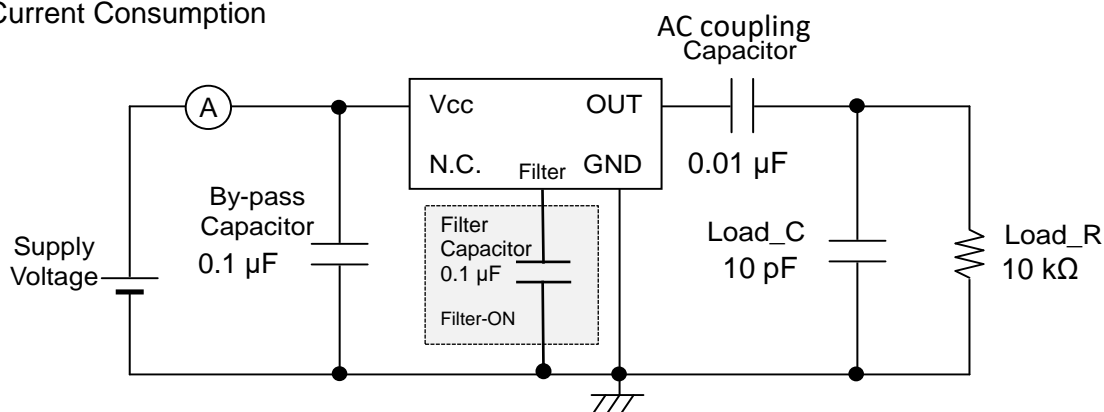
2) Current Consumption

3) Conditions

1. Oscilloscope: Impedance Min. 1MΩ
   Input capacitance Max. 10 pF
   Bandwidth Min. 300 MHz
2. Load_C includes probe capacitance
3. A bypass capacitor (0.1 μF) is placed between V_{CC} and GND close to the TCXO
4. Use the current meter whose internal impedance value is small
5. Power supply connections should be as low impedance as possible
6. GND pin should be connected to low impedance GND

[7] Chracteristic Data

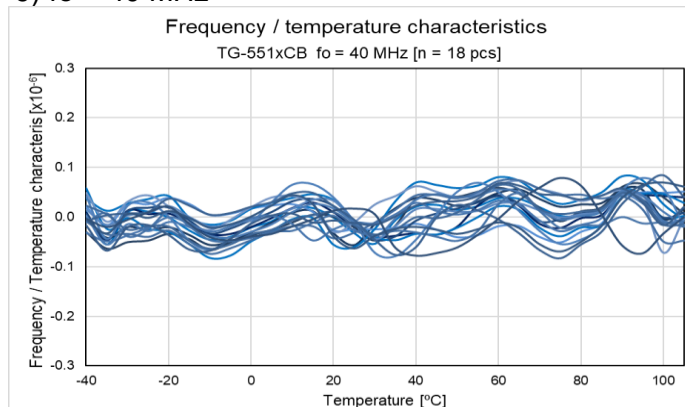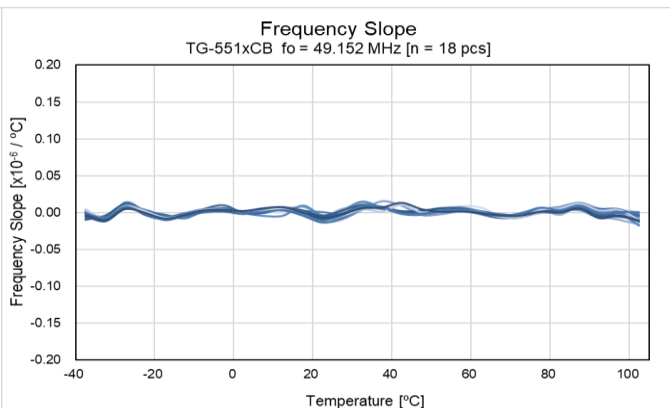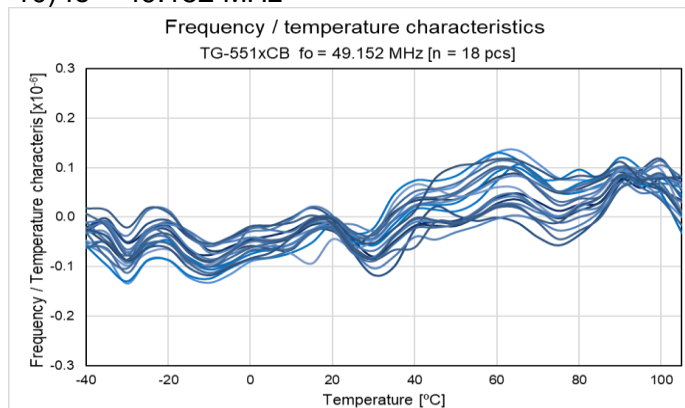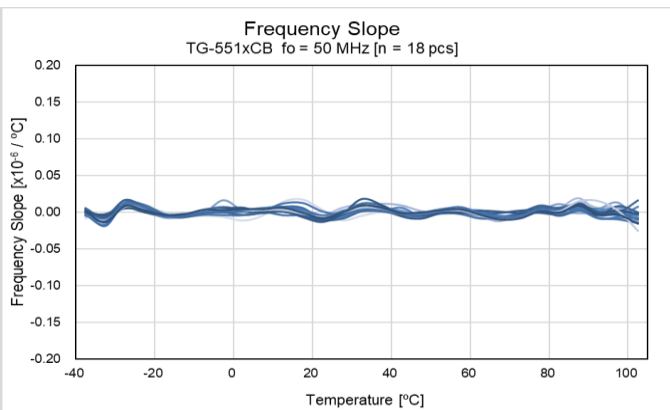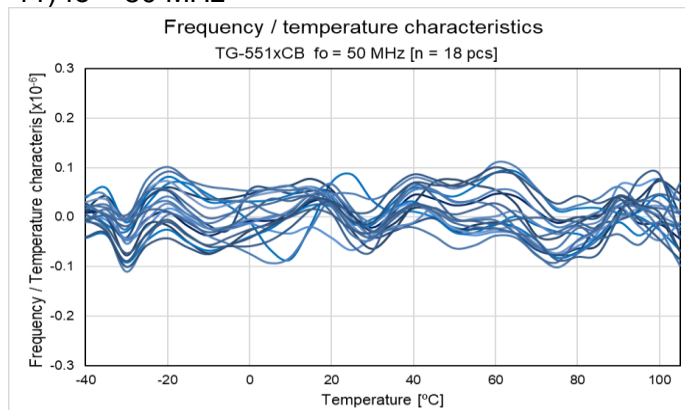## (7-1) Frequency / Temperature Characteristics & Frequency Slope

1) fo = 19.2 MHz

2) fo = 24.576 MHz

3) fo = 25 MHz
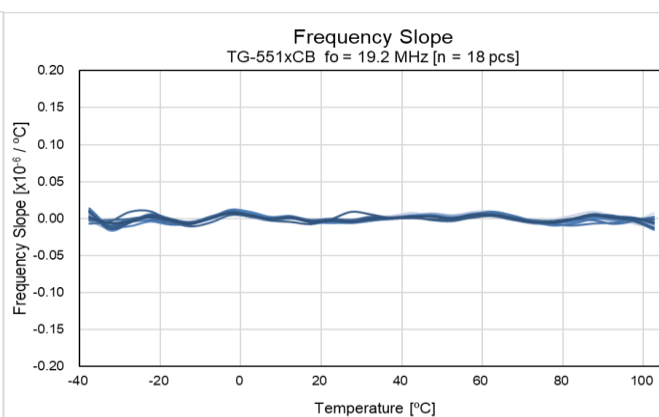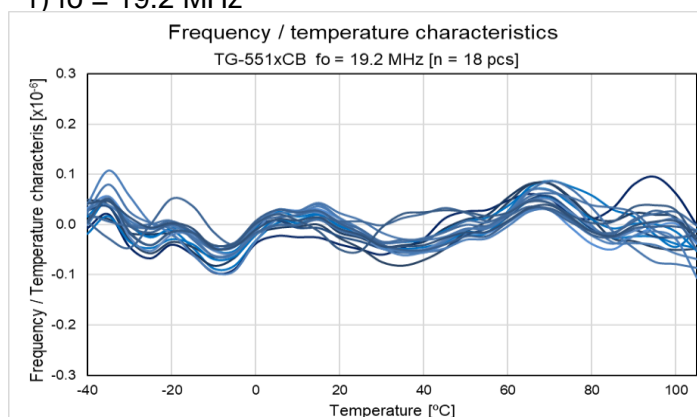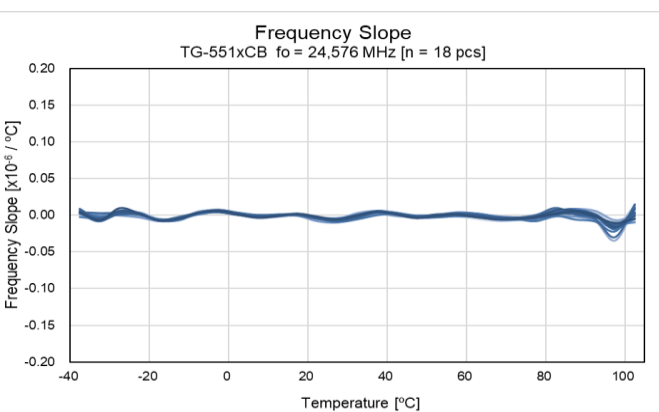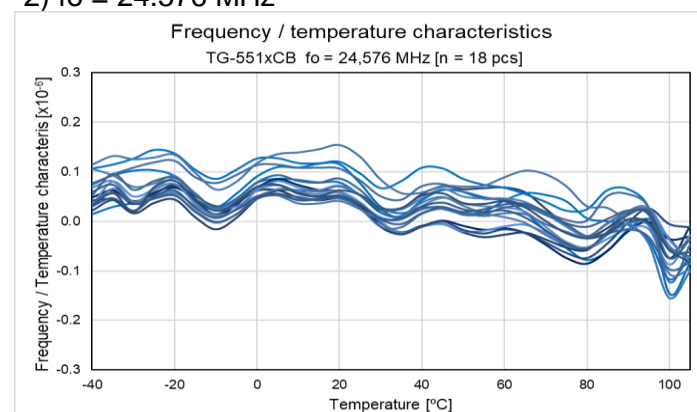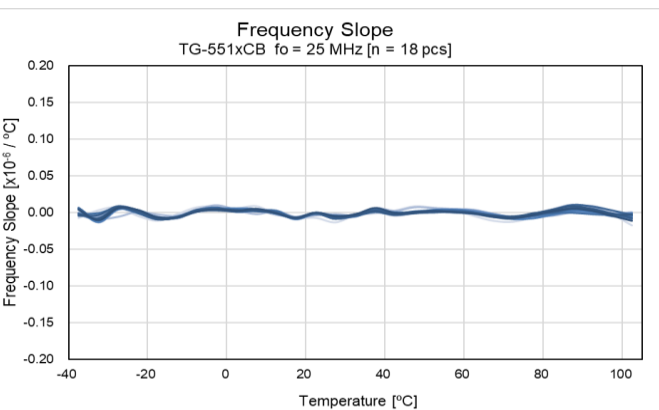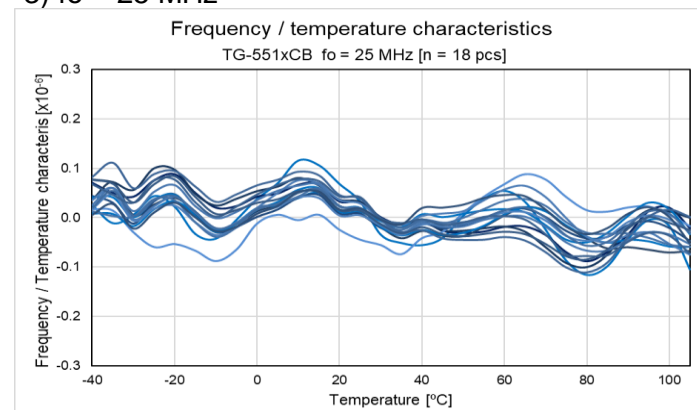
4) fo = 25.175 MHz

(7-1) Frequency / Temperature Characteristics & Frequency Slope

5) fo = 25.6 MHz
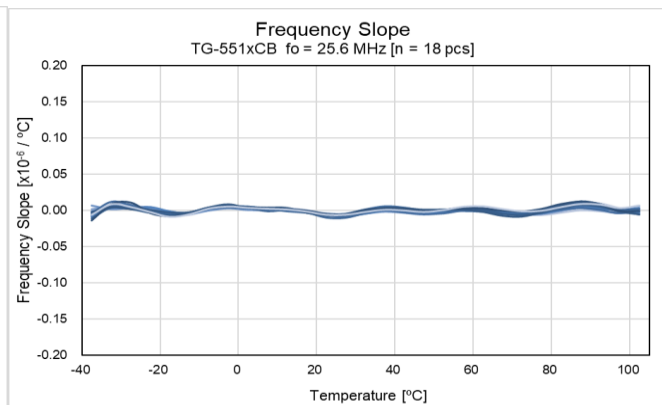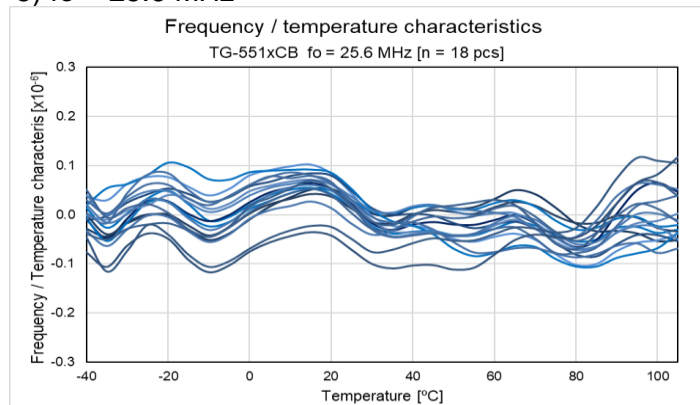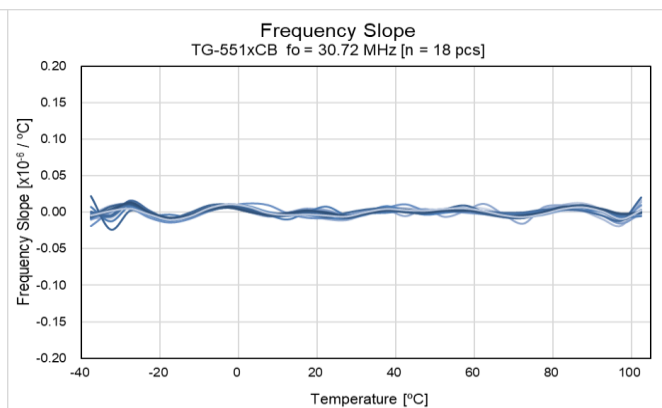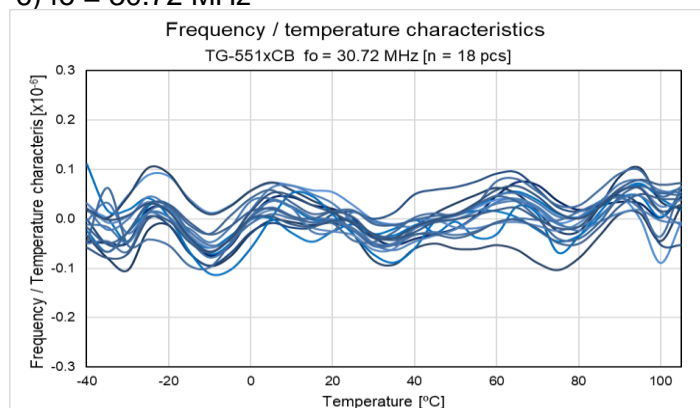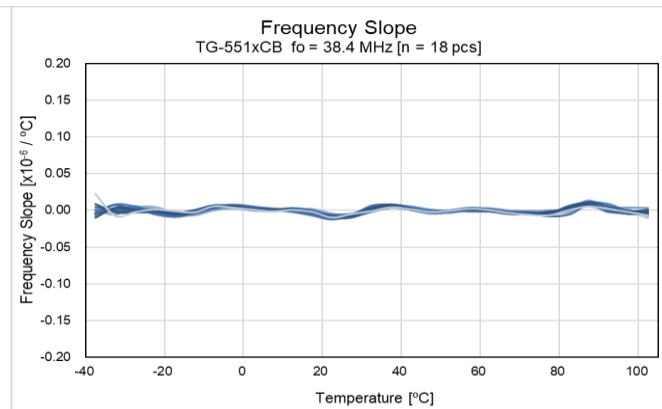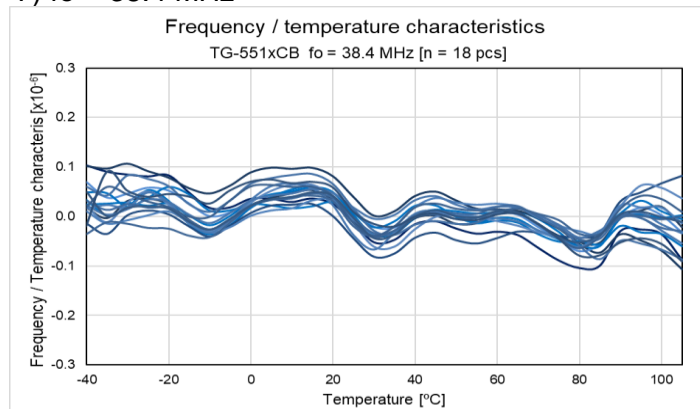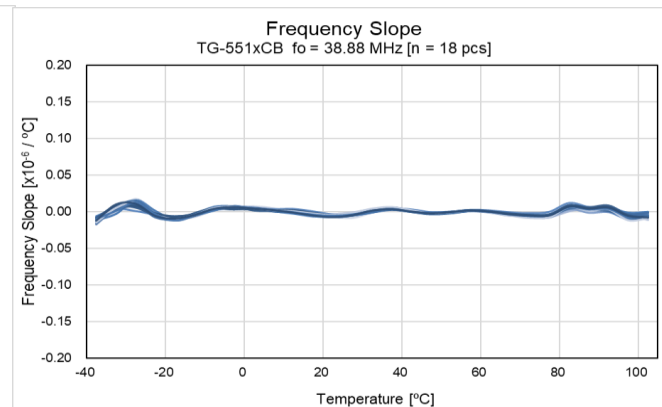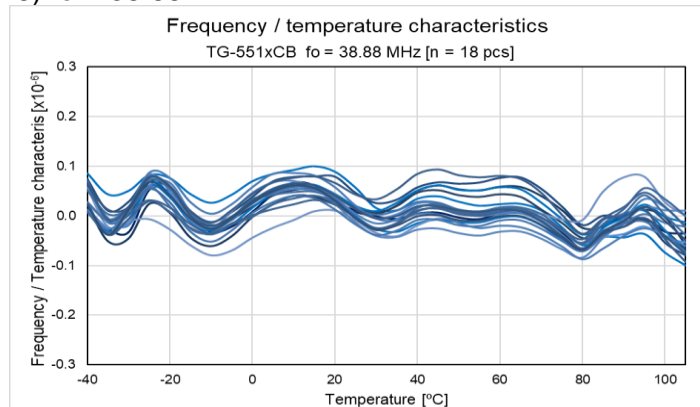
6) fo = 30.72 MHz

7) fo = 38.4 MHz

8) fo = 38.88 MHz

　　　　　　　　　　　　　　　　　　　No,: TG-5510CB_rev1.0

(7-1) Frequency / Temperature Characteristics & Frequency Slope

9) fo = 40 MHz

Frequency / temperature characteristics
TG-551xCB fo = 40 MHz [n = 18 pcs]

Frequency Slope
TG-551xCB fo = 40 MHz [n = 18 pcs]

10) fo = 49.152 MHz

Frequency / temperature characteristics
TG-551xCB fo = 49.152 MHz [n = 18 pcs]

Frequency Slope
TG-551xCB fo = 49.152 MHz [n = 18 pcs]

11) fo = 50 MHz

Frequency / temperature characteristics
TG-551xCB fo = 50 MHz [n = 18 pcs]

Frequency Slope
TG-551xCB fo = 50 MHz [n = 18 pcs]

(7-2) Frequency / Voltage Coefficient

Freq. / voltage cofficient vs fo
(V_{CC} = 3.3 v ± 5 %, n = 20 pcs)
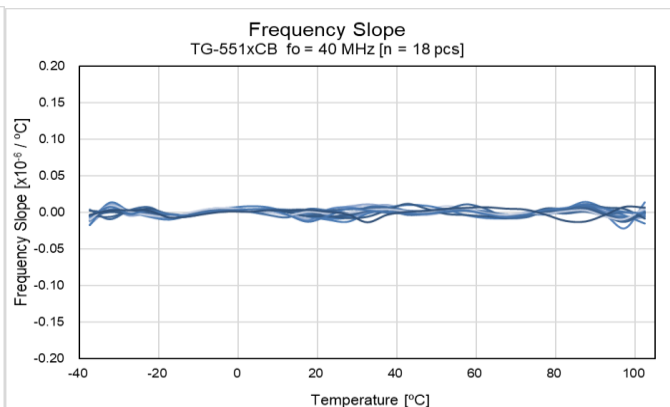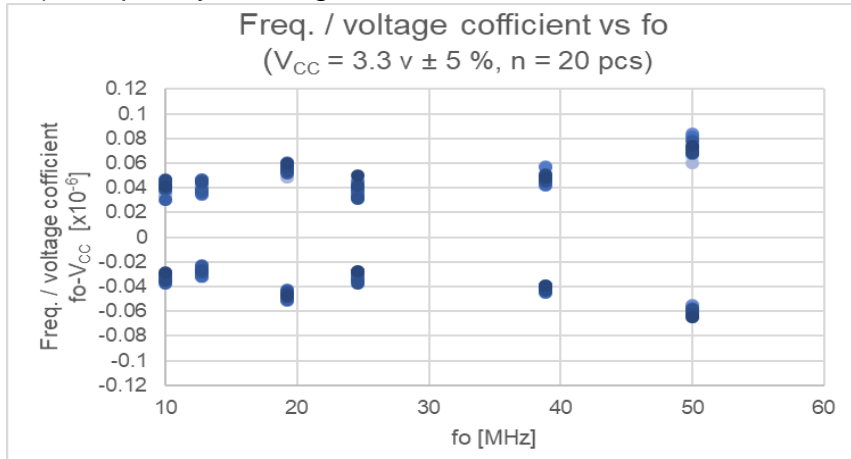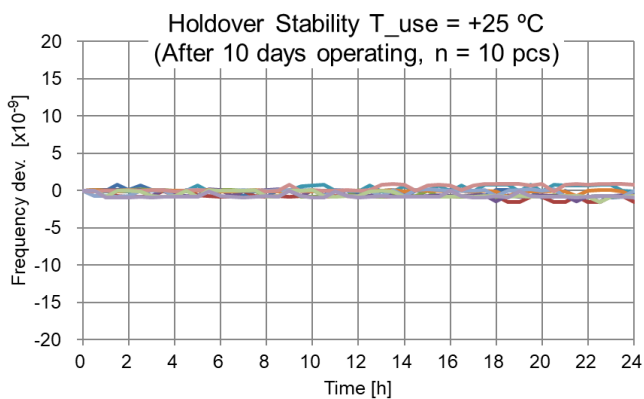
(7-3) Frequency Aging Estimation

 * Aging stability is estimated from environmental reliability tests and the expected amount of frequency variation of the product.

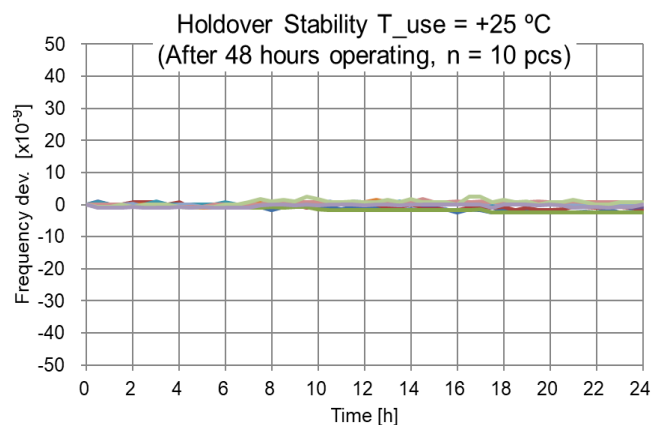It is not intended as a guarantee of performance over the product-life cycle.

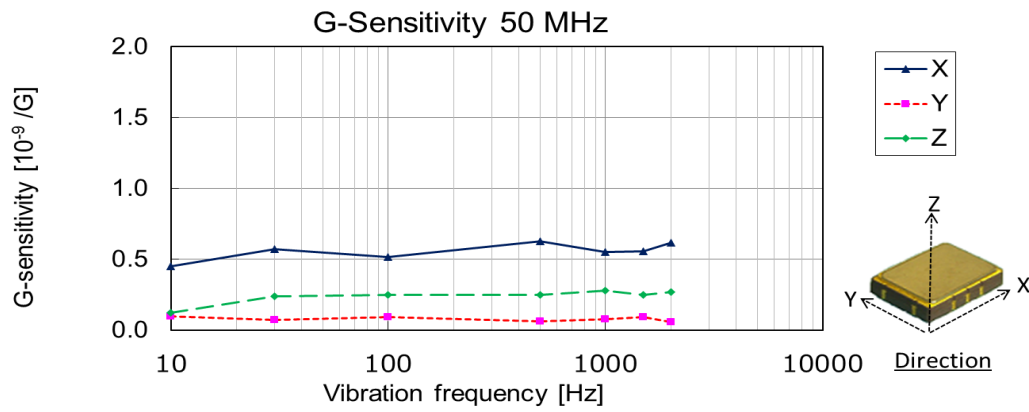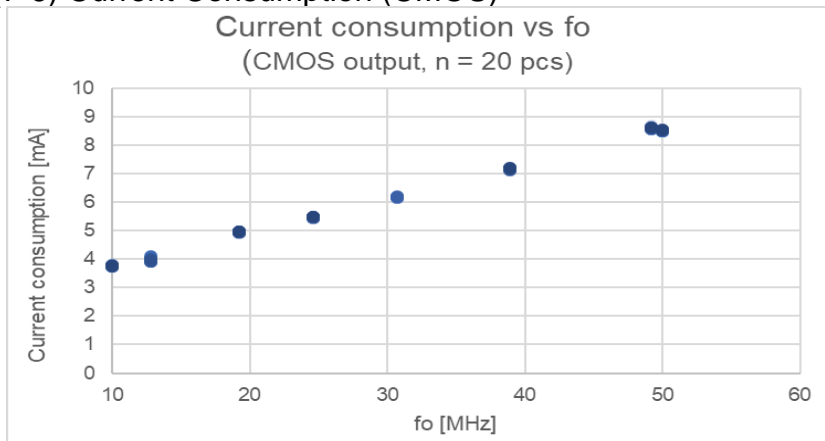50 MHz Accelerated Aging Test [n = 20 pcs]
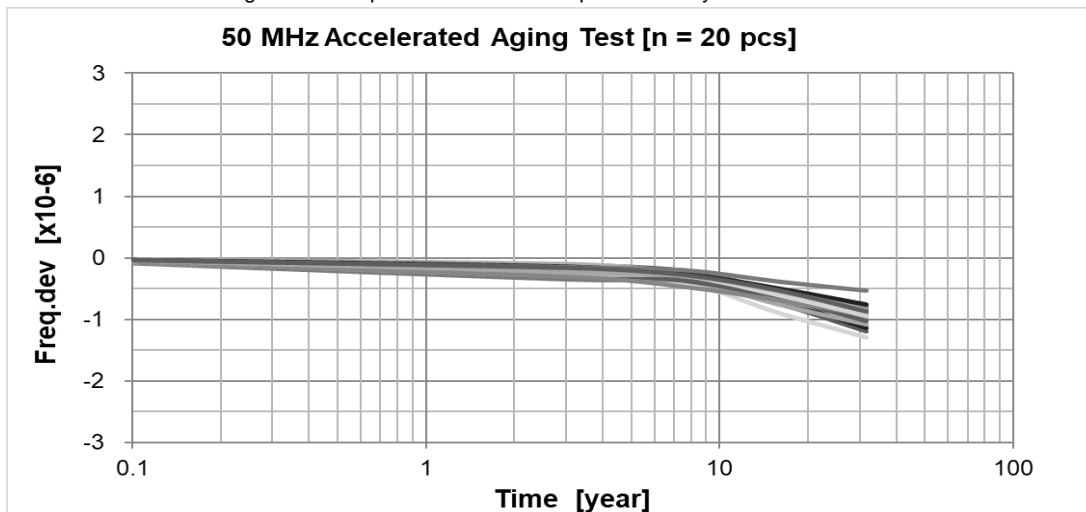
(7-4) Holdover

1) After 10 days

Holdover Stability T_use = +25 ºC
(After 10 days operating, n = 10 pcs)

2) After 48 hours
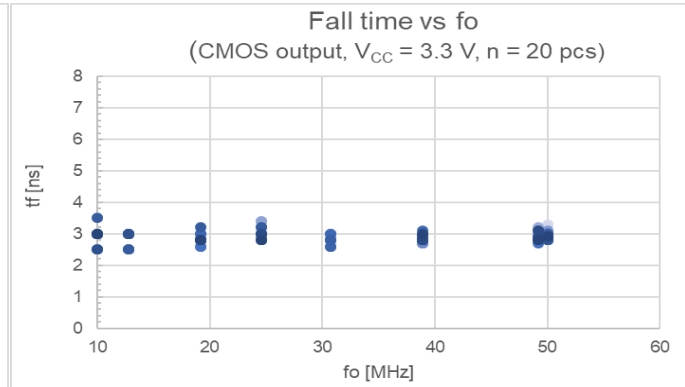

Holdover Stability T_use = +25 ºC
(After 48 hours operating, n = 10 pcs)

(7-5) Vibration Sensitivity

G-Sensitivity 50 MHz

Legend: X, Y, Z

G-sensitivity $[10^{-9}$ /G] vs Vibration frequency [Hz]

Direction (Z, X, Y axes shown)

(7-6) Current Consumption (CMOS)

Current consumption vs fo
(CMOS output, n = 20 pcs)

Current consumption [mA] vs fo [MHz]

(7-7) Rise Time / Fall Time (CMOS)

Rise time vs fo
(CMOS output, V_{CC} = 3.3 V, n = 20 pcs)

tr [ns] vs fo [MHz]

Fall time vs fo
(CMOS output, V_{CC} = 3.3 V, n = 20 pcs)

tf [ns] vs fo [MHz]
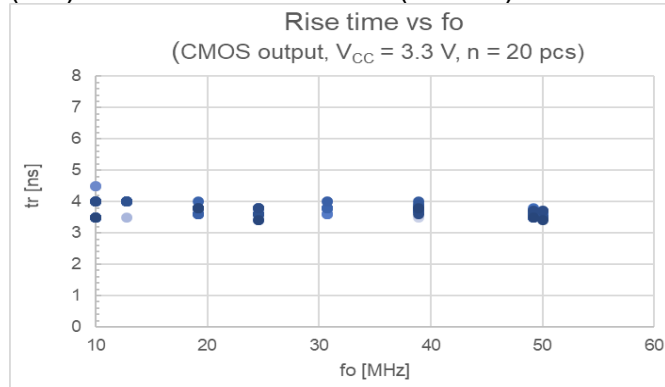
(7-8) Output Voltage [V_{OH}, V_{OL}] (CMOS)

Output voltage High level vs fo
(CMOS output, V_{CC} = 3.3 V, n = 20 pcs)

VOH [V] vs fo [MHz]

Output voltage Low level vs fo
(CMOS output, V_{CC} = 3.3 V, n = 20 pcs)

VOL [V] vs fo [MHz]

(7-9) Symmetry (CMOS)
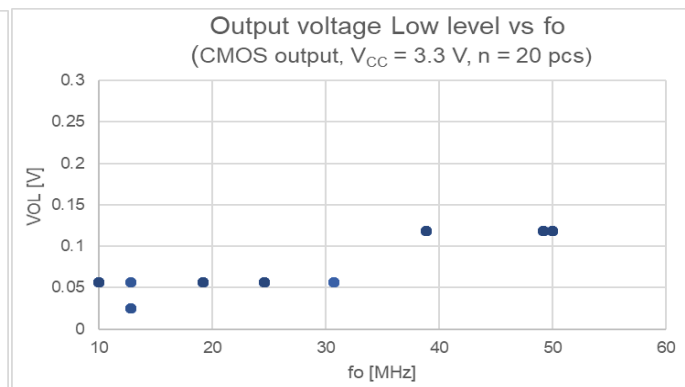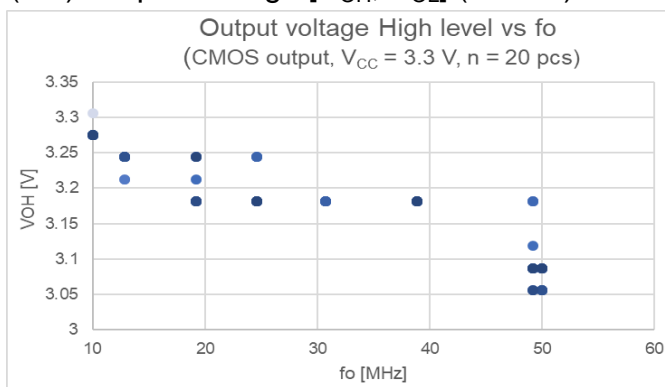
Symmetry vs fo
(CMOS output, V_{CC} = 3.3 V, n = 20 pcs)
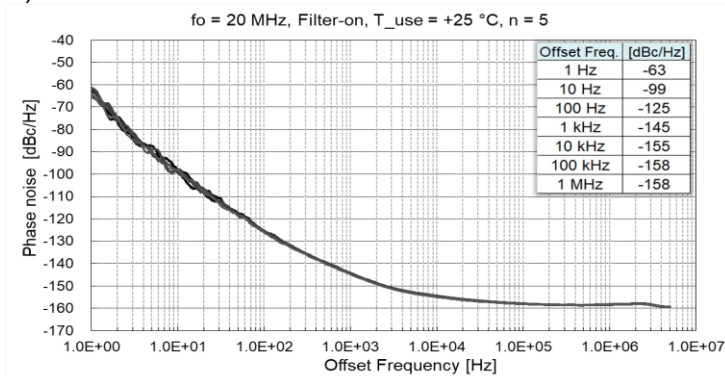

(7-10) Phase Noise * Filter-ON requires the option setting of Filter-ON and the connection of the phase noise filter capacitor 0.1 µF (recommended) to Pin7.

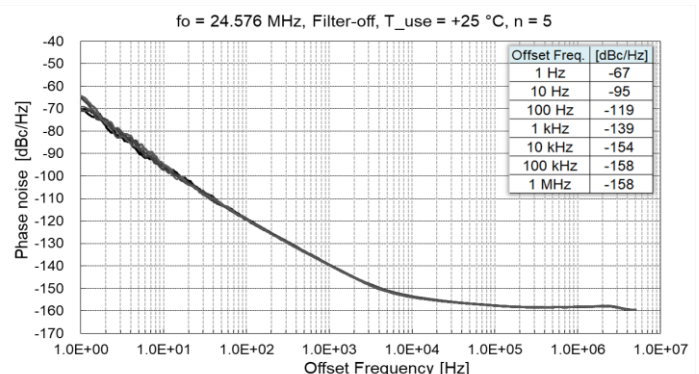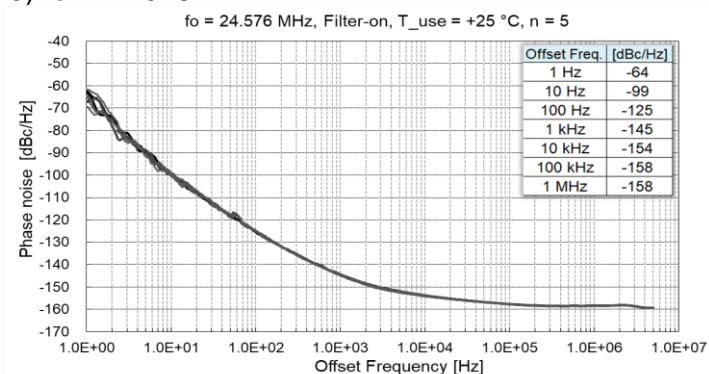### 1) fo = 19.2 MHz

fo = 19.2 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-67
10 Hz	-100
100 Hz	-126
1 kHz	-145
10 kHz	-155
100 kHz	-158
1 MHz	-158

fo = 19.2 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-67
10 Hz	-96
100 Hz	-120
1 kHz	-140
10 kHz	-154
100 kHz	-158
1 MHz	-158

2) fo = 20 MHz

fo = 20 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-63
10 Hz	-99
100 Hz	-125
1 kHz	-145
10 kHz	-155
100 kHz	-158
1 MHz	-158

3) fo = 24.576 MHz

fo = 24.576 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-64
10 Hz	-99
100 Hz	-125
1 kHz	-145
10 kHz	-154
100 kHz	-158
1 MHz	-158

fo = 24.576 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-67
10 Hz	-95
100 Hz	-119
1 kHz	-139
10 kHz	-154
100 kHz	-158
1 MHz	-158

(7-10) Phase Noise * Filter-ON requires the option setting of Filter-ON and the connection of the phase noise filter capacitor 0.1 μF (recommended) to Pin7.

4) fo = 25 MHz

fo = 25 MHz, Filter-on, T_use = +25 °C, n = 5
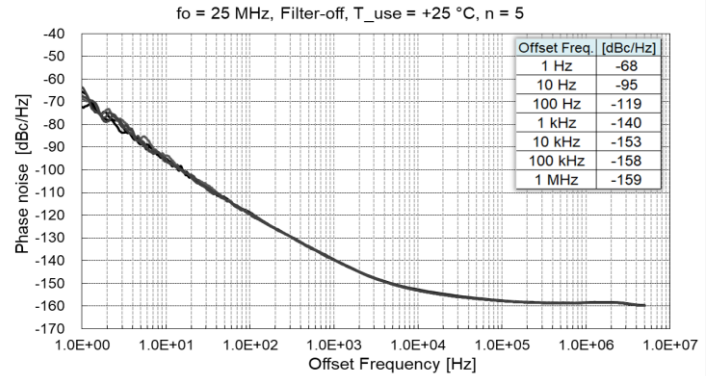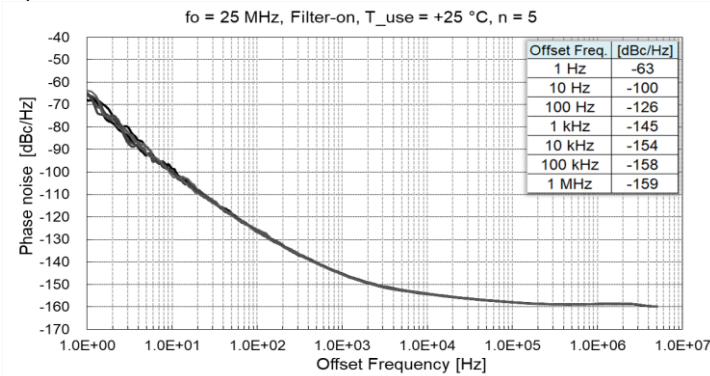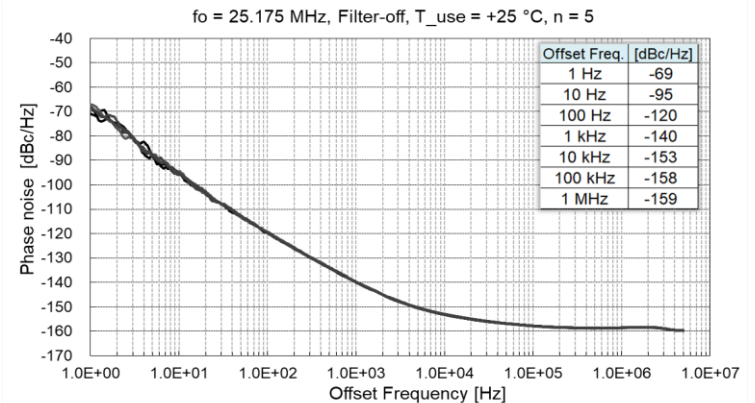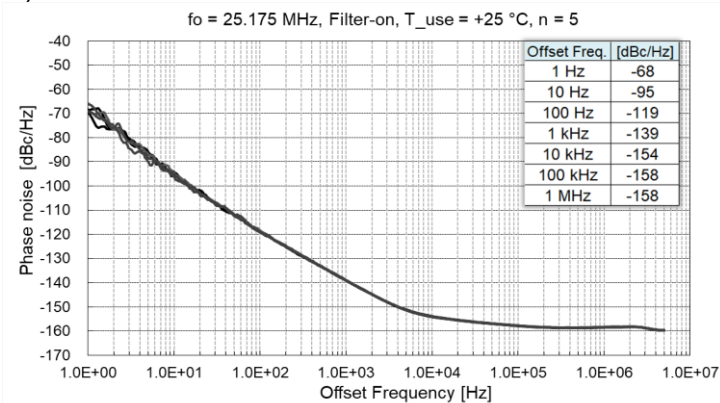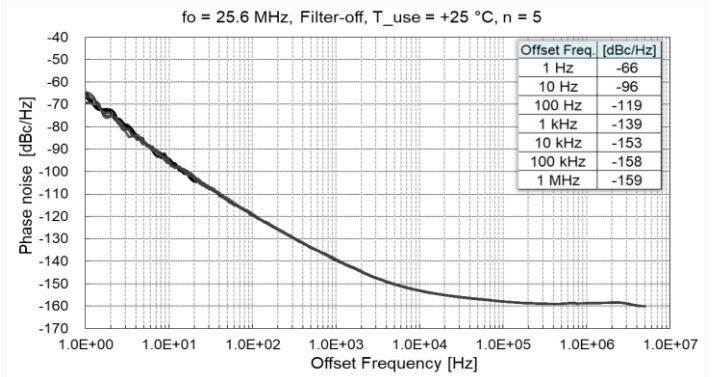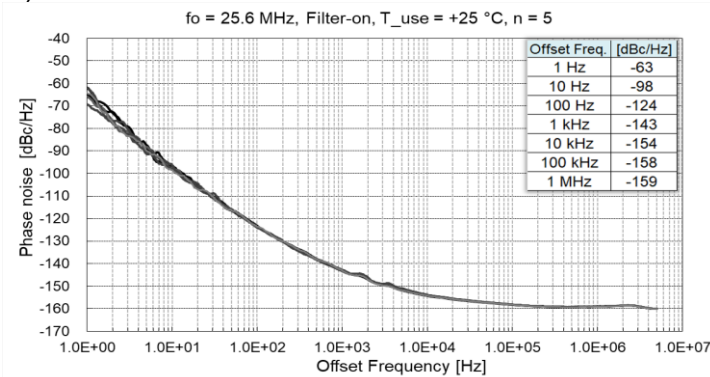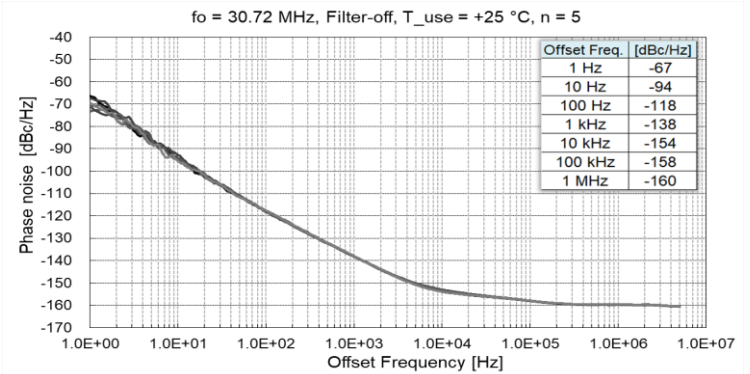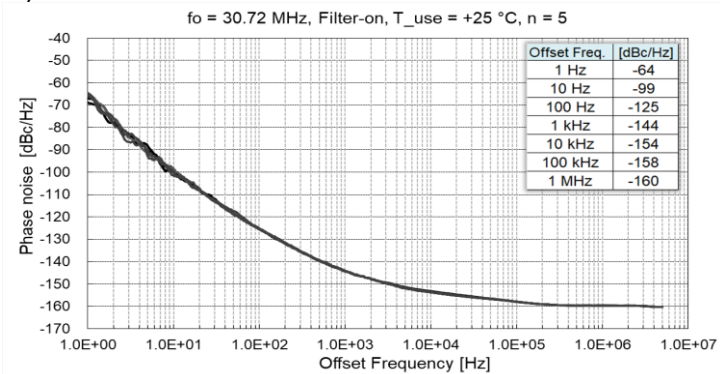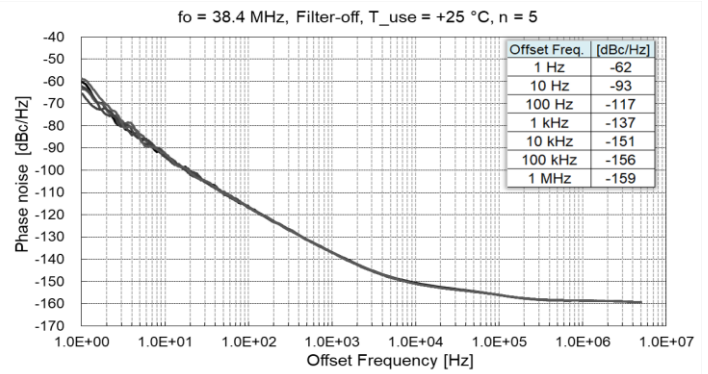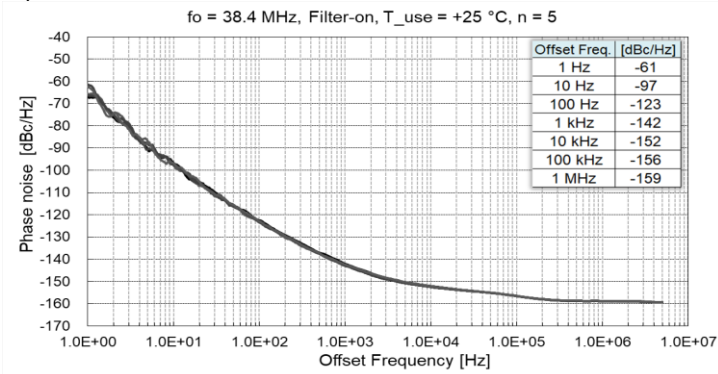
Offset Freq.	[dBc/Hz]
1 Hz	-63
10 Hz	-100
100 Hz	-126
1 kHz	-145
10 kHz	-154
100 kHz	-158
1 MHz	-159

fo = 25 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-68
10 Hz	-95
100 Hz	-119
1 kHz	-140
10 kHz	-153
100 kHz	-158
1 MHz	-159

5) fo = 25.175 MHz

fo = 25.175 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-68
10 Hz	-95
100 Hz	-119
1 kHz	-139
10 kHz	-154
100 kHz	-158
1 MHz	-158

fo = 25.175 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-69
10 Hz	-95
100 Hz	-120
1 kHz	-140
10 kHz	-153
100 kHz	-158
1 MHz	-159

6) fo = 25.6 MHz

fo = 25.6 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-63
10 Hz	-98
100 Hz	-124
1 kHz	-143
10 kHz	-154
100 kHz	-158
1 MHz	-159

fo = 25.6 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-66
10 Hz	-96
100 Hz	-119
1 kHz	-139
10 kHz	-153
100 kHz	-158
1 MHz	-159

7) fo = 30.72 MHz

fo = 30.72 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-64
10 Hz	-99
100 Hz	-125
1 kHz	-144
10 kHz	-154
100 kHz	-158
1 MHz	-160

fo = 30.72 MHz, Filter-off, T_use = +25 °C, n = 5

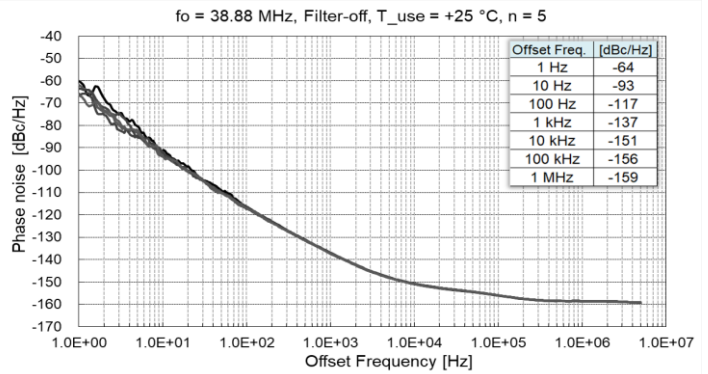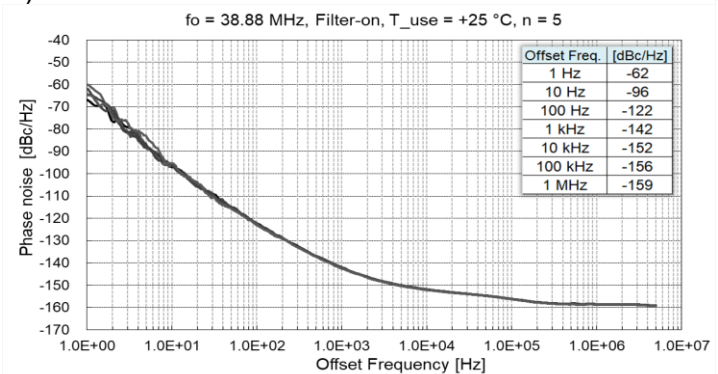| Offset Freq.	[dBc/Hz]
1 Hz	-67
10 Hz	-94
100 Hz	-118
1 kHz	-138
10 kHz	-154
100 kHz	-158
1 MHz	-160

(7-10) Phase Noise * Filter-ON requires the option setting of Filter-ON and the connection of the phase noise filter capacitor 0.1 µF (recommended) to Pin7.
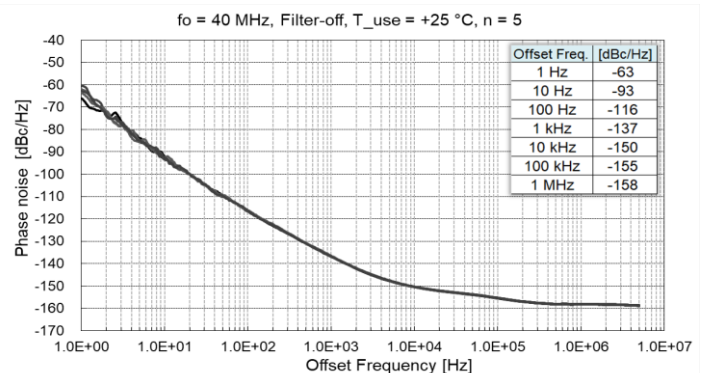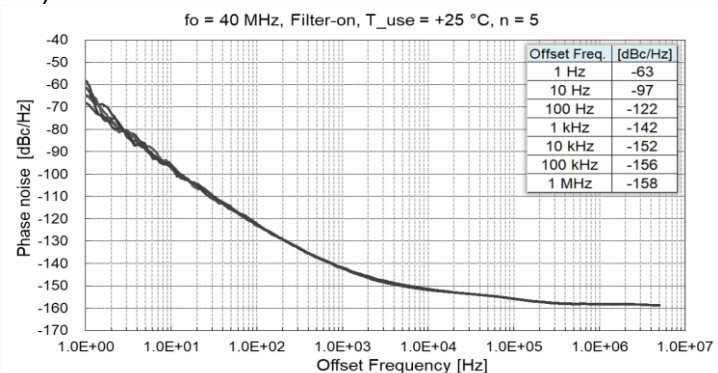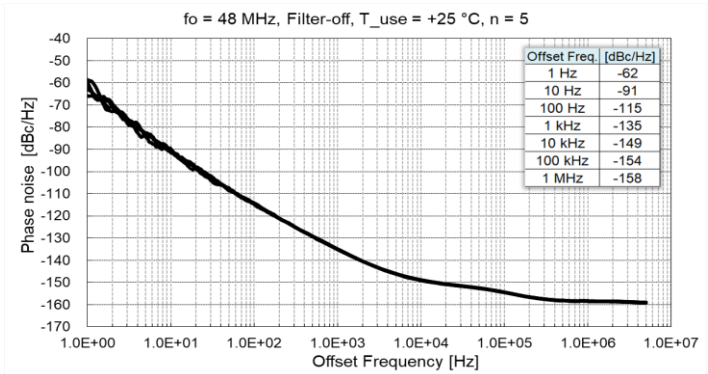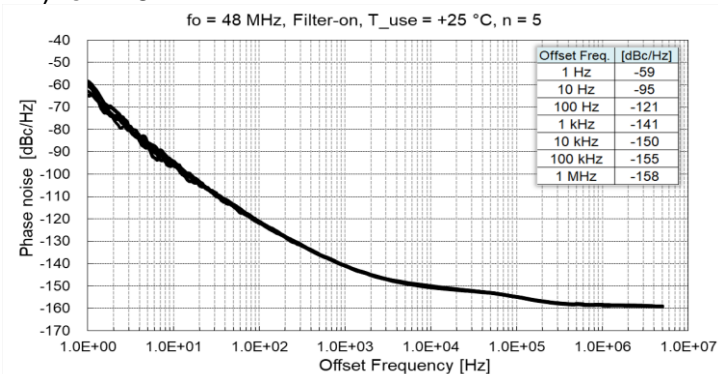
8) fo = 38.4 MHz

fo = 38.4 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-61
10 Hz	-97
100 Hz	-123
1 kHz	-142
10 kHz	-152
100 kHz	-156
1 MHz	-159

fo = 38.4 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-62
10 Hz	-93
100 Hz	-117
1 kHz	-137
10 kHz	-151
100 kHz	-156
1 MHz	-159

9) fo = 38.88 MHz

fo = 38.88 MHz, Filter-on, T_use = +25 °C, n = 5

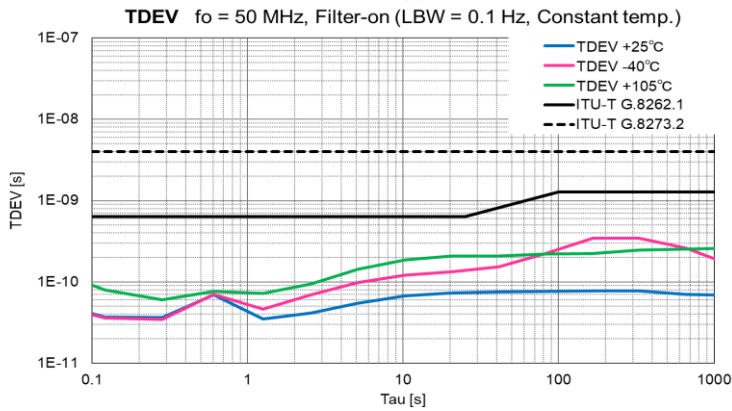| Offset Freq.	[dBc/Hz]
1 Hz	-62
10 Hz	-96
100 Hz	-122
1 kHz	-142
10 kHz	-152
100 kHz	-156
1 MHz	-159

fo = 38.88 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-64
10 Hz	-93
100 Hz	-117
1 kHz	-137
10 kHz	-151
100 kHz	-156
1 MHz	-159

10) fo = 40 MHz

fo = 40 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-63
10 Hz	-97
100 Hz	-122
1 kHz	-142
10 kHz	-152
100 kHz	-156
1 MHz	-158

fo = 40 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-63
10 Hz	-93
100 Hz	-116
1 kHz	-137
10 kHz	-150
100 kHz	-155
1 MHz	-158

11) fo = 48 MHz

fo = 48 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-59
10 Hz	-95
100 Hz	-121
1 kHz	-141
10 kHz	-150
100 kHz	-155
1 MHz	-158

fo = 48 MHz, Filter-off, T_use = +25 °C, n = 5

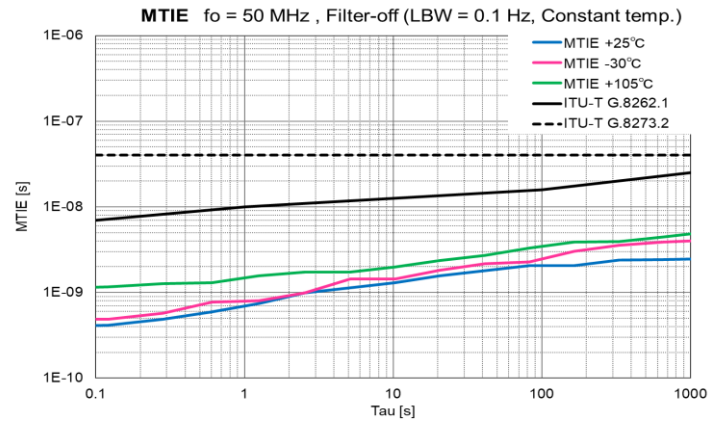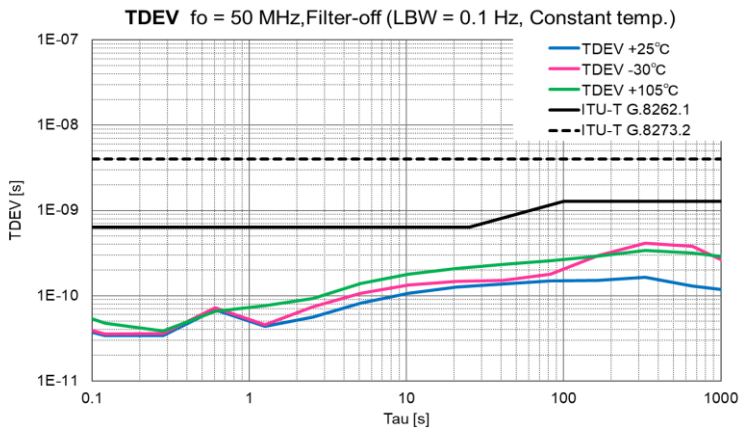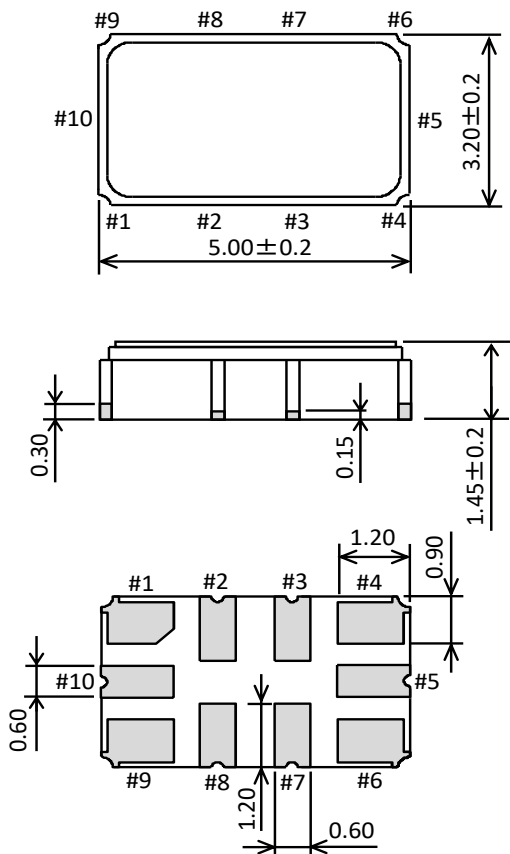| Offset Freq.	[dBc/Hz]
1 Hz	-62
10 Hz	-91
100 Hz	-115
1 kHz	-135
10 kHz	-149
100 kHz	-154
1 MHz	-158

(7-10) Phase Noise * Filter-ON requires the option setting of Filter-ON and the connection of the phase noise filter capacitor 0.1 µF (recommended) to Pin7.

12) fo = 49.152 MHz

fo = 49.152 MHz, Filter-on, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-61
10 Hz	-95
100 Hz	-121
1 kHz	-140
10 kHz	-150
100 kHz	-155
1 MHz	-158

fo = 49.152 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-63
10 Hz	-90
100 Hz	-114
1 kHz	-134
10 kHz	-149
100 kHz	-154
1 MHz	-158

13) fo = 50 MHz

fo = 50 MHz, Filter-on, T_use = +25 °C, n = 4

Offset Freq.	[dBc/Hz]
1 Hz	-59
10 Hz	-95
100 Hz	-121
1 kHz	-140
10 kHz	-150
100 kHz	-154
1 MHz	-158

fo = 50 MHz, Filter-off, T_use = +25 °C, n = 5

Offset Freq.	[dBc/Hz]
1 Hz	-61
10 Hz	-89
100 Hz	-113
1 kHz	-134
10 kHz	-149
100 kHz	-154
1 MHz	-158

(7-11) Short Term Stability

Short term stability fo = 19.2 MHz, T_use = +25 °C, Filter-on
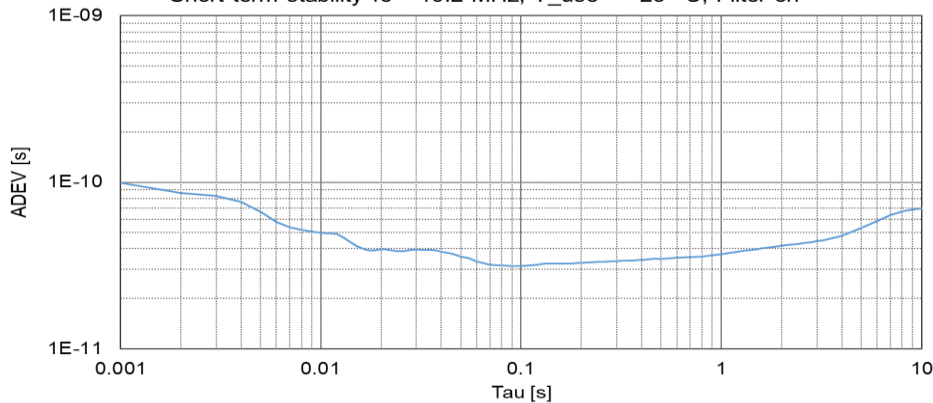
Short term stability fo = 19.2 MHz, T_use = +25 °C, Filter-off

(7-12) Wander

TDEV fo = 50 MHz,Filter-off (LBW = 0.1 Hz, Constant temp.)

- TDEV +25℃
- TDEV -30℃
- TDEV +105℃
- ITU-T G.8262.1
- ITU-T G.8273.2

MTIE fo = 50 MHz , Filter-off (LBW = 0.1 Hz, Constant temp.)

- MTIE +25℃
- MTIE -30℃
- MTIE +105℃
- ITU-T G.8262.1
- ITU-T G.8273.2

TDEV fo = 50 MHz, Filter-on (LBW = 0.1 Hz, Constant temp.)

- TDEV +25℃
- TDEV -40℃
- TDEV +105℃
- ITU-T G.8262.1
- ITU-T G.8273.2

MTIE fo = 50 MH, Filter-on (LBW = 0.1 Hz, Constant temp.)

- MTIE +25℃
- MTIE -40℃
- MTIE +105℃
- ITU-T G.8262.1
- ITU-T G.8273.2
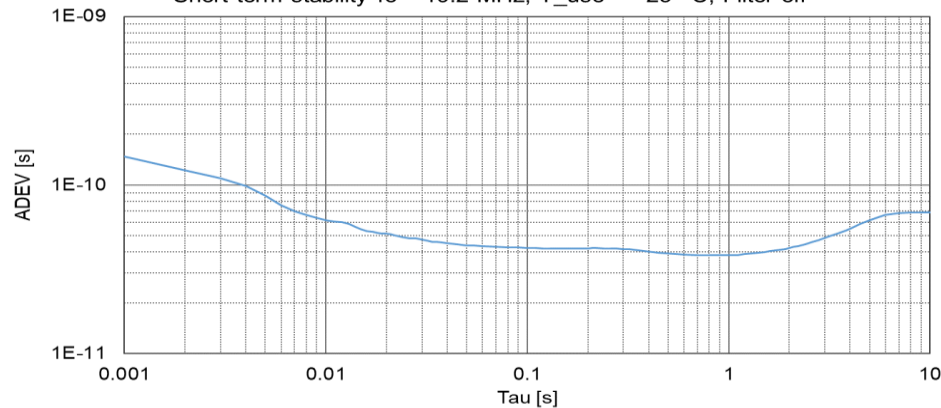
[8] Outline Drawing   unit: mm

Terminal plating: Au

Marking

[9] Recommended Foot Print   unit: mm

(TOP View)
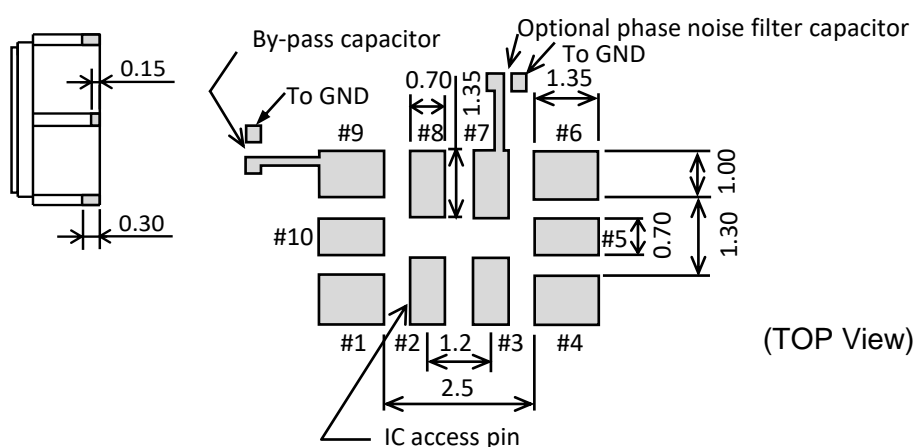
Pin #	Connection
1	N.C.
2	N.C.
3	OE
4	GND
5	N.C.
6	OUT
7	N.C. or Filter
8	N.C.
9	V_{CC}
10	N.C.

Do not connect pins marked as "N.C." with
to any other pin including those marked "N.C."

For proper operation, connect a 0.1 µF
by-pass capacitor between VCC to GND as near as
possible to the power source terminal of the oscillator.

For optional phase noise Filter-ON, connect a 0.1 µF
capacitor between Filter (pin 7) and GND.

If the OE Function is not used, we recommend connecting
OE (pin 3) to V_{CC} (pin 9) or this TCXO can also operate with
with pin 3, OE, as N.C.
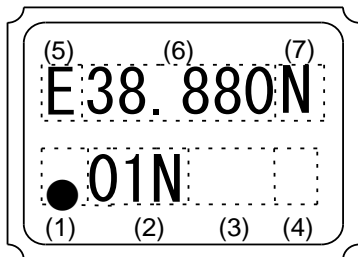
(1) Pin 1 identifier
(2) TCXO model ID [01N]
(3) TCXO Lot No. (3 digits)
(4) Location code
(5) Epson logo Mark [E]
(6) Frequency data
(7) TG-5510CB code: N

[9] Moisture Sensitivity Level and Electro-Static Discharge

(1)Moisture Sensitivity Level (MSL)

Parameter	Specifications	Conditions
MSL	LEVEL1	JEDEC J-STD-020D

(2)Electro-Static Discharge (ESD)

Parameter	Specifications	Conditions
Human Body Model (HBM)	2,000 V Min.	IEC 60749-26 Ed.2.0:2006(b), 100 pF, 1.5 kΩ, 3 times
Machine Model (MM)	200 V Min.	IEC 60749-27 Ed.2.0:2006(b), 200 pF, 0 Ω, 1 time

[10] Reflow Profile (follow to IPC / JEDEC J-STD-020D.1)

Temperature [°C]

TP   ; +260 °C
        +255 °C
TL   ; +217 °C
Ts max ; +200 °C
Ts min ; +150 °C

Avg. Ramp-up
3 °C / s Max.

tL
60 s to 150 s
(+217 °C over)

tp ; 20 s to 40 s

Ramp-down
6 °C / s Max.

ts
60 s to 180 s
(+150 °C to +200 °C)

Time +25 °C to Peak

Time [s]

No,: TG-5510CB_rev1.0
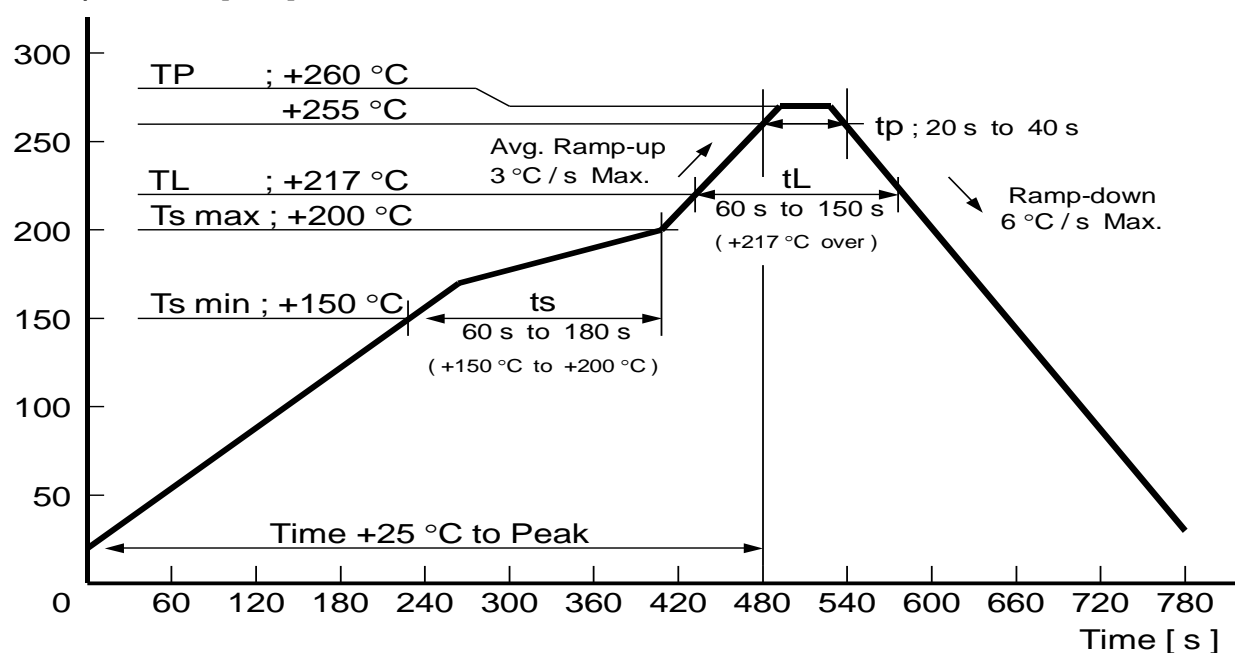
[11] Packing Information
 (1) Quantity
     For this product series, the standard for the last two digits of the product number is "14", 1,000 pcs/Reel.

     TG-5510CB: X1G006061xxxx14

 (2) Taping Specification

                         Subject to EIA-481, IEC-60286 and JIS C0806
                         1) Tape dimensions
                         Carrier Tape Material: PS (Polystyrene)
                         Top Tape Material: PET (Polyethylene Terephthalate) + PE (Polyethylene)
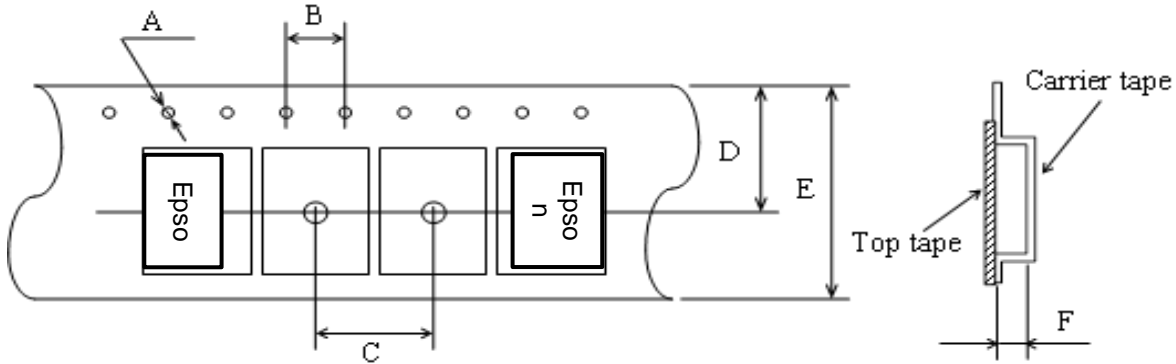
Symbol	A	B	C	D	E	F
Value	φ1.5	4.0	8.0	7.25	12.0	1.7

                         2) Reel dimensions
                         Center Material: PS (Polystyrene)
                         Reel Material: PS (Polystyrene)
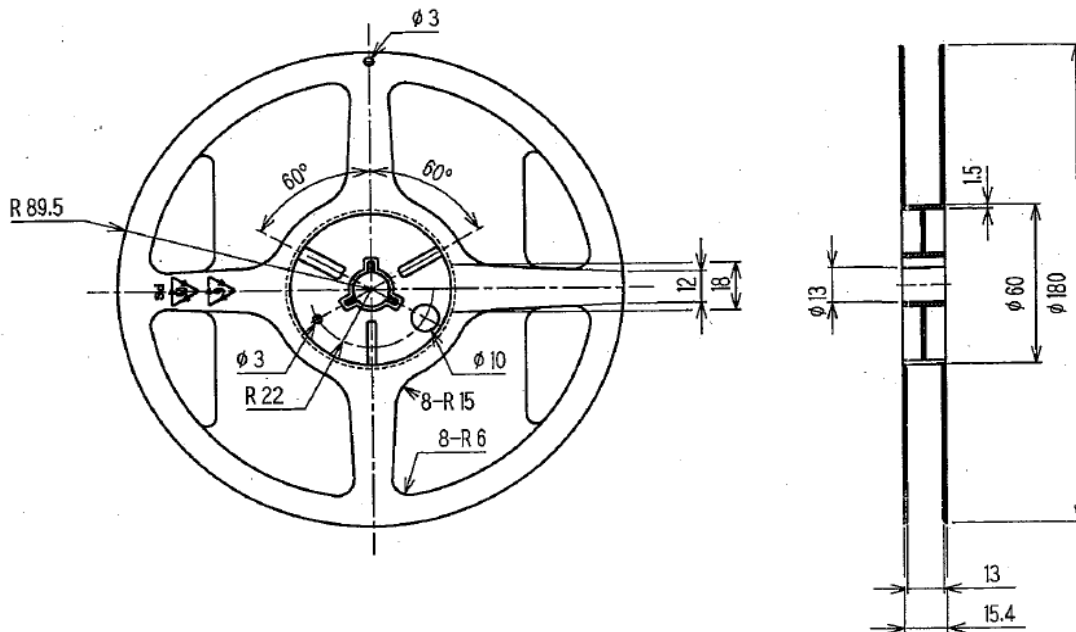
[12] Handling Precautions

Prior to using this product, please carefully read the section entitled "Precautions" on our website
(https://www5.epsondevice.com/en/information/#precaution) for instructions on how to handle and use
the product properly to ensure optimal performance in your equipment.
Before using the product under any conditions other than those specified therein,
please consult with us to verify and confirm that the performance of the product will not be negatively
affected by use under such conditions.

In addition to the foregoing precautions, in order to avoid the deteriorating performance of the product,
we strongly recommend that you DO NOT use the product under ANY of the following conditions:

(1) Do not expose this product to excessive mechanical shock or vibration.
(2) This product can be damaged by mechanical shock during the soldering process depending on the equipment
   used, process conditions, and any impact forces experienced. Always follow appropriate procedures,
   particularly when changing the assembly process in any way and be sure to follow applicable process
   qualification standards before starting production.
(3) These devices are sensitive to ESD, use appropriate precautions during handling, assembly,
   test, shipment, and installation.
(4) This product contains semiconductor content that should not be exposed to electromagnetic waves.
(5) The use of ultrasonic technology for cleaning, bonding, etc. can damage the Xtal unit inside this product.
   Please carefully check this consideration before using ultrasonic equipment for volume production with this product.
(6) Noise and ripple on the power supply may have undesirable affects on operation and cause degradation
   of phase noise characteristics. Evaluate the operation of this device with appropriate power supplies carefully before use.
(7) When applying power, ensure that the supply voltage increases monotonically for proper operation.
   On power down, do not reapply power until the supplies, bypass capacitors, and any bulk capacitors
   are completely discharged since that may cause the unit to malfunction.
(8) Aging specifications are estimated from environmental reliability tests and expected frequency variation
   over time. They do not provide a guarantee of aging over the product lifecycle.
(9) The metal cap on top of the device is directly connected to the GND terminal (pin #2). Take necessary
   precautions to prevent any conductor not at ground potential from contacting the cap as that could cause a short circuit to GND.
(10) Do not route any signal lines, supply voltage lines, or GND lines underneath the area where the oscillators
   are mounted including any internal layers and on the opposite side of the PCB.
   To avoid any issues due to interference of other signal lines, please take care not to place signal lines
   near the product as this may have an adverse affect on the performance of the product.
(11) A bypass capacitor of the recommended value(s) must be connected between the V_{CC} and GND terminals
   of the product. Whenever possible, mount the capacitor(s) on the same side of the PCB and as close to the
   product as possible to keep the routing traces short.
(12) Power supply connections to V_{CC} and GND pins should be routed as thick as possible while keeping the
   high frequency impedance low in order to get the best performance.
(13) The use of a filter or similar element in series with the power supply connections to protect from
   electromagnetic radiation noise may increase the high frequency impedance of the power supply line
   and may cause the oscillator to not operate properly. Please verify the design to ensure sufficient
   operational margin prior to use.
(14) Keep PCB routing from the output terminal(s) to the load as short as possible for best performance.
(15) Do not short the output to GND as that will damage the product. Always use with an appropriate
   load resistor connected.
(16) Product failures during the warranty period only apply when the product is used according to the
   recommended operating conditions described in the specifications. Products that have been opened
   for analysis or damaged will not be covered. It is recommended to store and use in the normal temperature
   and humidity environments described in the specifications to ensure frequency accuracy and prevent
   moisture condensation. If the product is stored for more than one year, please confirm the pin solderability prior to use.
(17) If the oscillation circuit is exposed to condensation, the frequency may change or oscillation may stop.
   Do not use in any conditions where condensation occurs.
(18) Do not store or use the product in an environment where it can be exposed to chemical substances
   that are corrosive to metal or plastics such as salt water, organic solvents, chemical gasses, etc.
   Do not use the product when it is exposed to sunlight, dust, corrosive gasses, or other materials for
   long periods of time.
(19) When using water-soluble solder flux make sure to completely remove the flux residue after soldering.
   Pay particular attention when the residues contain active halogens which will negatively affect the
   product and its performance.

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM
CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

> ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major manufacturers as standard.

> IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

■ Explanation of marks used in this datasheet

Pb Free	●**Pb free.**
RoHS Compliant	●**Complies with EU RoHS directive.** *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive (Contains Pb in sealing glass, high melting temperature type solder or other)