Temperature Compensated Crystal Oscillator (TCXO)

- Package size (7.0 mm × 5.0 mm × 1.5 mm)
- · Ultra high stability TCXO
- · Output waveform : CMOS
- [1] Product Number / Product Name
- (1-1) Product Number / Ordering Code

X1G0060710090xx

last 2 digits code(xx) define Quantity.

The standard is "14", 1 000 pcs/Reel.

(1-2) Product Name / Model Name

TG-5511CB-90N 25.000000 MHz

[2] Operating range

] = per agg						
Parameter	Symbol	•,	Specification	s	Unit	Conditions
	Symbol	Min.	Тур.	Max.		
Supply voltage	V_{CC}	3.135	3.3	3.465	V	Vcc = 3.3 V ± 5 %
Supply voltage	GND	0	-	0	V	-
Operating temperature range	T_use	-40	-	+85	°C	-
Output load	Load_C	13.5	15	16.5	pF	-

[3] Frequency characteristics

(Vcc = 3.3 V, GND = 0.0 V, Load = 15 pF, T_use = +25 °C)

1 3 11 requericy characteris		(VCC = 3.5 V, GND = 0.0 V, Load = 15 pr, 1_dse = +25 C)				
Parameter	Symbol		Specifications		Unit	Conditions
Faianietei	Symbol	Min.	Тур.	Max.	Offic	Conditions
Output Frequency	fo	-	25	-	MHz	-
Frequency tolerance *1	f_tol	-1.0	-	+1.0	x10 ⁻⁶	T_use = +25 °C ±2 °C After 2 reflows *2
Frequency / temperature characteristics	fo-Tc	-0.28	-	+0.28	x10 ⁻⁶	T_use = -40 °C to +85 °C (Reference to (fmax+fmin)/2)
Frequency / load coefficient	fo-Load	-0.1	-	+0.1	x10 ⁻⁶	Load ±10 %
Frequency / voltage coefficient	fo-V _{CC}	-0.1	-	+0.1	x10 ⁻⁶	Vcc ±5 %
Fraguency aging for	f_age	-0.5	-	+0.5	x10 ⁻⁶	T_use = +25 °C first year
Frequency aging	i_aye	-3.0	-	+3.0	х10 ⁻⁶	T_use = +25 °C 20 years
Holdover stability (Free-run accuracy)	-	-4.6	-	+4.6	x10 ⁻⁶	*3

^{*1} Include initial frequency tolerance and frequency deviation after reflow cycles.

[4] Electrical characteristics

(Vcc = 3.3 V, GND = 0.0 V, Load = 15 pF, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
1 didilietei	Symbol	Min.	Тур.	Max.	Offic	Conditions
Current consumption	I _{CC}	-	-	7.0	mA	-
Output voltage	Voн	90 %	-	-	V	-
Output voltage	Vol	ı	-	10 %	V	-
Rise time	tr	1	-	8.0	ns	10 % Vcc to 90 % Vcc level
Fall time	tf	1	-	8.0	ns	90 % Vcc to 10 % Vcc level
Start-up time	t_str	ı	-	5.0	ms	t = 0 % at 90 % Vcc
Symmetry	SYM	45	-	55	%	50 % Vcc Level

[For other general specifications, please refer to the attached Full Data Sheet below]

1/20 Page No.: TG-5511CA ver1.0

^{*2} Measured in the elapse of 24 hours after reflow soldering.

^{*3} This includes initial frequency tolerance, frequency / temperature characteristics, frequency / load coefficient, frequency/voltage coefficient and frequency aging (+25 °C, 20 years).

High Stability Temperature Compensated Crystal Oscillator (TCXO) Supports $\pm 0.28 \times 10^{-6}$ over -40 °C to +85 °C

TG-5511CB

Key Specifications

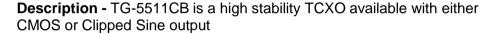
Frequency Range: 10 MHz to 54 MHz

Supply Voltage: 3.3 V Typ.

Operation Temperature: -40 °C to +85 °C (+105 °C option) Freq. / Temp characteristic: $\pm 0.28 \times 10^{-6}$ Max. over temperature $\pm 0.2 \times 10^{-6}$ / °C Max. over temperature

Frequency Aging: $\pm 3.0 \times 10^{-6}$ Max. / 20 years (meets Stratum3)

Package: $5.0 \times 3.2 \times 1.45$ mm (4 pins) With optional on-chip filter capacitor for lower phase noise



- Specified to +85 °C for 5G Base Stations and Edge Computing needs outdoor installation, miniaturization, and fan-less operation
- Compared to other TCXOs it offers a variety of improvements such as low temperature slope and phase noise
- Complies with GR-1244-CORE Stratum3 and G.8262.1, G.8273.2 (Class A, B)
- Note: this product does not have a voltage control (Vc) function





TG-5511CB (4 pins)

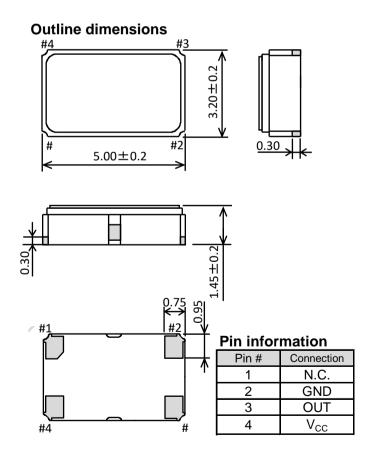
Applications

Network equipment

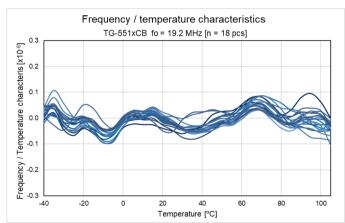
- Base station
- Microwave

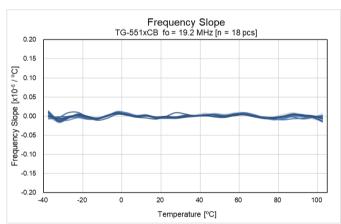
Sync compliance standards

- Stratum3
- SyncE
- IEEE1588



Characteristics





[1] Product Number / Product Name

(1) Product Name

TG-5511CB - *** 38.880000MHz

(1)

2 3

4)

①Model ②Package type (5.0 x 3.2 x 1.45 mm) ③Spec segment (please contact Epson) ④Frequency Selectable specs.: Output (CMOS, Clipped sine), Frequency/temperature (±0.28 x 10⁻⁶ Max., ±0.25 x 10⁻⁶ Max.) Oparating tenterature (-40 °C to +85 °C, -40 °C to +105 °C), OE function (Non), Filter function (NO-Filter)

(2) Product Number / Ordering Code

TG-5511CB: X1G006071xxxx14

The standard product name & number are as follows:

Product name	Freq. [MHz]	Product number	Selectable specs
TG-5511CB-98N	10.000	X1G006071009814	
TG-5511CB-94N	12.800	X1G006071009414	Output: CMOS
TG-5511CB-81N	19.200	X1G006071008114	Frequency/temperature: ±0.28 x 10 ⁻⁶ Max.
TG-5511CB-91N	19.440	X1G006071xxxx14	Oparating tenterature: -40 °C to <u>+85 °C</u>
TG-5511CB-95N	20.000	X1G006071009514	Filter function: NO-Filter
TG-5511CB-79N	24.000	X1G006071xxxx14	
TG-5511CB-77N	24.576	X1G006071007714	
TG-5511CB-90N	25.000	X1G006071009014	
TG-5511CB-72N	25.175	X1G006071007214	
TG-5511CB-92N	25.600	X1G006071009214	
TG-5511CB-87N	26.000	X1G006071xxxx14	
TG-5511CB-76N	30.720	X1G006071007614	
TG-5511CB-3:2N	23.403 33.885	X1C:005071308214 X1CC0C71509014	son for detail
TG-5511CB-96N	33.883	X10000071009014	Sull ful uctail
TG-5511CB-93N	40.000	X1G006071009314	l usa h a r
TG-5511CB-80N	48.000)(1G)(6)71x(xx14	umber
TG-5511CB-78N	49.152	X1G006071007814	
TG-5511CB-97N	50.000	X1G006071009714	
TG-5511CB-73N	52.000	X1G006071xxxx14	
TG-5511CB-74N	54.000	X1G006071xxxx14	
TG-5511CB-41N	10.000	X1G006071004114	
TG-5511CB-42N	19.200	X1G006071004214	
TG-5511CB-43N	20.000	X1G006071004314	Output: CMOS
TG-5511CB-44N	24.576	X1G006071004414	Frequency/temperature: ±0.28 × 10 ⁻⁶ Max.
TG-5511CB-45N	25.000	X1G006071004514	Oparating tenterature: -40 °C to +105 °C
TG-5511CB-46N	38.880	X1G006071004614	Filter function: NO-Filter
TG-5511CB-47N	40.000	X1G006071004714	
TG-5511CB-48N	50.000	X1G006071004814	

^{*} Please contact Epson for detail of product number

Please contact Epson for the other frequencies and specifications.

[2] Absolute Maximum Ratings

Parameter Symbol	,	Specification	S	Unit	Conditions	
Falailletei	Symbol	Min.	Тур.	Max.	Offic	Cortaitions
Supply voltage	V _{CC} -GND	-0.5	-	+4.0	V	
Storage temperature range	T_stg	-40	-	+105	°C	Storage as single product

[3] Operating Conditions

Parameter	Symbol	Specifications			Unit	Conditions
Farameter	Syllibol	Min.	Тур.	Max.		
Supply voltage	V_{CC}	3.135	3.3	3.465	V	$V_{CC} = 3.3 \text{ V} \pm 5 \%$
Supply voltage	GND	0	-	0	V	
Operating temperature range	T_use	-40	+25	+85	°C	105 °C Max. (Option)
CMOS load condition	L_CMOS	13.5	15	16.5	pF	
Output load (Clipped sine)	Load_R	9	10	11	kΩ	
Output load (Clipped sine)	Load_C	9	10	11	pF	
Output load (Clipped sine)	Сс	0.01	-	-	μF	AC coupling capacitor *

^{*} AC coupling capacitor is not included in this TCXO, please attach an external AC coupling capacitor to the OUT pin.

[4] Frequency Characteristics

 $(V_{CC} = 3.3 \text{ V}, \text{GND} = 0.0 \text{ V}, \text{T_use} = +25 ^{\circ}\text{C})$

					(*66 – 6.6 *	$\frac{1}{100} = 0.0 \text{ V}, 1_{0.00} = +25 \text{ C}$
Parameter	Symbol	Specifications			Unit	Conditions
Parameter	Syllibol	Min.	Тур.	Max.	Offic	Conditions
		10	-	54		
Output Frequency	fo		2, 20, 24.576, 38.4, 38.88, 4		MHz	Standard frequency
Frequency tolerance *1	f_tol	-1.0	ı	+1.0	x10 ⁻⁶	T_use = +25 °C ± 2 °C After 3 reflows *2
Frequency / temperature characteristics	fo-Tc	-0.28	-	+0.28	x10 ⁻⁶	T_use = -40 °C to +85 °C, Reference to (fmax+fmin) / 2
		(-0.25)	-	(+0.25)		(Option)
Frequency slope	f_slp	-0.2	-	+0.2	x10 ⁻⁶ / °C	T_use = -40 °C to +85 °C, One frequency reading every 5 °C Min.
		(-0.05)	-	(+0.05)		(Option)
Frequency / load coefficient	fo-Load	-0.1	1	+0.1	x10 ⁻⁶	Load_R // Load_C ± 10 % L_CMOS ± 10 %
Frequency / voltage coefficient	fo-V _{CC}	-0.1	-	+0.1	x10 ⁻⁶	$V_{CC} = 3.3 \text{ V} \pm 5 \%$
Frequency aging *3	f_age	-0.5	-	+0.5	x10 ⁻⁶	T_use = +25 °C, 1 year
Frequency aging *3	f_age	-3.0	-	+3.0	x10 ⁻⁶	T_use = +25 °C, 20 years
Holdover stability (Free-run accuracy) *4	f_hos	-4.6	-	+4.6	x10 ⁻⁶	T_use = +25 °C, 20 years

^{*1} Includes initial frequency tolerance and frequency deviation after reflow cycles.

Page 4/20 No,: TG-5511CB_rev1.0

^{*2} Measured 24 hours after reflow soldering.

^{*3} Aging is estimated from environmental reliability tests and the expected amount of frequency variation over time. It is not intended as a guarantee of performance over the product-life cycle.

^{*4} This includes initial frequency tolerance, frequency / temperature characteristics, frequency / load coefficient, frequency/voltage coefficient and frequency aging (+25 °C, 20 years).

[5] Electrical Characteristics

 $(V_{CC} = 3.135 \text{ V to } 3.465 \text{ V}, \text{ GND} = 0.0 \text{ V}, \text{ T_use} = +25 ^{\circ}\text{C})$

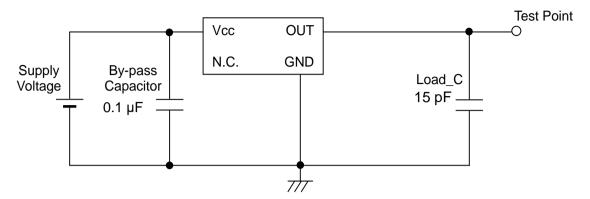
Parameter	Symbol		Specification	S	Unit	Conditions
i arameter	arameter Symbol	Min.	Тур.	Max.		
Current consumption (CMOS)	I _{cc}	-	-	7 9 10	mA	10 MHz ≤ fo ≤ 26 MHz 26 MHz < fo ≤ 40 MHz 40 MHz < fo ≤ 54 MHz
Current consumption (Clipped sine)	I _{cc}	ı	ı	6	mA	
Output voltage (CMOS)	Voн	90 % V _{CC}	-	-	V	
Output voltage (CMOS)	Vol	-	-	10 % V _{cc}	V	
Output voltage (Clipped sine)	Vpp	0.8	-	-	V	Peak to peak voltage
Rise time (CMOS)	tr	1	1	8	ns	10 % V _{CC} to 90 % V _{CC} level
Fall time (CMOS)	tf	1	1	8	ns	90 % V _{CC} to 10 % V _{CC} level
Start-up time	t_str	-	-	5	ms	Until output signal has been reached min 90 % of final amp.
Symmetry (CMOS)	SYM	45	50	55	%	Measured at Vcc/2
		-	-69	-		1 Hz offset
		1	-97	-		10 Hz offset
Phase noise (fo = 19.2 MHz) *		-	-120	-		100 Hz offset
NO-filter	L(f)	-	-140	_	dBc/Hz	1 kHz offset
The fine		-	-154	-		10 kHz offset
		-	-158	-		100 kHz offset
		-	-158	-		1 MHz offset

^{*} For other frequencies, please refer to Charts (7-10), Phase noise

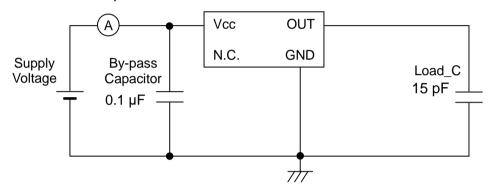
Page 5/20 No,: TG-5511CB_rev1.0

[6] Test Circuit: CMOS Output

1) Output Waveform



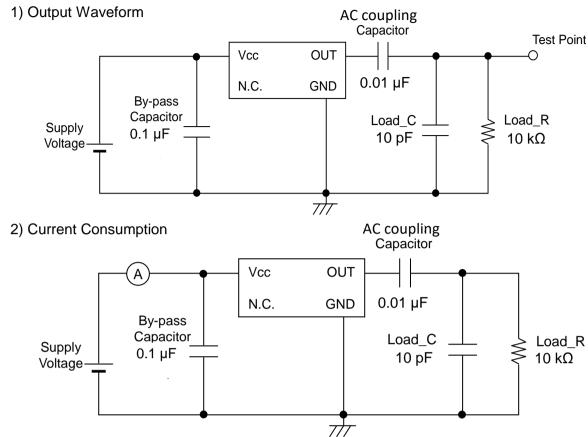
2) Current Consumption



3) Conditions

- 1. Oscilloscope: Impedance Min. 1 MΩ
 Input capacitance Max. 10 pF
 Bandwidth Min. 300 MHz
- 2. L_CMOS includes probe capacitance
- 3. A capacitor (By-pass: 0.1 $\mu \dot{F})$ is placed between V_{CC} and GND close to the TCXO
- 4. Use the current meter whose internal impedance value is small
- 5. Power supply connections should be as low impedance as possible
- 6. GND pin should be connected to low impedance GND

[6] Test Circuit: Clipped Sine Output

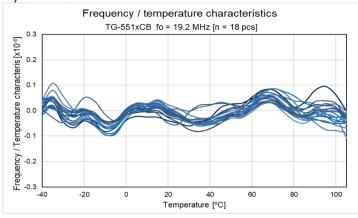


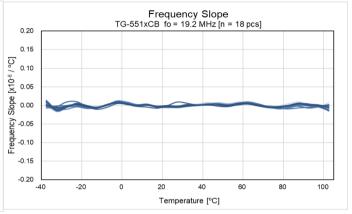
- 3) Conditions
 - Oscilloscope: Impedance Min. 1MΩ
 Input capacitance Max. 10 pF
 Bandwidth Min. 300 MHz
 - 2. Load_C includes probe capacitance
 - 3. A capacitor (By-pass: 0.1 μ F) is placed between V_{CC} and GND and close to the TCXO
 - 4. Use the current meter whose internal impedance value is small
 - 5. Power supply connections should be as low impedance as possible
 - 6. GND pin should be connected to low impedance GND

[7] Chracteristic Data

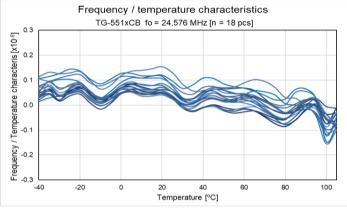
(7-1) Frequency / Temperature Characteristics & Frequency Slope

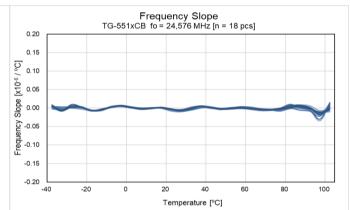
1) fo = 19.2 MHz



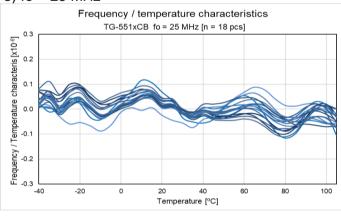


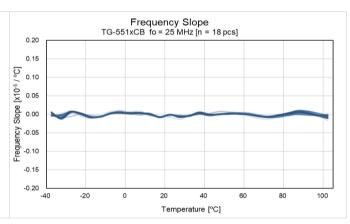
2) fo = 24.576 MHz



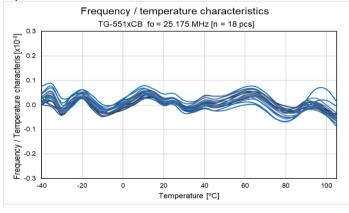


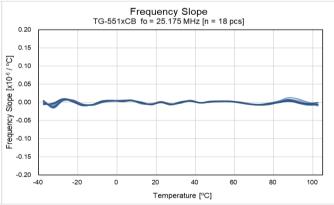
3) fo = 25 MHz





4) fo = 25.175 MHz

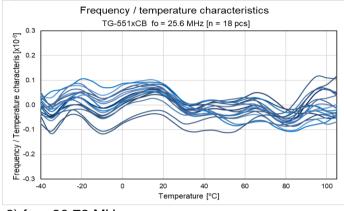


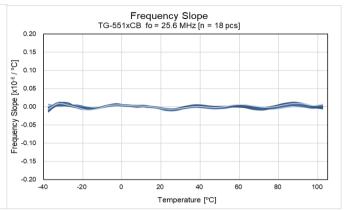


Page 8/20 No,: TG-5511CB_rev1.0

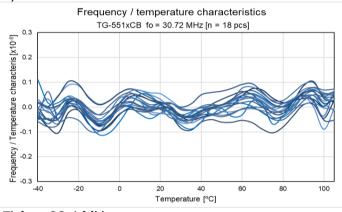
(7-1) Frequency / Temperature Characteristics & Frequency Slope

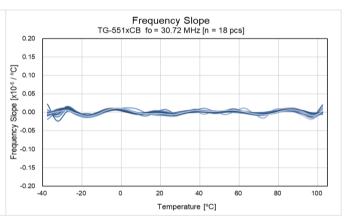




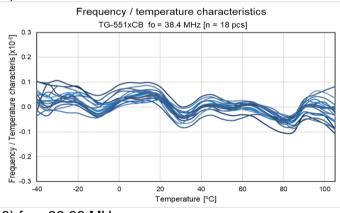


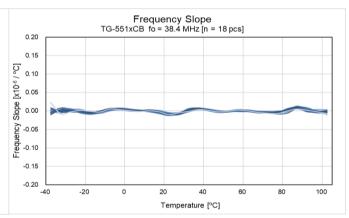
6) fo = 30.72 MHz



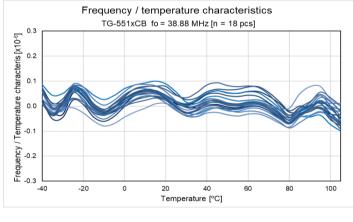


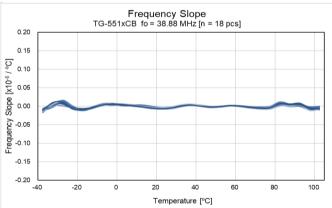
7) fo = 38.4 MHz





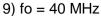
8) fo = 38.88 MHz

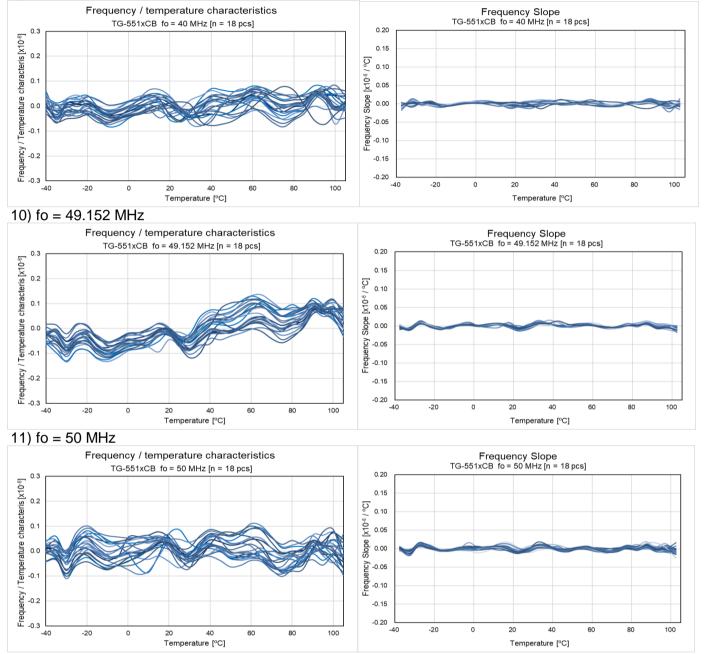




Page 9/20 No,: TG-5511CB_rev1.0

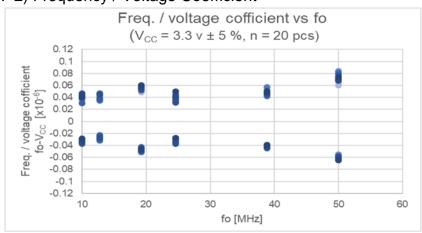
(7-1) Frequency / Temperature Characteristics & Frequency Slope





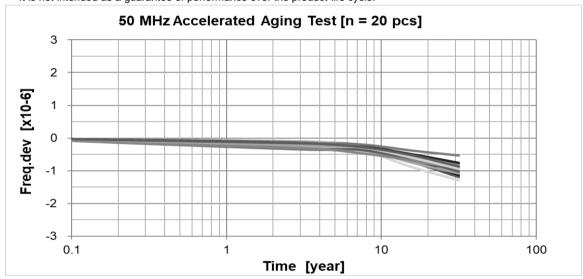
Page 10/20 No,: TG-5511CB_rev1.0

(7-2) Frequency / Voltage Coefficient



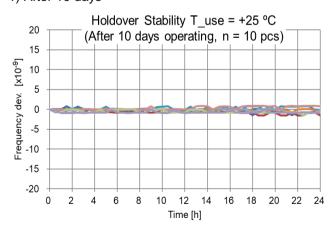
(7-3) Frequency Aging Estimation

* Aging stability is estimated from environmental reliability tests and the expected amount of frequency variation of the product. It is not intended as a guarantee of performance over the product-life cycle.

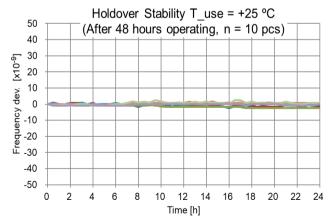


(7-4) Holdover

1) After 10 days

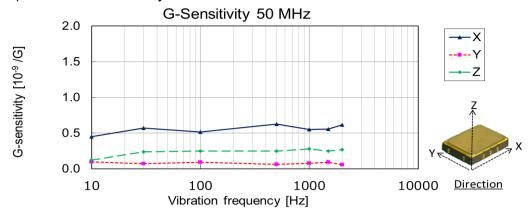


2) After 48 hours

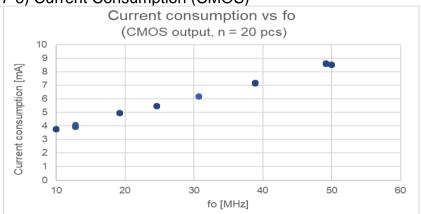


Page 11/20 No,: TG-5511CB_rev1.0

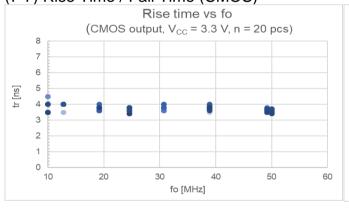
(7-5) Vibration Sensitivity

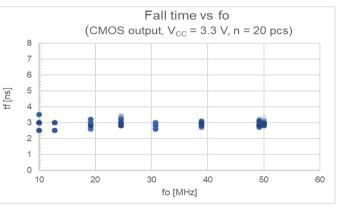


(7-6) Current Consumption (CMOS)

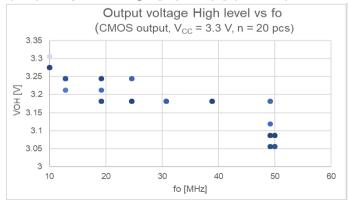


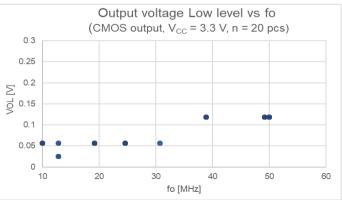
(7-7) Rise Time / Fall Time (CMOS)





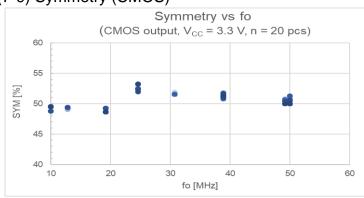
(7-8) Output Voltage [V_{OH}, V_{OL}] (CMOS)





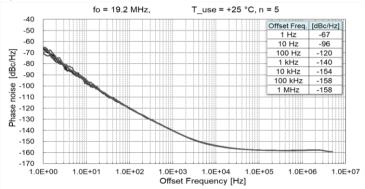
Page 12/20 No,: TG-5511CB_rev1.0

(7-9) Symmetry (CMOS)

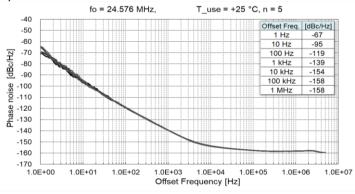


(7-10) Phase Noise

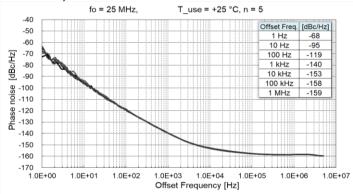
1) fo = 19.2 MHz



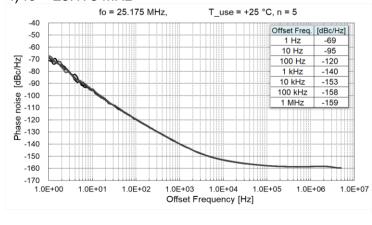
2) fo = 24.576 MHz



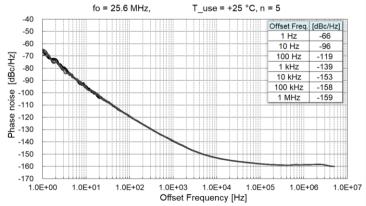
3) fo = 25 MHz



4) fo = 25.175 MHz



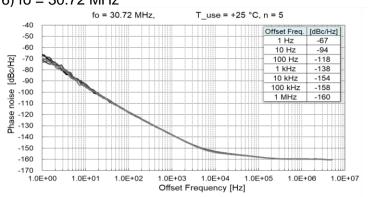
5) fo = 25.6 MHz

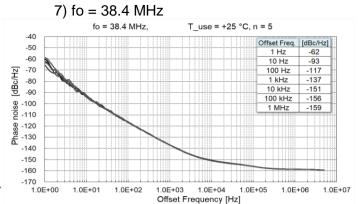


Page 13/20

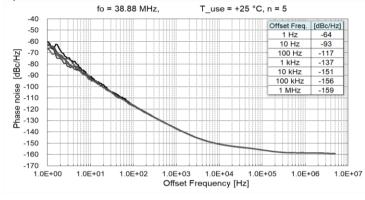
(7-10) Phase Noise

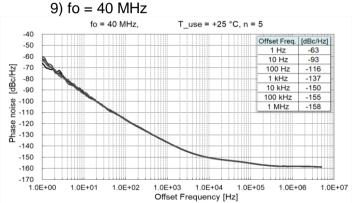
6) fo = 30.72 MHz



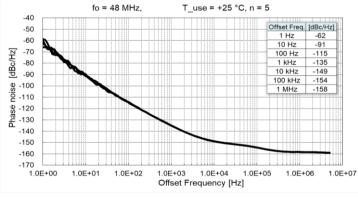


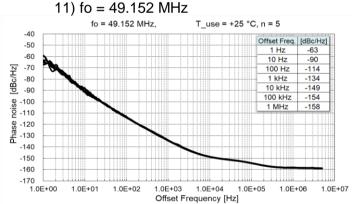
8) fo = 38.88 MHz



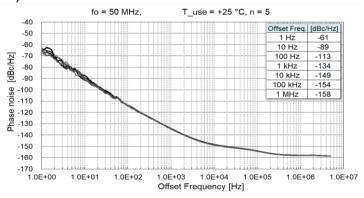


10) fo = 48 MHz



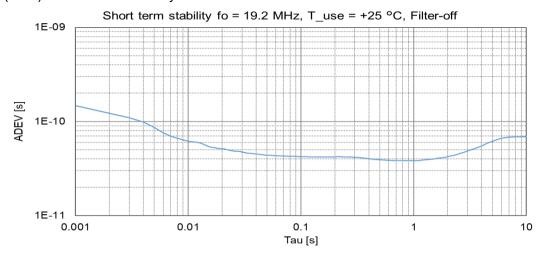


12) fo = 50 MHz

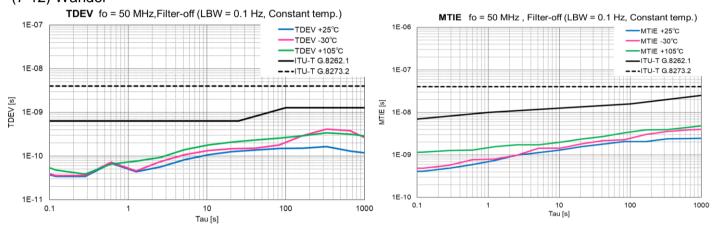


Page 14/20 No,: TG-5511CB_rev1.0

(7-11) Short Term Stability



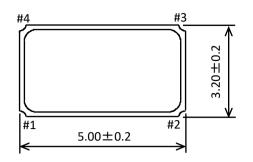
(7-12) Wander

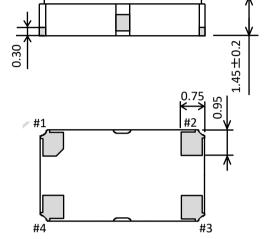


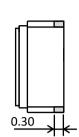
Page 15/20 No,: TG-5511CB_rev1.0

[8] Outline Drawing

unit: mm



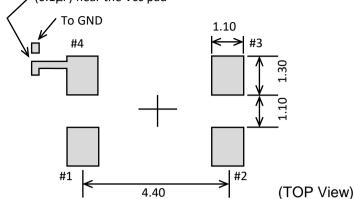




[9] Recommended Foot Print ur

unit: mm

Please place the bypass capacitor (0.1μF) near the Vcc pad



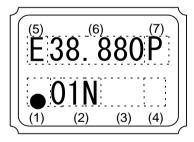
Pin#	Connection
1	N.C.
2	GND
3	OUT
4	V_{CC}

Do not connect pins marked as "N.C." with to any other pin including those marked "N.C."

For proper operation, connect a 0.1 µF by-pass capacitor between Vcc and GND as near as possible to the power source terminal of the oscillator

Terminal plating: Au

Marking



- (1) Pin 1 identifier
- (2) TCXO model ID [01N]
- (3) TCXO Lot No. (3 digits)
- (4) Location code
- (5) Epson logo Mark [E]
- (6) Frequency data
- (7) TG-5511CB code: P



[9] Moisture Sensitivity Level and Electro-Static Discharge

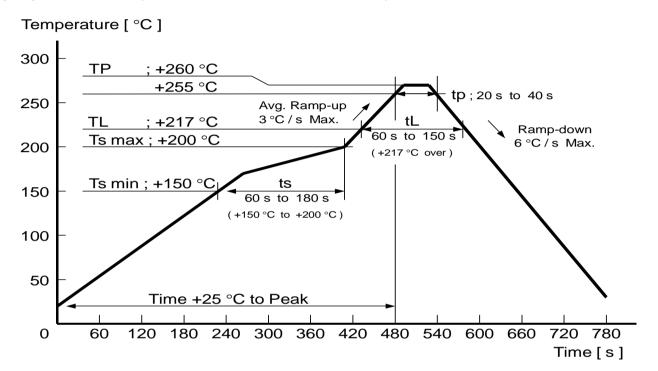
(1) Moisture Sensitivity Level (MSL)

Parameter	Specifications	Conditions
MSL	LEVEL1	JEDEC J-STD-020D

(2)Electro-Static Discharge (ESD)

Parameter	Specifications	Conditions
Human Body Model (HBM)	2,000 V Min.	IEC 60749-26 Ed.2.0:2006(b), 100 pF, 1.5 kΩ, 3 times
Machine Model (MM)	200 V Min.	IEC 60749-27 Ed.2.0:2006(b), 200 pF, 0 Ω, 1 time

[10] Reflow Profile (follow to IPC / JEDEC J-STD-020D.1)



Page 17/20 No,: TG-5511CB_rev1.0

[11] Packing Information

(1) Quantity

For this product series, the standard for the last two digits of the product number is "14", 1,000 pcs/reel.

TG-5511CB: X1G006071xxxx14

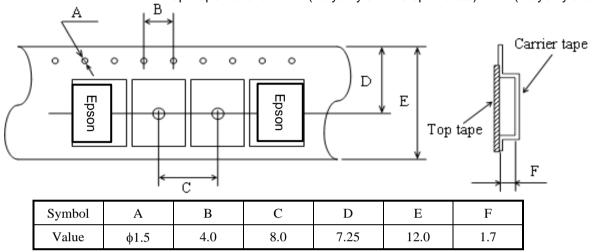
(2) Taping Specification

Subject to EIA-481, IEC-60286 and JIS C0806

1) Tape dimensions

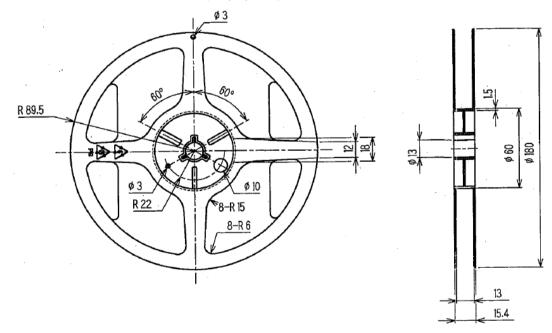
Carrier Tape Material: PS (Polystyrene)

Top Tape Material: PET (Polyethylene Terephthalate) + PE (Polyethylene)



2) Reel dimensions

Center Material: PS (Polystyrene) Reel Material: PS (Polystyrene)



[12] Handling Precautions

Prior to using this product, please carefully read the section entitled "Precautions" on our website (https://www5.epsondevice.com/en/information/#precaution) for instructions on how to handle and use the product properly to ensure optimal performance in your equipment. Before using the product under any conditions other than those specified therein, please consult with us to verify and confirm that the performance of the product will not be negatively affected by use under such conditions.

In addition to the foregoing precautions, in order to avoid negatively impacting the performance of the product, we strongly recommend that you DO NOT use the product under ANY of the following conditions:

- (1) Do not expose this product to excessive mechanical shock or vibration.
- (2) This product can be damaged by mechanical shock during the soldering process depending on the equipment used, process conditions, and any impact forces experienced. Always follow appropriate procedures, particularly when changing the assembly process in any way and be sure to follow applicable process qualification standards before starting production.
- (3) These devices are sensitive to ESD, use appropriate precautions during handling, assembly, test, shipment, and installation.
- (4) This product contains semiconductor content that should not be exposed to electromagnetic waves.
- (5) The use of ultrasonic technology for cleaning, bonding, etc. can damage the Xtal unit inside this product.

 Please carefully check this consideration before using ultrasonic equipment for volume production with this product.
- (6) Noise and ripple on the power supply may have undesirable affects on operation and cause degradation of phase noise characteristics. Evaluate the operation of this device with appropriate power supplies carefully before use.
- (7) When applying power, ensure that the supply voltage increases monotonically for proper operation. On power down, do not reapply power until the supplies, bypass capacitors, and any bulk capacitors are completely discharged since that may cause the unit to malfunction.
- (8) Aging specifications are estimated from environmental reliability tests and expected frequency variation over time. They do not provide a guarantee of aging over the product lifecycle.
- (9) The metal cap on top of the device is directly connected to the GND terminal (pin #2). Take necessary precautions to prevent any conductor not at ground potential from contacting the cap as that could cause a short circuit to GND.
- (10) Do not route any signal lines, supply voltage lines, or GND lines underneath the area where the oscillators are mounted including any internal layers and on the opposite side of the PCB. To avoid any issues due to interference of other signal lines, please take care not to place signal lines

near the product as this may have an adverse affect on the performance of the product.

- (11) A bypass capacitor of the recommended value(s) must be connected between the V_{CC} and GND terminals of the product. Whenever possible, mount the capacitor(s) on the same side of the PCB and as close to the product as possible to keep the routing traces short.
- (12) Power supply connections to V_{CC} and GND pins should be routed as thick as possible while keeping the high frequency impedance low in order to get the best performance.
- (13) The use of a filter or similar element in series with the power supply connections to protect from electromagnetic radiation noise may increase the high frequency impedance of the power supply line and may cause the oscillator to not operate properly. Please verify the design to ensure sufficient operational margin prior to use.
- (14) Keep PCB routing from the output terminal(s) to the load as short as possible for best performance.
- (15) Do not short the output to GND as that will damage the product. Always use with an appropriate load resistor connected.
- (16) Product failures during the warranty period only apply when the product is used according to the recommended operating conditions described in the specifications. Products that have been opened for analysis or damaged will not be covered. It is recommended to store and use in the normal temperature and humidity environments described in the specifications to ensure frequency accuracy and prevent moisture condensation. If the product is stored for more than one year, please confirm the pin solderability prior to use.
- (17) If the oscillation circuit is exposed to condensation, the frequency may change or oscillation may stop. Do not use in any conditions where condensation occurs.
- (18) Do not store or use the product in an environment where it can be exposed to chemical substances that are corrosive to metal or plastics such as salt water, organic solvents, chemical gasses, etc. Do not use the product when it is exposed to sunlight, dust, corrosive gasses, or other materials for long periods of time.
- (19) When using water-soluble solder flux make sure to completely remove the flux residue after soldering. Pay particular attention when the residues contain active halogens which will negatively affect the product and its performance.

Page 19/20 No,: TG-5511CB_rev1.0

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard. All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

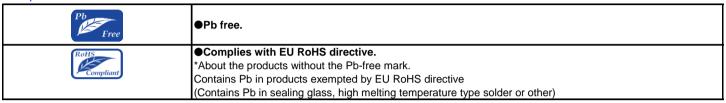
ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major manufacturers as standard.

IATF 16949 is the international standard that added the sectorspecific supplemental requirements for automotive industry based on ISO9001

Explanation of marks used in this datasheet



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Page 20/20 No,: TG-5511CB_rev1.0