

DATA SHEET

**ELECTROSTATIC DISCHARGE
PROTECTION DEVICES**

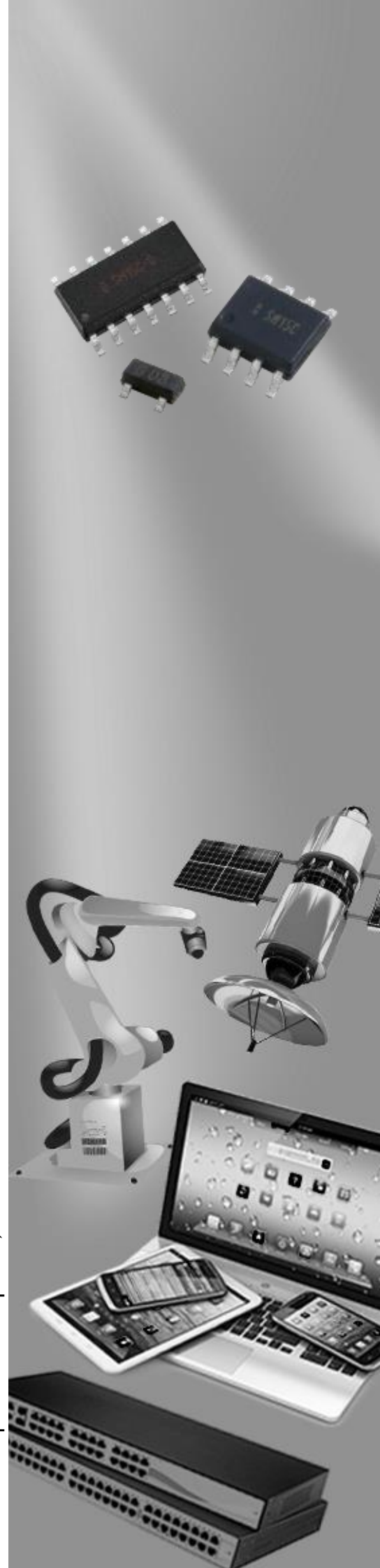
INDUSTRIAL / CONSUMER

UAD92A03L01

RoHS compliant & Halogen free



Product specification— April 11, 2023 V.0



Electrostatic Discharged Protection Devices (ESD) Data Sheet

Features

- Transient protection for high-speed data lines
IEC61000-4-2 ESD : 25KV (Air)
20KV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50 ns)
- Cable Discharge Event (CDE)
- SOD-923 package
- Package optimized for high-speed lines
- Protects one data, control or power line
- Low capacitance: 0.5pF (Typical)
- Low leakage current: 0.1 μ A @ V_{RWM} (Typical)
- Low clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for ± 8 kV contact discharge
- Flammability Rating: UL 94V-0
- Marking: C

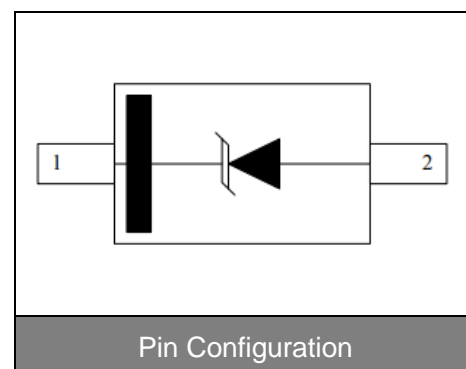


Contact : 20KV
Air : 25KV



Applications

- Serial ATA
- PCI Express
- Desktops, Servers and Notebooks
- Cellular Phones
- MDDI Ports
- USB2.0/3.0 Power and Data Line Protection
- Display Ports
- High Definition Multi-Media Interface (HDMI)
- Digital Visual Interfaces (DVI)



Maximum Ratings

Rating	Symbol	Value	Unit
Peak pulse power ($t_p=8/20\mu$ s waveform)	P_{PP}	50	W
ESD voltage (Contact discharge)	V_{ESD}	± 20	kV
ESD voltage (Air discharge)		± 25	
Operating Temperature	T_J	-55~+125	$^{\circ}$ C
Storage Temperature	T_{stg}	-55~+150	$^{\circ}$ C

Electrical Characteristics ($T_J=25^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				3.3	V
Reverse breakdown voltage	V_{BR}	$I_{\text{BR}}=1\text{mA}$	6.0			V
Reverse leakage current	I_{R}	$V_{\text{R}}=3.3\text{V}$		0.1	1.0	μA
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_{C}	$I_{\text{PP}}=1\text{A}$			10.0	V
Peak pulse current ($t_p=8/20\mu\text{s}$)	I_{PP}				4.0	A
Off state junction capacitance	C_{J}	$0\text{Vdc}, f=1\text{MHz}$		0.85		pF

Typical Characteristics Curves

Figure 1. Voltage Sweeping of I/O to GND

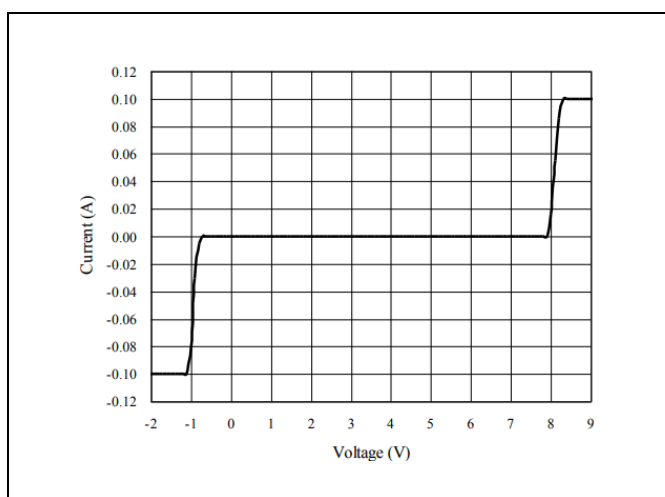


Figure 2. Insertion Loss S21 of I/O to GND

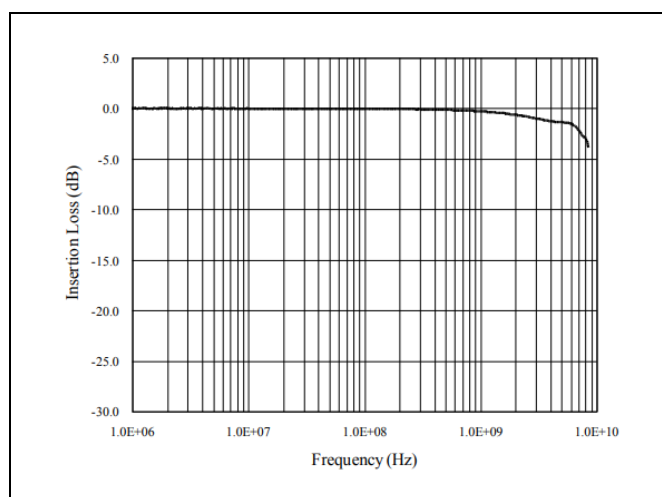


Figure 3. Capacitance vs. Reverse Voltage

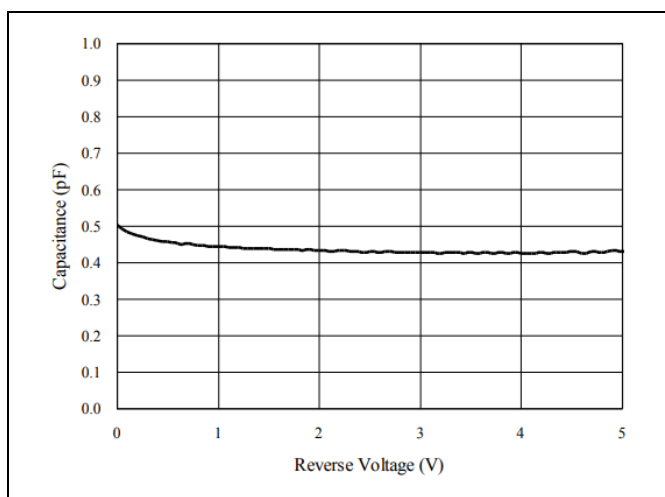
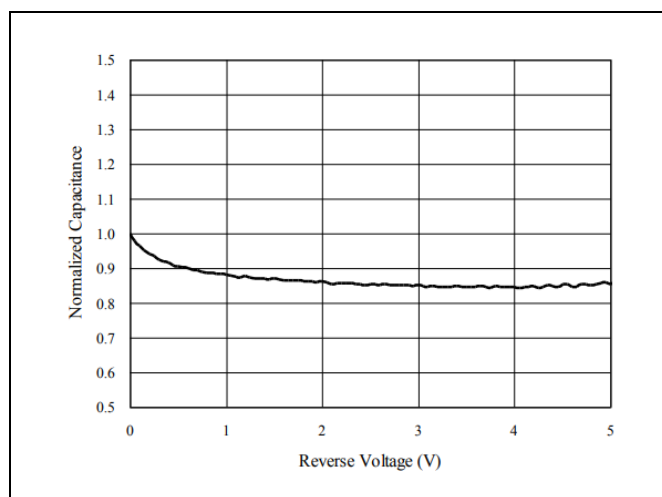
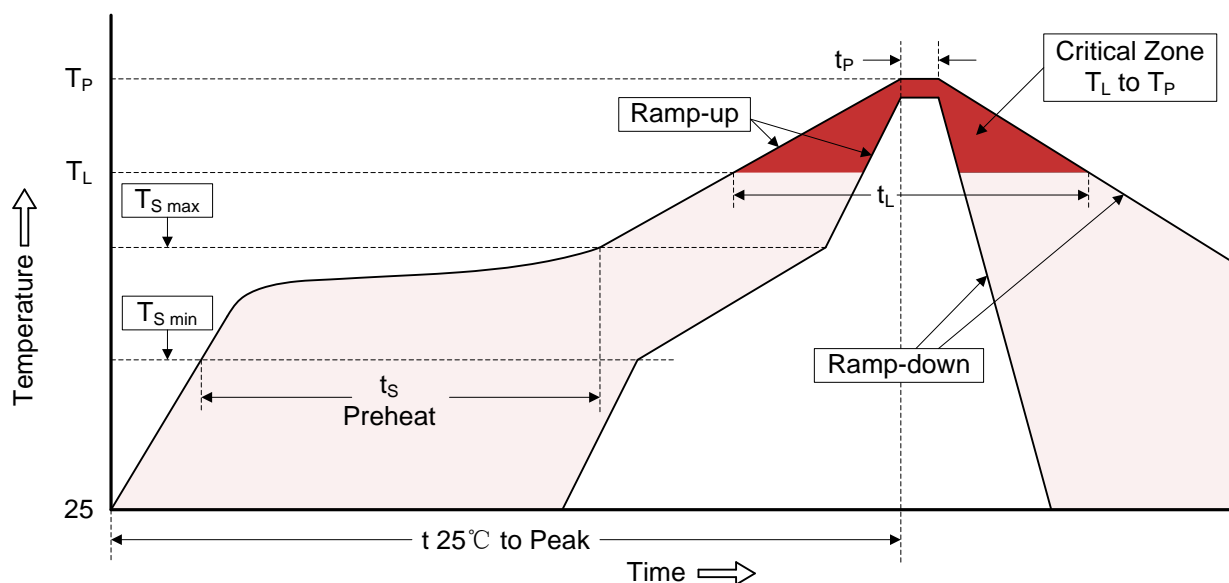


Figure 4. Normalized Capacitance vs. Reverse Voltage



Recommended Soldering Conditions

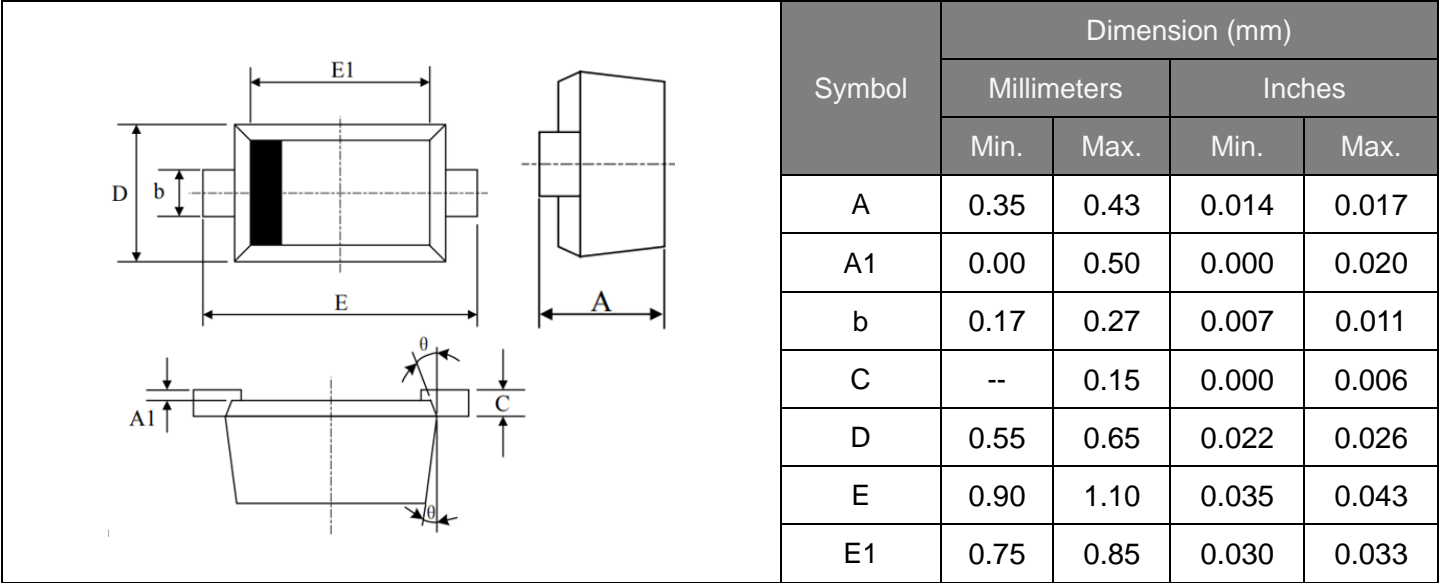
Reflow Soldering



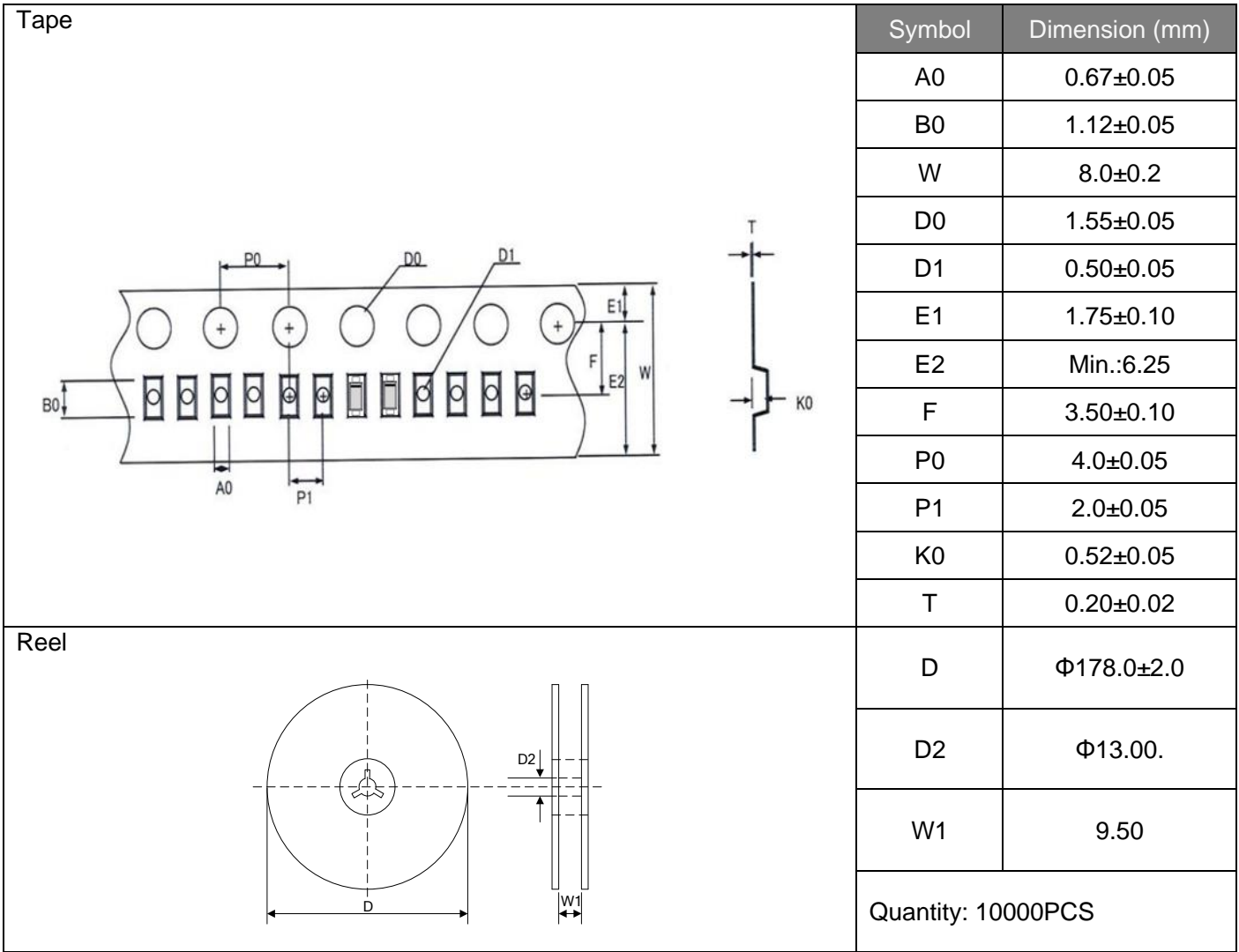
Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat <ul style="list-style-type: none"> -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) (t_s) 	150°C 200°C 60-180 seconds
$T_{S\ max}$ to T_L <ul style="list-style-type: none"> -Ramp-up Rate 	3°C/second max.
Time maintained above: <ul style="list-style-type: none"> -Temperature (T_L) -Time (t_L) 	217°C 60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (SOD-923)



Packaging



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