

Innovative Service Around the Globe

DATA SHEET ELECTROSTATIC DISCHARGE PROTECTION DEVICES INDUSTRIAL / CONSUMER UBD8C12L01

RoHS compliant & Halogen free



Electrostatic Discharged Protection Devices (ESD) Data Sheet

Description

The UBD8C12L01 Transient Voltage Suppressors is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computer, and PDAs. It offer superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. It is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).

Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOD882 surface mount package
- Working voltage: 12V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: B12

Applications

- USB 3.0/USB 2.0
- Touch Panels
- Personal digital assistants (PDA)
- Serial ATA protection
- Wireless system devices
- Handhelds and notebooks
- Digital cameras
- Portable Devices

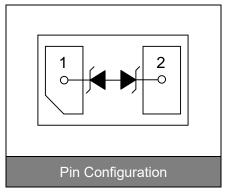
Maximum Ratings

Rating	Symbol	Value	Unit	
ESD voltage (Contact discharge)		±8	kV	
ESD voltage (Air discharge)	V _{ESD}	±15		
Lead soldering temperature	TL	260	°C	
Storage & operating temperature range	T _{STG} ,TJ	-55~+150	°C	



Contact : ±8kV Air : ±15kV





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Electrical Characteristics (TJ=25°C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Reverse stand-off voltage	V _{RWM}				12	V
Reverse breakdown voltage	V _{BR}	I _{BR} =1.0mA	13.7			V
Reverse leakage current	I _R	V _R =12V			0.5	μA
Clamping voltage (tp=8/20µs)	Vc	I _{PP} =3.0A		35		V
Peak pulse current (tp=8/20µs)	I _{PP}				3	А
ESD Clamping voltage (TLP)	Vc	I _{PP} =8.0A		30		V
ESD Clamping voltage (TLP)	Vc	I _{PP} =16A		44		V
ESD Dynamic Turn-on Resistance	R _{dynamic}			1.75		Ω
Off state junction capacitance	CJ	0Vdc,f=1MHz		0.3	0.4	pF

Typical Characteristics Curves

Figure 1. Pulse Waveforms

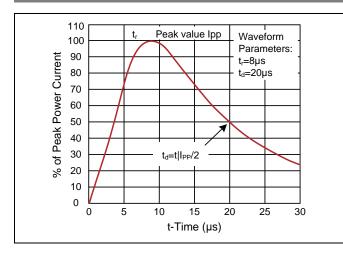
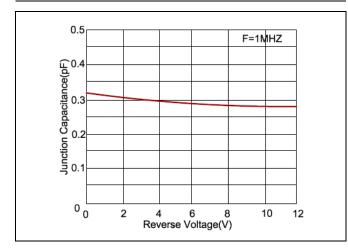


Figure 3. Capacitance vs. Reverse Voltage



Jul. 04, 2023 V.I

Figure 2. Clamping Voltage vs. Peak Pulse Current

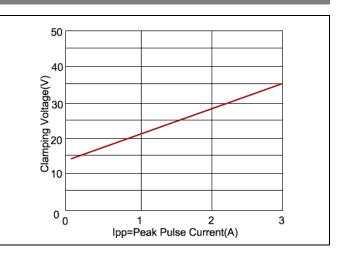
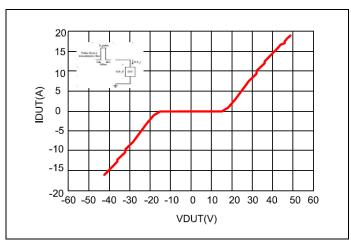
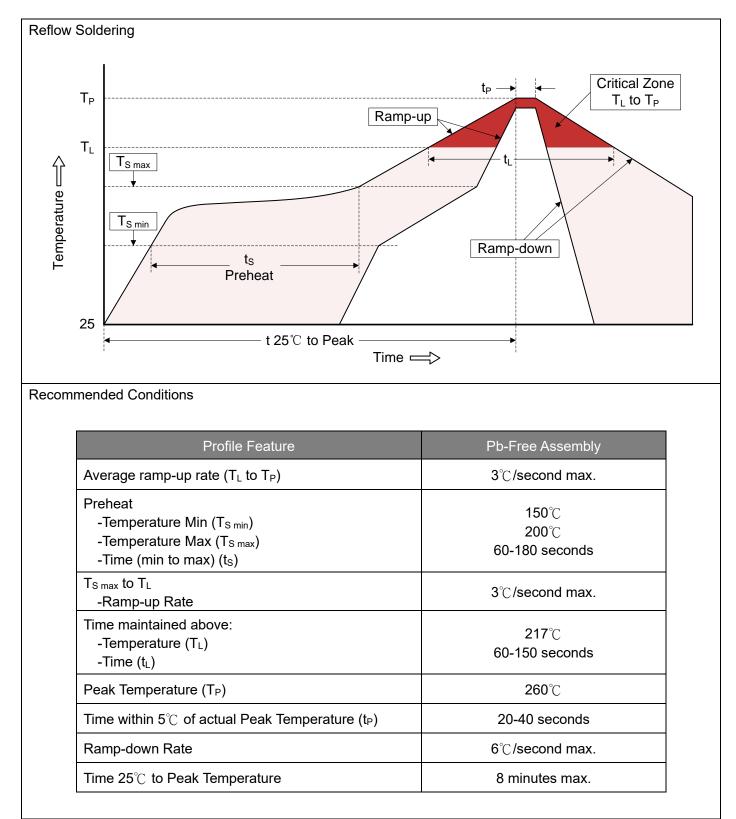


Figure4 .Transmission Line Pulsing(TLP)Measurement

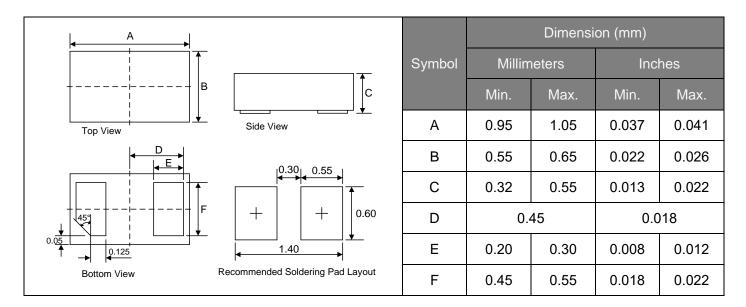


Recommended Soldering Conditions

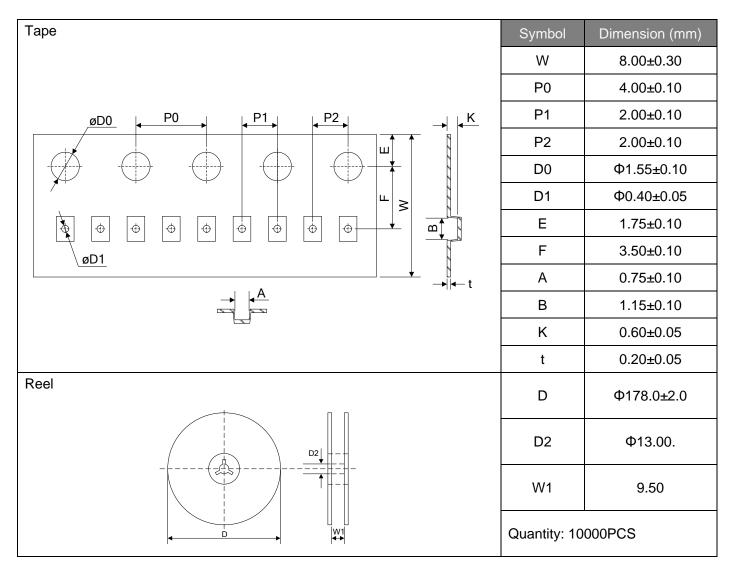


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Dimensions (SOD882)



Packaging



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