

# DATA SHEET

**ELECTROSTATIC DISCHARGE  
PROTECTION DEVICES**

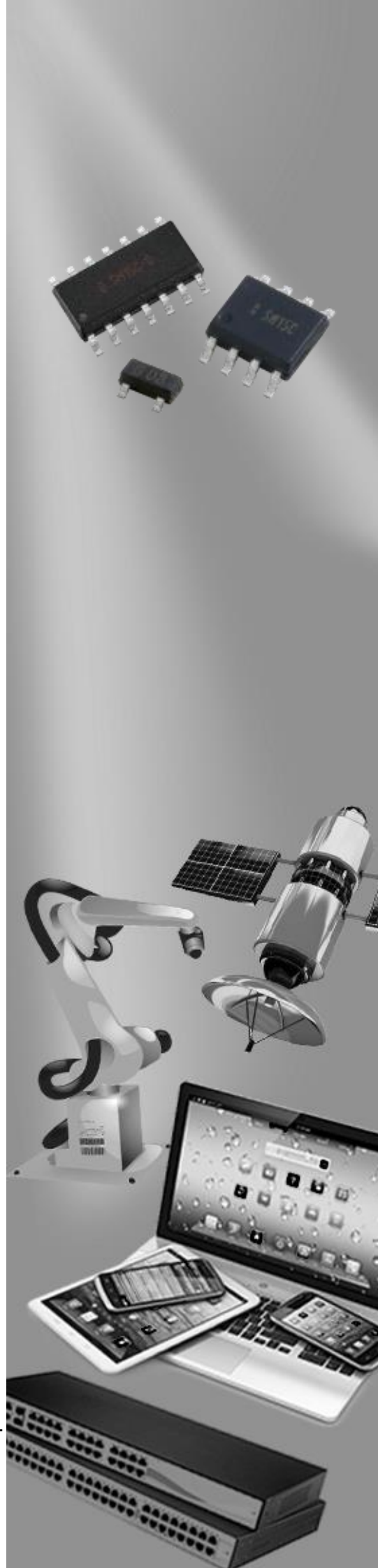
**INDUSTRIAL / CONSUMER**

UBT32A05L02

RoHS compliant & Halogen free



Product specification— December 28, 2023 V.4



## Electrostatic Discharged Protection Devices (ESD) Data Sheet

### Description

This is ultra low capacitance TVS arrays designed to protect high speed data interfaces. It has been specifically designed to protect sensitive components which is connected to high-speed data and transmission lines from overvoltage caused by electrostatic (ESD), cable discharge events (CDE) and electrical fast transients (EFT). It has a typical capacitance of only 0.8pF(typ.). This means it can be used on circuits operating in excess of 3GHz without signal attenuation.

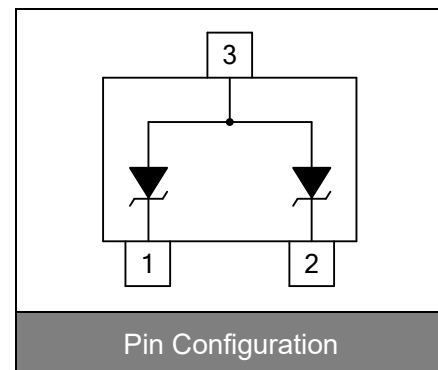


Contact :  $\pm 8\text{kV}$   
Air :  $\pm 15\text{kV}$



### Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOT-323 surface mount package
- Protects two high speed data lines
- Working voltage: 5V
- Ultra low capacitance and clamping voltages
- Low leakage current
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Marking: B BU



### Applications

- HDMI interface protection
- Mobile display digital interface
- RF/Antenna circuits
- USB 2.0 & Firewire ports
- GaAs photodetector protection
- HBT power Amp protection
- Infiniband transceiver protection

### Maximum Ratings

Rating	Symbol	Value	Unit
ESD voltage (Contact discharge)	$V_{\text{ESD}}$	$\pm 8$	kV
ESD voltage (Air discharge)		$\pm 15$	
Storage & operating temperature range	$T_{\text{STG}}, T_{\text{J}}$	-55~+150	°C

Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ )

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	$V_{RWM}$				5	V
Reverse breakdown voltage	$V_{BR}$	$I_{BR}=1\text{mA}$	6			V
Reverse leakage current	$I_R$	$V_R=5\text{V}$ Each I/O pin			1	$\mu\text{A}$
Clamping voltage ( $t_p=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=3\text{A}$			15	V
Peak pulse current ( $t_p=8/20\mu\text{s}$ )	$I_{PP}$				3	A
Off state junction capacitance	$C_J$	0Vdc, $f=1\text{MHz}$ Between I/O pins and GND		0.8		pF

## Typical Characteristics Curves

Figure 1. Power Derating Curve

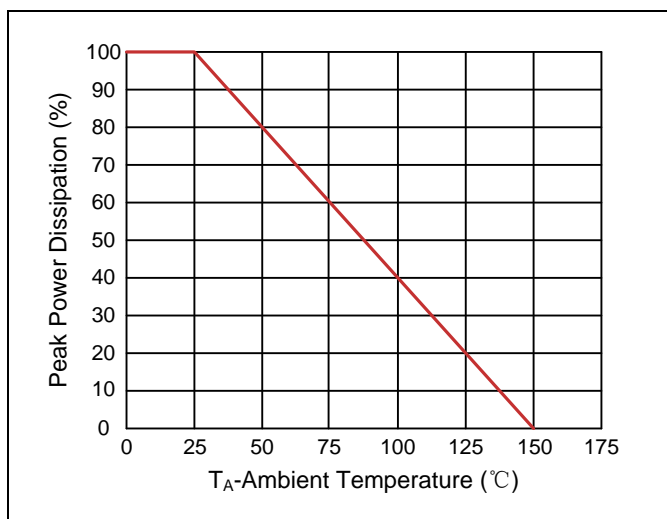


Figure 2. Pulse Waveforms

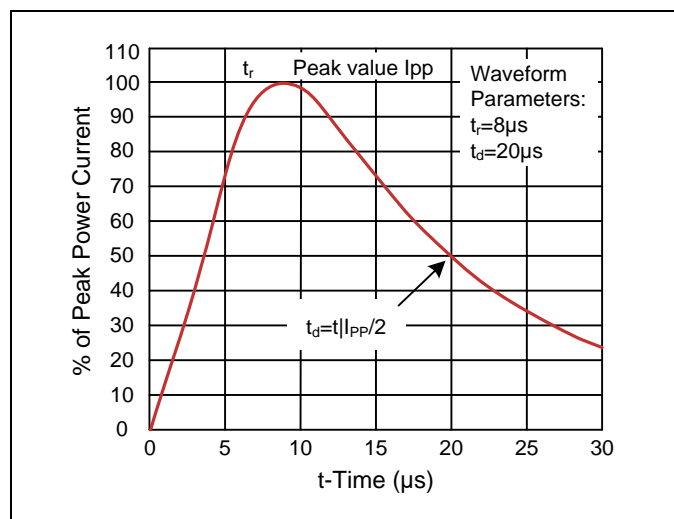


Figure 3. Non-Repetitive Peak Pulse vs. Pulse Time

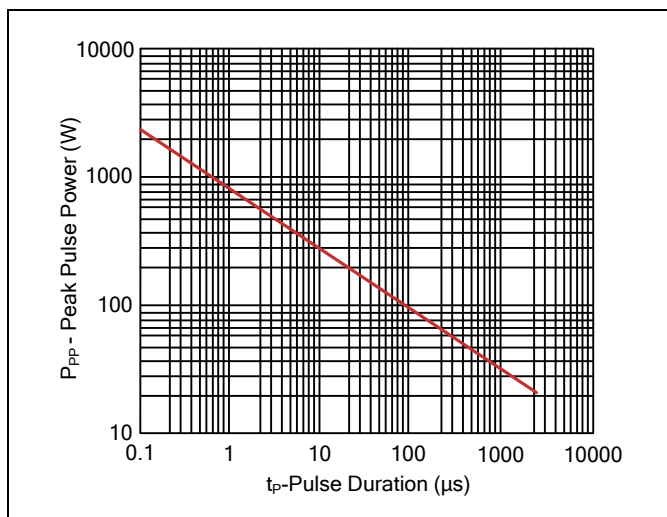
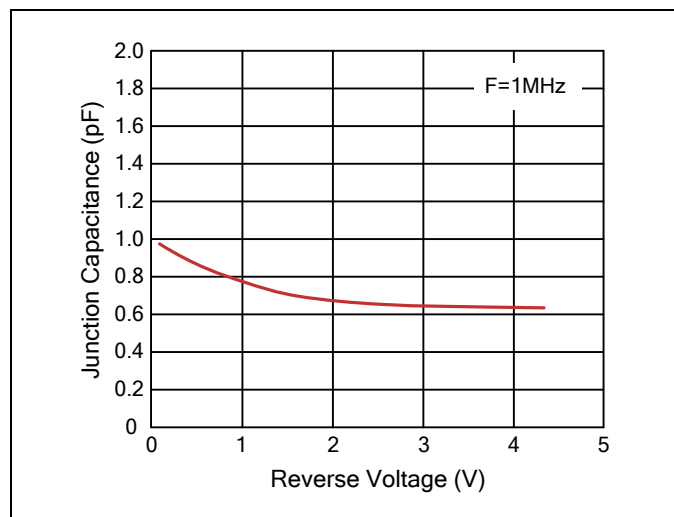
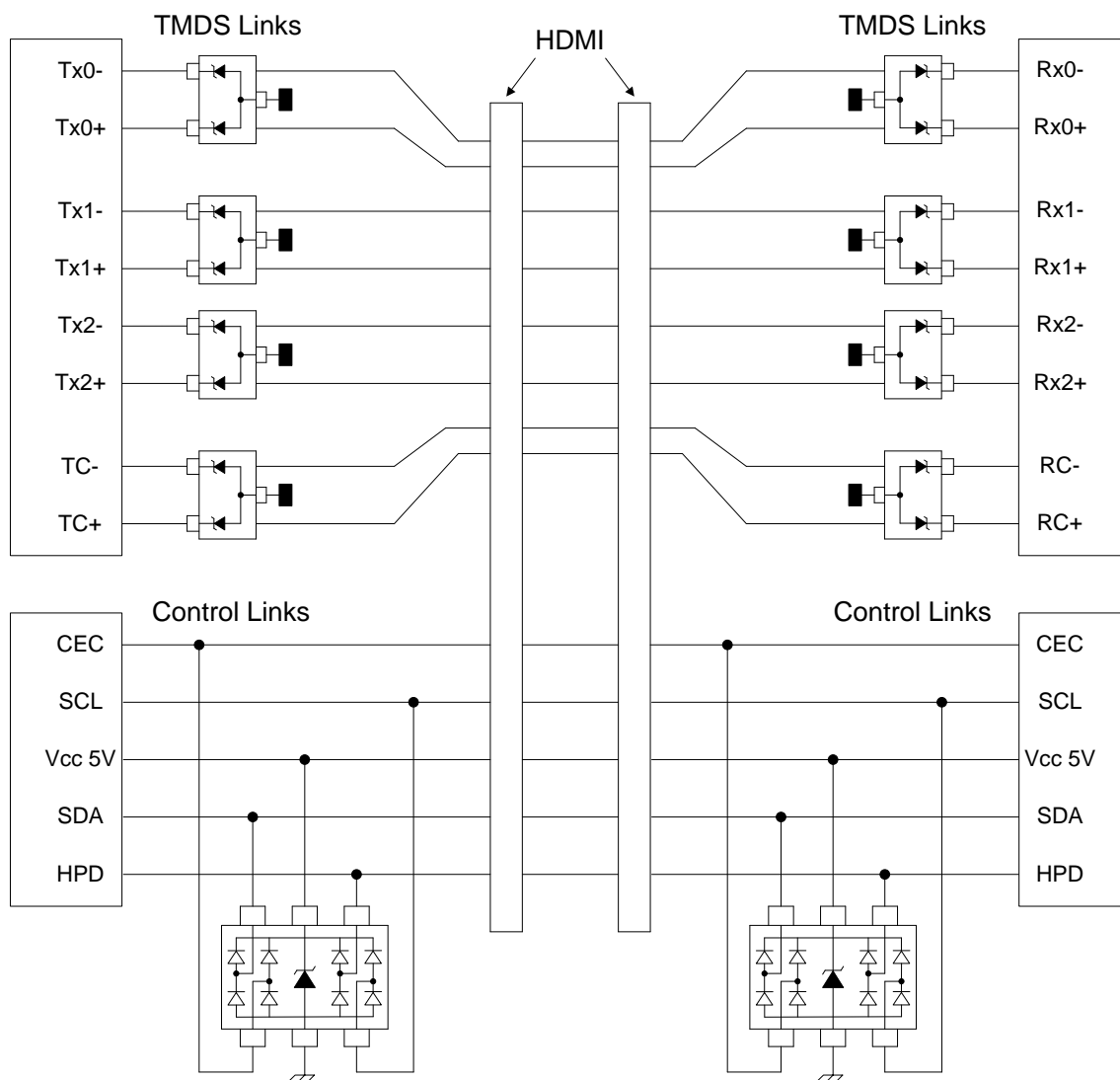


Figure 4. Capacitance vs. Reverse Voltage

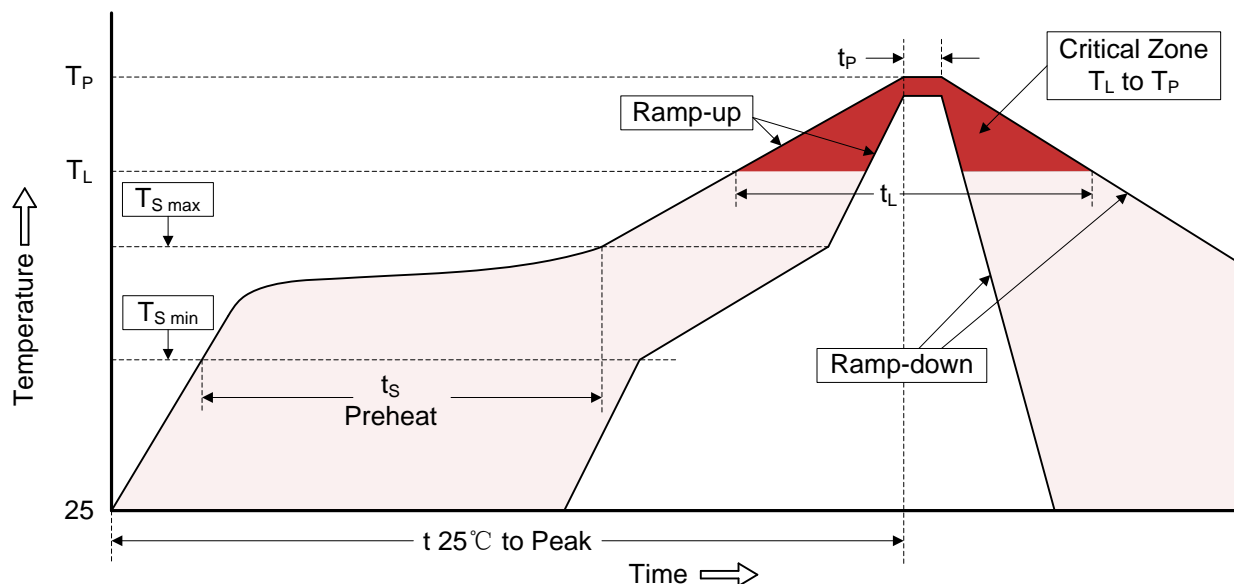


## Applications Information



## Recommended Soldering Conditions

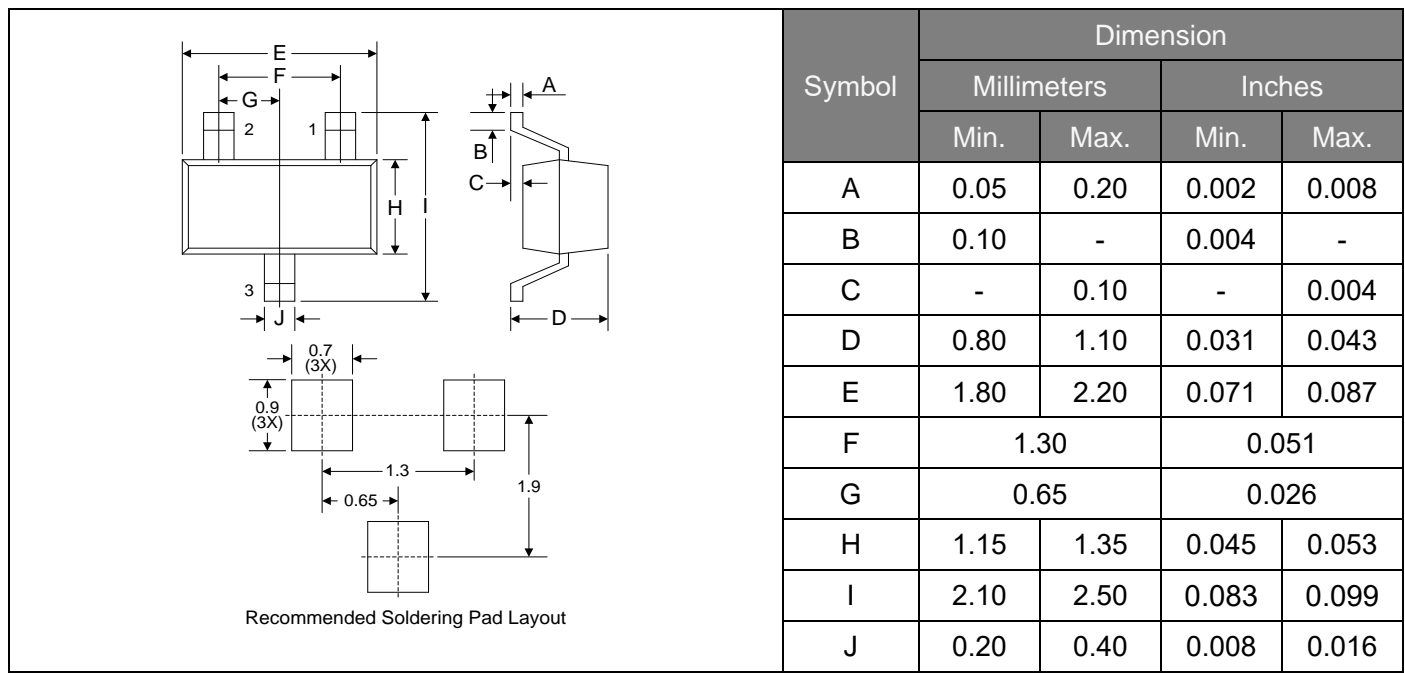
### Reflow Soldering



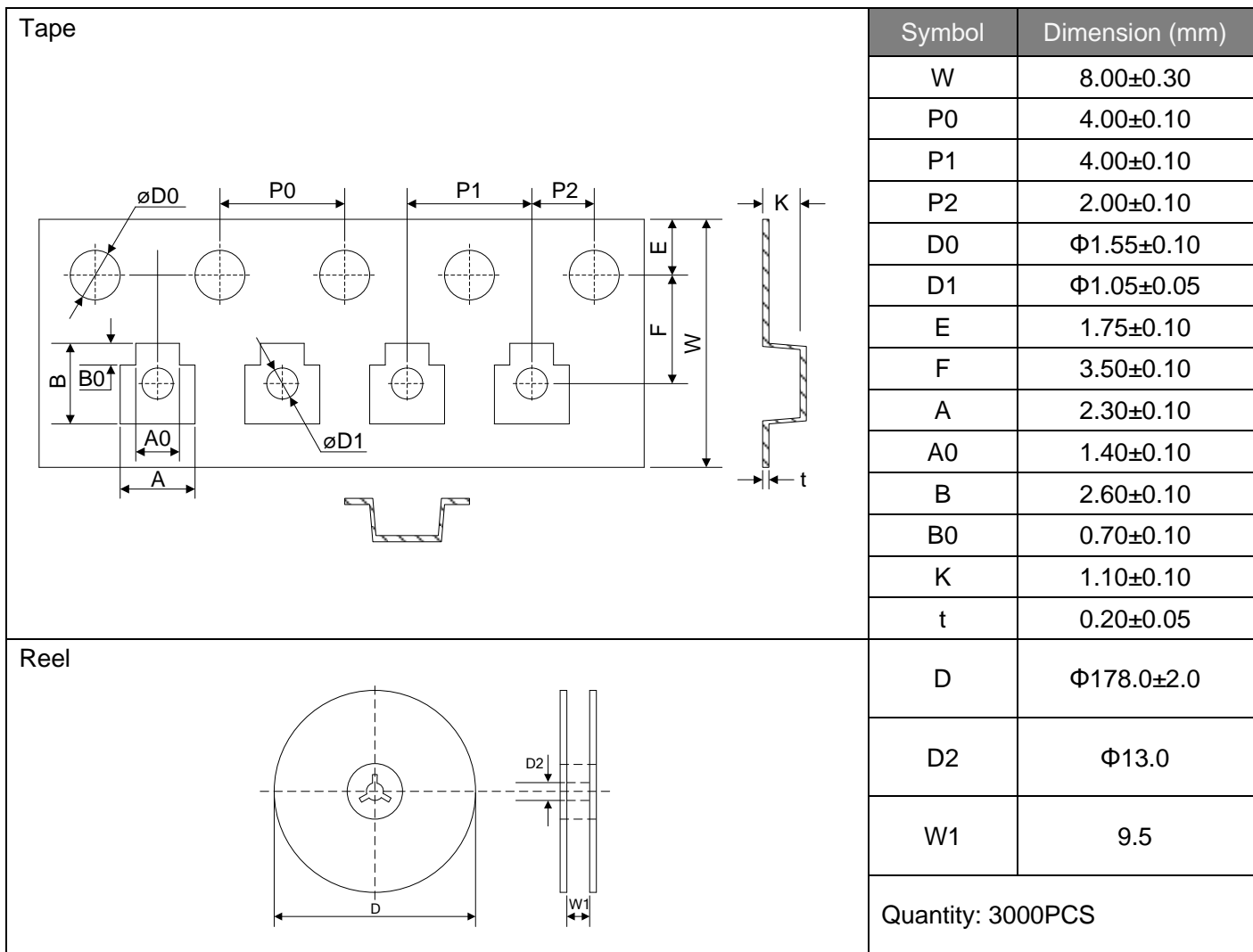
### Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )	3°C/second max.
Preheat <ul style="list-style-type: none"> <li>-Temperature Min (<math>T_{S\ min}</math>)</li> <li>-Temperature Max (<math>T_{S\ max}</math>)</li> <li>-Time (min to max) (<math>t_s</math>)</li> </ul>	150°C 200°C 60-180 seconds
$T_{S\ max}$ to $T_L$ <ul style="list-style-type: none"> <li>-Ramp-up Rate</li> </ul>	3°C/second max.
Time maintained above: <ul style="list-style-type: none"> <li>-Temperature (<math>T_L</math>)</li> <li>-Time (<math>t_L</math>)</li> </ul>	217°C 60-150 seconds
Peak Temperature ( $T_P$ )	260°C
Time within 5°C of actual Peak Temperature ( $t_P$ )	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (SOT-323)



Packaging



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