XG-2102CA

Product name XG-2102CA 156.250000MHz +/-100ppm PHRN

Product Number / Ordering code X1M0003010004xx

Please refer to the 9.Packing information about xx (last 2 digits)

Output waveform LV-PECL

Pb free / Complies with EU RoHS directive

Reference weight Typ. 133 mg

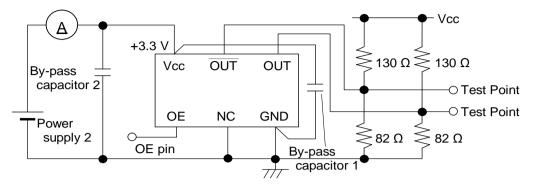
1.Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.5	-	4	V	-
Storage temperature	T_stg	-55	-	125	°C	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	ST or OE Terminal

2.Specifications(characteri	stics)						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks	
Output frequency	fO	-	156.2500	-	MHz		
Supply voltage	Vcc	2.97	3.3	3.63	V	-	
Operating temperature	T_use	-5	-	85	°C	-	
Frequency tolerance	f_tol	-100	-	100	x10 ⁻⁶	-	
Current consumption	lcc	-	-	60	mA	OE=Vcc,L_ECL=50ohm	
Stand-by current	I_std	-	-	-	mA	-	
Disable current	I_dis	-	-	2.0	mA	OE=GND	
Symmetry	SYM	45	-	55	%	As output crodding point	
Output voltage(LV-PECL)	V _{OH}	Vcc-1.025	2.35	Vcc-0.88	V	-	
	V _{OL}	Vcc-1.81	1.6	Vcc-1.62	V	-	
Output load condition(ECL)	L_ECL	-	50	-	Ω	Terminated to Vcc-2.0V	
Input voltage	V _{IH}	70% Vcc	-	-		OE Terminal	
	V _{IL}	-	-	30% Vcc		OE Terminal	
Rise time	t _r	-	-	400	ps	At 20% to 80% output swing	
Fall time	tf	-	-	400	ps	At 20% to 80% output swing	
Start-up time	t_str	-	-	10	ms	-	
Jitter	t _{DJ}	-	-	N/A	ps	Deterministic Jitter	
	T _{RJ}	-	-	N/A	ps	Random Jitter	
	t _{RMS}	-	-	N/A	ps	δ(RMS of total distribution)	
	t _{p-p}	-	-	N/A	ps	Peak to Peak	
	t _{acc}	-	-	N/A	ps	Accumulated Jitter(δ) n=2 to 50000 cycles	
Phase jitter	t _{PJ}	-	-	0.22	ps	Off set Frequency: 12kHz to 20MHz	
Phase noise	L(f)	-	-	-	dBc/Hz	Off set 1Hz	
		-	-52.0	-	dBc/Hz	Off set 10Hz	
		-	-82.3	-	dBc/Hz	Off set 100Hz	
		-	-111.9	-	dBc/Hz	Off set 1kHz	
		-	-141.2	-	dBc/Hz	Off set 10kHz	
		-	-149.8	-	dBc/Hz	Off set 100kHz	
		-	-153.8	-	dBc/Hz	Off set 1MHz	
Frequency aging	f_age	-10	-	10	x10 ⁻⁶ /Year	@+25°C first year	

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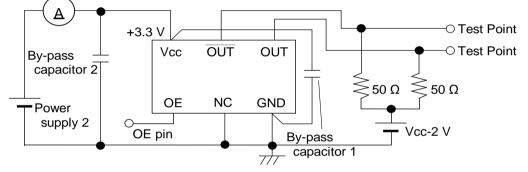
3.Test circuit

1) To observe waveform and current (case 1)



- * The lines from OUT and OUT pin are same length.
- * To measure the disable current, OE pin is connected to GND

2) To observe waveform and current (case 2)



* The lines from OUT and OUT pin are same length.

* To measure the disable current, OE pin is connected to GND

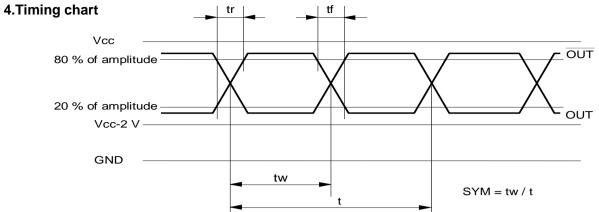
- 3) Measurement condition
- A) Oscilloscope

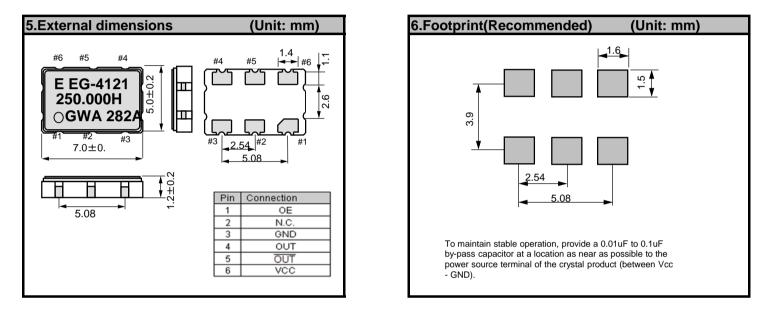
•Bandwidth should be 5 times higher than DUT's output frequency (4 GHz).

•Probe ground should be placed closely from test point and lead length should be as short as possible.

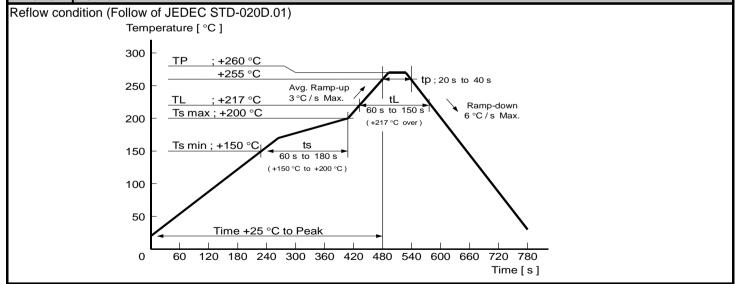
- B) By-pass capacitor 1 (approx. 0.01 µF to 0.1 µF) places closely between Vcc and GND.
- C) By-pass capacitor 2 (approx. 10 µF) places closely between power supply terminals on the board.
- D) Use the current meter whose internal impedance value is small.
- E) Power supply
- Start up time (0 Vg90 %Vcc) of power source should be more than 150 µs and slew rate should be less than 19.8 mV/µs.
- Impedance of power supply should be as low as possible.

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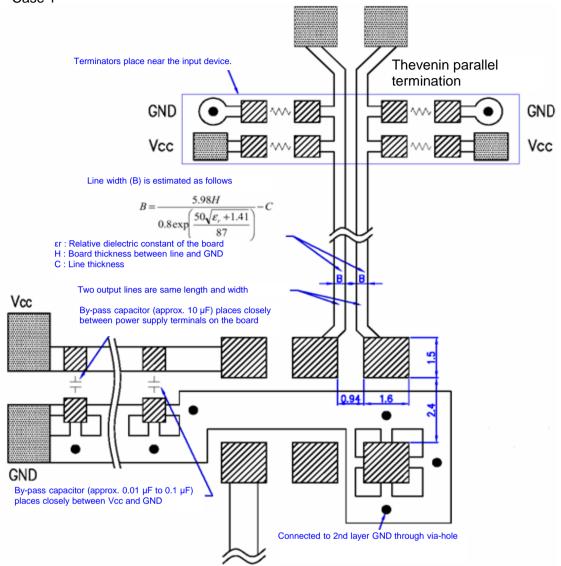




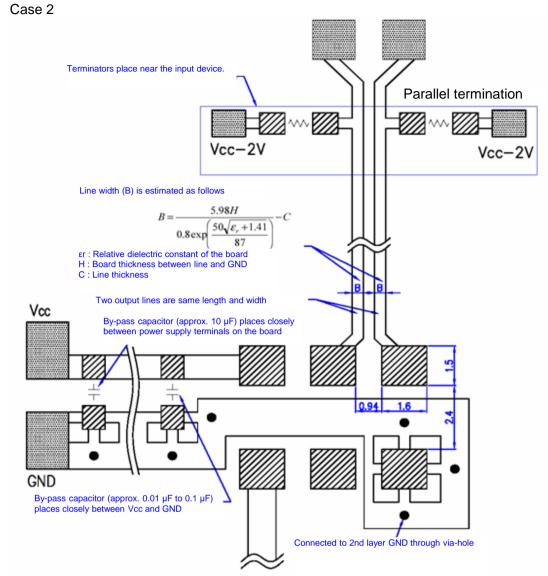
7.Reflow profile



8.PCB layout (2 layers, 2nd layer is all GND pattern) Case 1

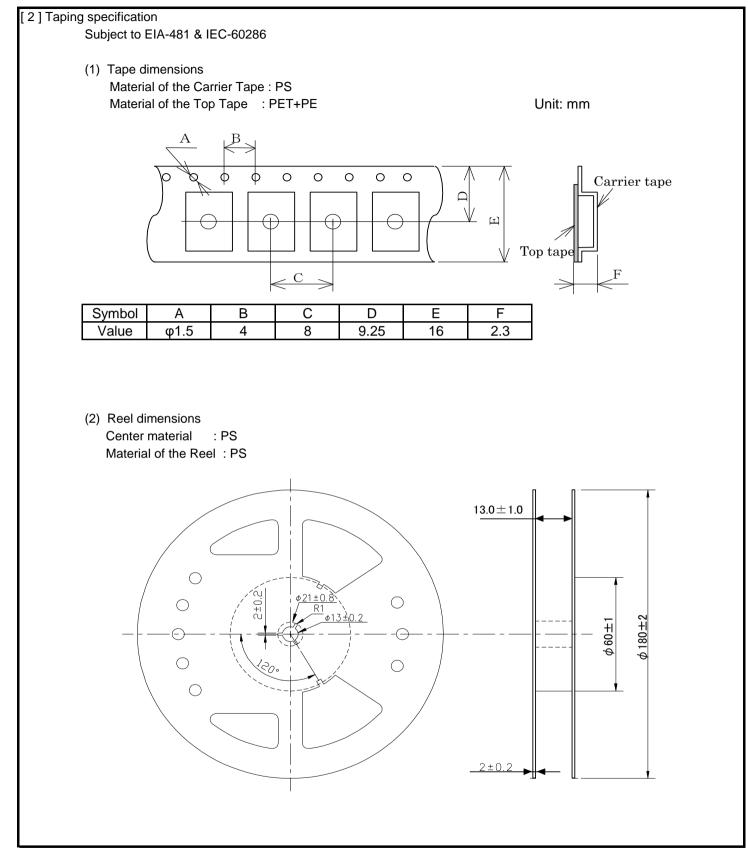


- * By-pass capacitor (approx. 0.01 μ F to 0.1 μ F) places closely between Vcc and GND.
- * By-pass capacitor (approx. 10 μF) places closely between power supply terminals on the board.
- * Please design the two output lines by characteristic impedance 50 Ω and same length, and try to make the output lines as short as possible.
- * Terminators place near the input device.



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9.Packing	g informa	tion			
[1]Produc	1]Product number last 2 digits code(xx) description			The recommended code is "00"	
	X1M0003	3010004xx			
	Code	Condition	Code	Condition	
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel	
	11	Any Q'ty / Reel	00	1000pcs / Reel	
	12	250pcs / Reel			
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