Clock OSC XG-2121CA

Product name XG-2121CA 98.304000 MHz +/-100ppm PHPN

Product Number / Ordering code X1M0003110018xx

Please refer to the 9.Packing information about xx (last 2 digits)

Output waveform LV-PECL

Pb free / Complies with EU RoHS directive

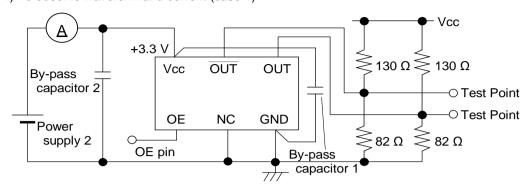
Reference weight Typ. 133 mg

0 71 0							
1.Absolute maximum ratings							
Parameter	ter Symbol Min. Typ. Max. Unit Conditions / Remarks		Conditions / Remarks				
Maximum supply voltage	Vcc-GND	-0.5	-	4	V	-	
Storage temperature	T_stg	-55	-	125	٥C	Storage as single product	
Input voltage	Vin	-0.5	-	Vcc+0.5	V	ST or OE Terminal	

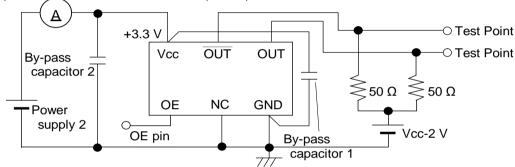
2.Specifications(characte	ristics)						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks	
Output frequency	f0	-	98.3040	-	MHz		
Supply voltage	Vcc	2.375	2.5	2.625	V	-	
Operating temperature	T_use	0	-	70	°C	-	
Frequency tolerance	f_tol	-100	-	100	x10 <sup>-6</sup>	-	
Current consumption	Icc	-	-	60	mA	OE=Vcc,L_ECL=50ohm	
Stand-by current	l_std	-	-	-	mA	-	
Disable current	I_dis	-	-	2.0	mA	OE=GND	
Symmetry	SYM	45	-	55	%	As output crodding point	
Output voltage(LV-PECL)	V <sub>OH</sub>	Vcc-1.025	1.55	Vcc-0.88	V	-	
	V <sub>OL</sub>	Vcc-1.81	0.8	Vcc-1.62	V	-	
Output load condition(ECL)	L_ECL	-	50	-	Ω	Terminated to Vcc-2.0V	
Input voltage	V <sub>IH</sub>	30% Vcc	-	-		OE Terminal	
	V <sub>IL</sub>	-	-	30% Vcc		OE Terminal	
Rise time	t <sub>r</sub>	-	-	400	ps	At 20% to 80% output swing	
Fall time	tf	-	-	400	ps	At 20% to 80% output swing	
Start-up time	t_str	-	-	10	ms	-	
Jitter	t <sub>DJ</sub>	-	-	N/A	ps	Deterministic Jitter	
	$T_{RJ}$	-	-	N/A	ps	Random Jitter	
	t <sub>RMS</sub>	-	-	N/A	ps	δ(RMS of total distribution)	
	t <sub>p-p</sub>	-	-	N/A	ps	Peak to Peak	
	t <sub>acc</sub>	-	-	N/A	ps	Accumulated Jitter(δ) n=2 to 50000 cycles	
Phase jitter	t <sub>PJ</sub>	-	-	0.23	ps	Off set Frequency: 12kHz to 20MHz	
Phase noise	L(f)	-	ı	-	dBc/Hz	Off set 1Hz	
		-	-59.0	-	dBc/Hz	Off set 10Hz	
		-	-90.2	-	dBc/Hz	Off set 100Hz	
		-	-117.9	-	dBc/Hz	Off set 1kHz	
		-	-144.9	-	dBc/Hz	Off set 10kHz	
		-	-152.5	-	dBc/Hz	Off set 100kHz	
		-	-154.6	-	dBc/Hz	Off set 1MHz	
Frequency aging	f_age	-10	-	10	x10 <sup>-6</sup> /Year	@+25°C first year	

#### 3.Test circuit

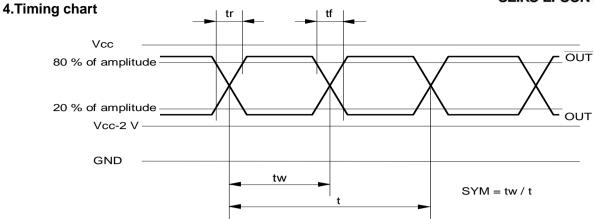
1) To observe waveform and current (case 1)

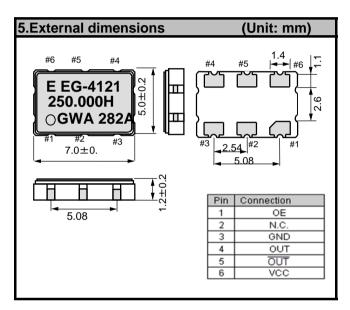


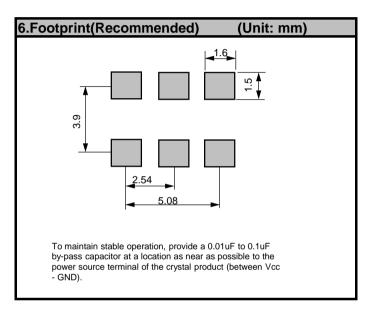
- \* The lines from OUT and OUT pin are same length.
- \* To measure the disable current, OE pin is connected to GND
- 2) To observe waveform and current (case 2)

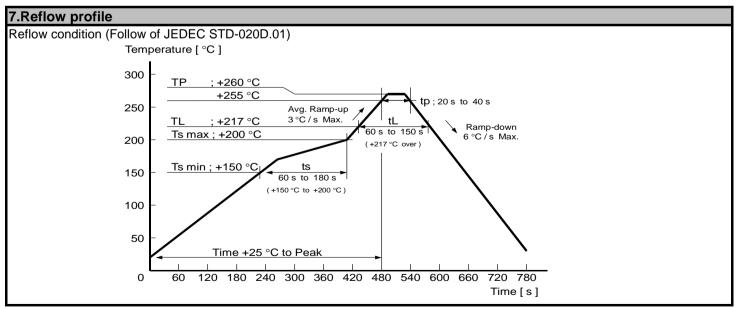


- \* The lines from OUT and OUT pin are same length.
- \* To measure the disable current, OE pin is connected to GND
- 3) Measurement condition
- A) Oscilloscope
- •Bandwidth should be 5 times higher than DUT's output frequency (4 GHz).
- •Probe ground should be placed closely from test point and lead length should be as short as possible.
- B) By-pass capacitor 1 (approx. 0.01 μF to 0.1 μF) places closely between Vcc and GND.
- C) By-pass capacitor 2 (approx. 10 µF) places closely between power supply terminals on the board.
- D) Use the current meter whose internal impedance value is small.
- E) Power supply
- Start up time (0 Vg90 %Vcc) of power source should be more than 150  $\mu s$  and slew rate should be less than 19.8 mV/ $\mu s$ .
- Impedance of power supply should be as low as possible.

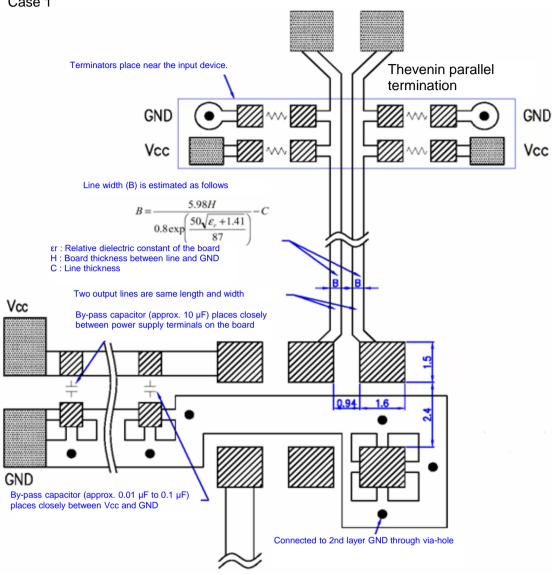




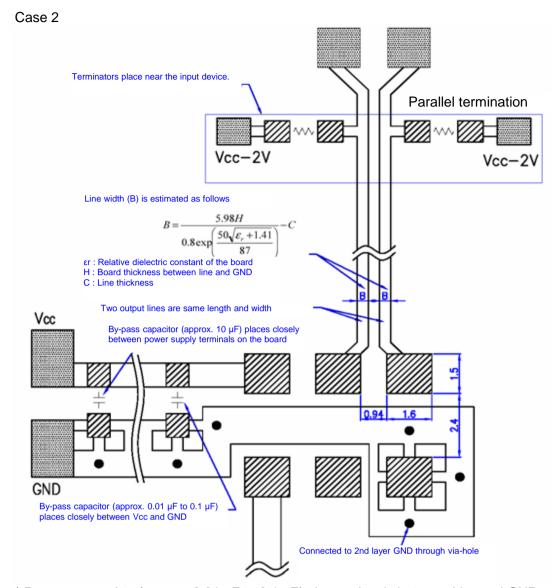




# 8.PCB layout (2 layers, 2nd layer is all GND pattern) Case 1



- \* By-pass capacitor (approx. 0.01 μF to 0.1 μF) places closely between Vcc and GND.
- \* By-pass capacitor (approx. 10 μF) places closely between power supply terminals on the board.
- $^{\star}$  Please design the two output lines by characteristic impedance 50  $\Omega$  and same length, and try to make the output lines as short as possible.
- \* Terminators place near the input device.



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- \* By-pass capacitor (approx. 10 μF) places closely between power supply terminals on the board.
- \* Please design the two output lines by characteristic impedance 50  $\Omega$  and same length, and try to make the output lines as short as possible.
- \* Terminators place near the input device.

9.Packing	g informa	tion		
[ 1 ]Produc	t number l	ast 2 digits code(xx) description		The recommended code is "00"
	X1M0003	3110018xx		
	Code	Condition	Code	Condition
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
	11	Any Q'ty / Reel	00	1000pcs / Reel
	12	250pcs / Reel		

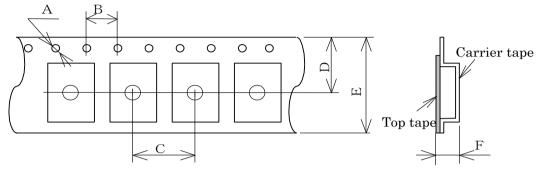
# [ 2 ] Taping specification

Subject to EIA-481 & IEC-60286

## (1) Tape dimensions

Material of the Carrier Tape: PS
Material of the Top Tape: PET+PE

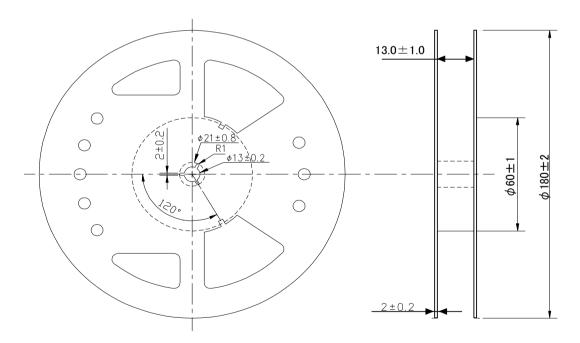
Unit: mm



Symbol	Α	В	С	D	Е	F
Value	φ1.5	4	8	9.25	16	2.3

## (2) Reel dimensions

Center material : PS Material of the Reel : PS



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