Clock OSC

XG-2121CA

XG-2121CA 200.000000MHz +/-50ppm LGRN Product name Product Number / Ordering code

X1M0003510012xx

Please refer to the 9.Packing information about xx (last 2 digits)

Output waveform LVDS

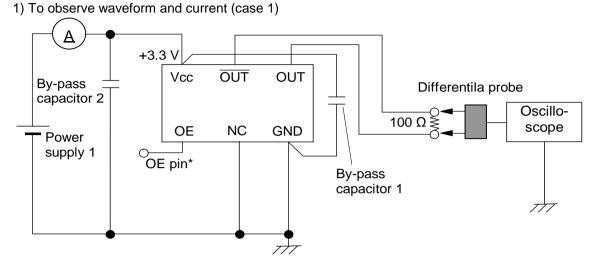
Pb free / Complies with EU RoHS directive

Reference weight Typ. 133 mg

1.Absolute maximum ratings	i					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.5	-	4	V	-
Storage temperature	T_stg	-55	-	125	°C	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	OE Terminal

2.Specifications(characteri	stics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO	-	200.0000	-	MHz	-
Supply voltage	Vcc	-0.5	-	4	V	-
Operating temperature	T_use	-5	-	85	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 ⁻⁶	-
Current consumption	lcc	-	-	30	mA	-
Stand-by current	I_std	-	-	-	mA	-
Disable current	I_dis	-	-	15.0	mA	-
Symmetry	SYM	45	-	55	%	-
Output voltage(LVDS)	Vod	247	-	454	mV	-
	dVod	-	-	50	mV	-
	Vos	1.125	-	1.375	V	-
	dVos	-	-	150	mV	-
Output load condition(LVDS)	L_LVDS	-	100	-	Ω	-
Input voltage	V _{IH}	0.7Vcc	-	-		-
	V _{IL}	-	-	0.3Vcc		-
Rise time	t _r	-	-	400	ps	-
Fall time	tf	-	-	400	ps	-
Start-up time	t_str	-	-	10	ms	-
Jitter	t _{DJ}	-	-	N/A	ps	Deterministic Jitter
	T _{RJ}	-	-	N/A	ps	Random Jitter
	t _{RMS}	-	-	N/A	ps	$\delta(RMS \text{ of total distribution})$
	t _{p-p}	-	-	N/A	ps	Peak to Peak
	t _{acc}	-	-	N/A	ps	Accumulated Jitter(δ) n=2 to 50000 cycles
Phase jitter	t _{PJ}	-	-	0.23	ps	Off set Frequency: 12kHz to 20MHz
Phase noise	L(f)	-	-	-	dBc/Hz	Off set 1Hz
		-	-52.4	-	dBc/Hz	Off set 10Hz
		-	-81.3	-	dBc/Hz	Off set 100Hz
		-	-111.8	-	dBc/Hz	Off set 1kHz
		-	-139.4	-	dBc/Hz	Off set 10kHz
		-	-147.5	-	dBc/Hz	Off set 100kHz
		-	-149.6	-	dBc/Hz	Off set 1MHz
Frequency aging	f_age	-10	-	10	x10 ⁻⁶ /Year	@+25⁰C first year
		-	-	-		-

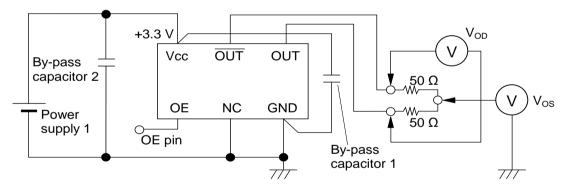
3.Test circuit



* The lines from OUT and OUT pin are same length.

* To measure the disable current, OE pin is connected to GND

2) To observe waveform and current (case 2)



* The lines from OUT and OUT pin are same length.

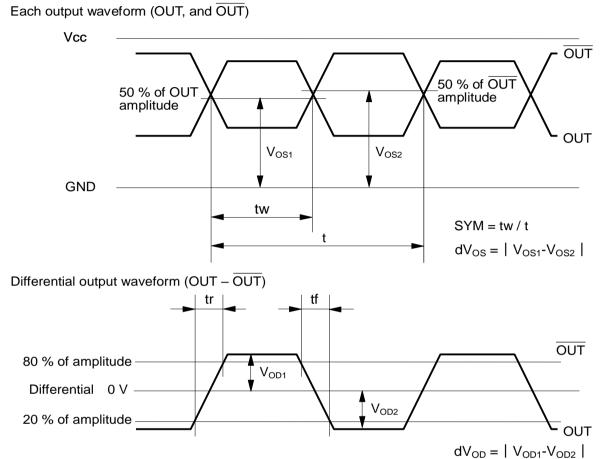
- 3) Measurement condition
- A) Oscilloscope

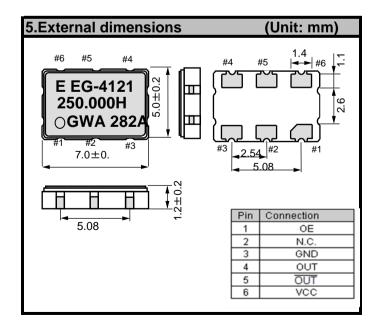
•Bandwidth should be 5 times higher than DUT's output frequency (4 GHz).

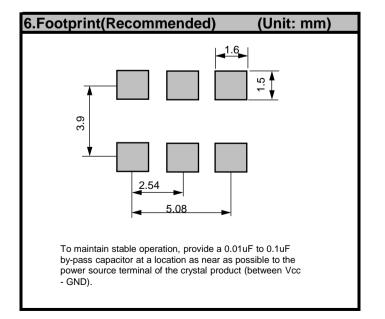
•Probe ground should be placed closely from test point and lead length should be as short as possible.

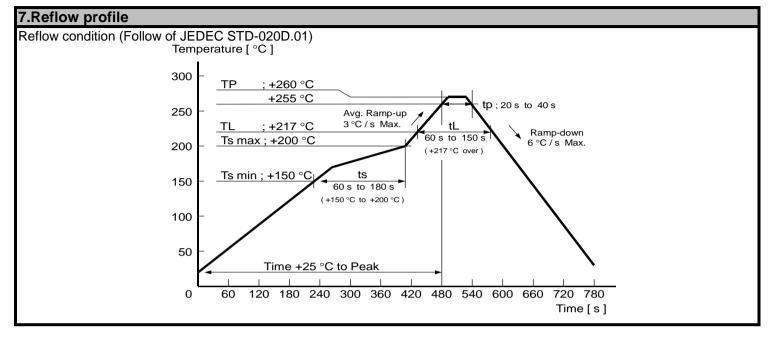
- B) By-pass capacitor 1 (approx. 0.01 µF to 0.1 µF) places closely between Vcc and GND.
- C) By-pass capacitor 2 (approx. 10 µF) places closely between power supply terminals on the board.
- D) Use the current meter whose internal impedance value is small.
- E) Power supply
- Start up time (0 Vg90 %Vcc) of power source should be more than 150 μs and slew rate should be less than 19.8 mV/ $\mu s.$
- Impedance of power supply should be as low as possible.

4.Timing chart

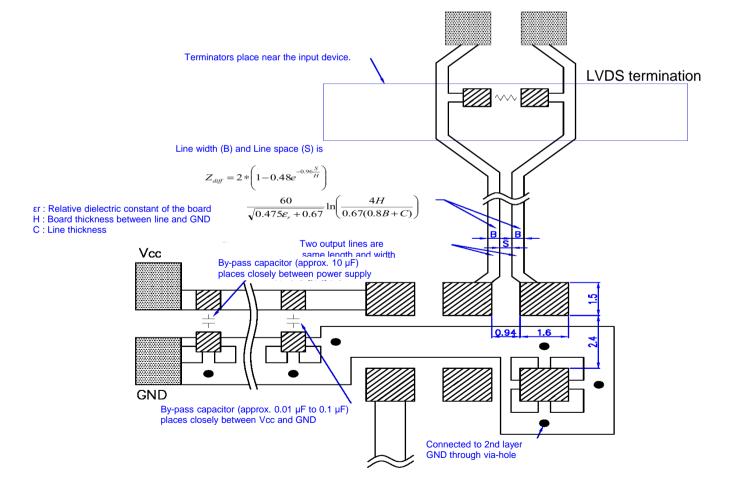






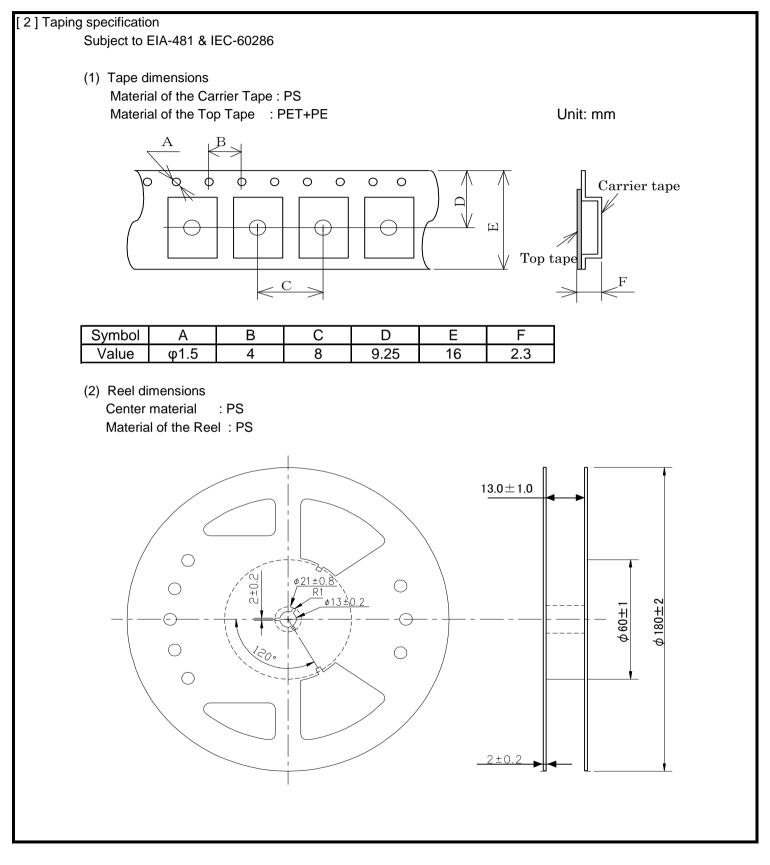


8.PCB layout (2 layers, 2nd layer is all GND pattern)



- * By-pass capacitor (approx. 0.01 μ F to 0.1 μ F) places closely between Vcc and GND.
- * By-pass capacitor (approx. 10 µF) places closely between power supply terminals on the board.
- * Please design the two output lines by characteristic impedance 100 Ω and same length, and try to make the output lines as short as possible.

9.Packing	g informa	tion		
[1]Produc	t number la	ast 2 digits code(xx) description		The recommended code is "00"
	X1M0003	3510012xx		
	Code	Condition	Code	Condition
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
	11	Any Q'ty / Reel	00	1000pcs / Reel
	12	250pcs / Reel		



10.Notice

- This material is subject to change without notice.
- Any part of this material may not be reproduced or duplicated in any form or any means without the written permission of Seiko Epson.
- The information about applied circuitry, software, usage, etc. written in this material is intended for reference only. Seiko Epson does not assume any liability for the occurrence of infringing on any patent or copyright of a third party. This material does not authorize the licensing for any patent or intellectual copyrights.
- When exporting the products or technology described in this material, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
- You are requested not to use the products (and any technical information furnished, if any) for the development and/or manufacture of weapon of mass destruction or for other military purposes. You are also requested that you would not make the products available to any third party who may use the products for such prohibited purposes.
- These products are intended for general use in electronic equipment. When using them in specific applications that require extremely high reliability, such as the applications stated below, you must obtain permission from Seiko Epson in advance. / Space equipment (artificial satellites, rockets, etc.)
 - / Transportation vehicles and related (automobiles, aircraft, trains, vessels, etc.)
 - / Medical instruments to sustain life
 - / Submarine transmitters
 - / Power stations and related
 - / Fire work equipment and security equipment
 - / Traffic control equipment
 - / And others requiring equivalent reliability.
- All brands or product names mentioned herein are trademarks and/or registered trademarks of their respective.

11.Contact us

http://www5.epsondevice.com/en/contact/