# Clock OSC

## XG-2121CA

#### XG-2121CA 133.000000MHz +/-50ppm LGPA Product name Product Number / Ordering code

X1M0003510014xx

Please refer to the 9.Packing information about xx (last 2 digits)

### Output waveform LVDS

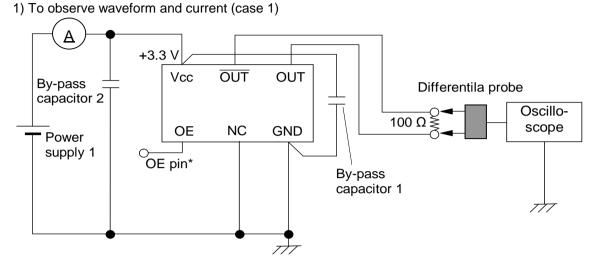
Pb free / Complies with EU RoHS directive

Reference weight Typ. 133 mg

1.Absolute maximum ratings	5					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.5	-	4	V	-
Storage temperature	T_stg	-55	-	125	°C	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	OE Terminal

2.Specifications(character	istics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO	-	133.0000	-	MHz	-
Supply voltage	Vcc	-0.5	-	4	V	-
Operating temperature	T_use	0	-	70	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	-
Current consumption	lcc	-	-	30	mA	-
Stand-by current	I_std	-	-	-	mA	-
Disable current	I_dis	-	-	15.0	mA	-
Symmetry	SYM	45	-	55	%	-
Output voltage(LVDS)	Vod	247	-	454	mV	-
	dVod	-	-	50	mV	-
	Vos	1.125	-	1.375	V	-
	dVos	-	-	150	mV	-
Output load condition(LVDS)	L_LVDS	-	100	-	Ω	-
Input voltage	V <sub>IH</sub>	0.7Vcc	-	-		-
	V <sub>IL</sub>	-	-	0.3Vcc		-
Rise time	t <sub>r</sub>	-	-	400	ps	-
Fall time	tf	-	-	400	ps	-
Start-up time	t_str	-	-	10	ms	-
Jitter	t <sub>DJ</sub>	-	-	N/A	ps	Deterministic Jitter
	T <sub>RJ</sub>	-	-	N/A	ps	Random Jitter
	t <sub>RMS</sub>	-	-	N/A	ps	δ(RMS of total distribution)
	t <sub>p-p</sub>	-	-	N/A	ps	Peak to Peak
	t <sub>acc</sub>	-	-	N/A	ps	Accumulated Jitter( $\delta$ ) n=2 to 50000 cycles
Phase jitter	t <sub>PJ</sub>	-	-	0.27	ps	Off set Frequency: 12kHz to 20MHz
Phase noise	L(f)	-	-	-	dBc/Hz	Off set 1Hz
		-	-49.1	-	dBc/Hz	Off set 10Hz
		-	-77.3	-	dBc/Hz	Off set 100Hz
		-	-106.7	-	dBc/Hz	Off set 1kHz
		-	-136.6	-	dBc/Hz	Off set 10kHz
		-	-147.3	-	dBc/Hz	Off set 100kHz
		-	-150.9	-	dBc/Hz	Off set 1MHz
Frequency aging	f_age	-	-	-	x10 <sup>-6</sup> /Year	Included in Frequency tolerance 10 years
		-	-	-		-

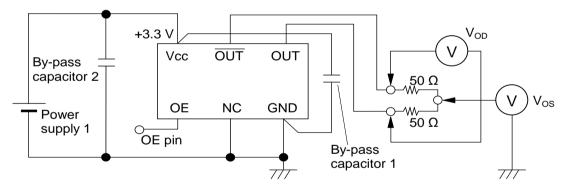
#### 3.Test circuit



\* The lines from OUT and OUT pin are same length.

\* To measure the disable current, OE pin is connected to GND

2) To observe waveform and current (case 2)



\* The lines from OUT and OUT pin are same length.

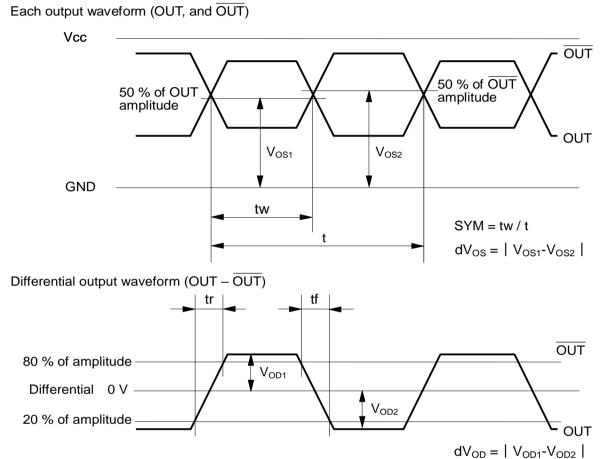
- 3) Measurement condition
- A) Oscilloscope

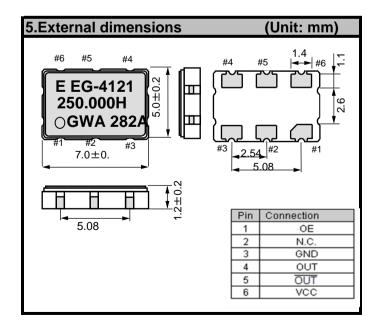
•Bandwidth should be 5 times higher than DUT's output frequency (4 GHz).

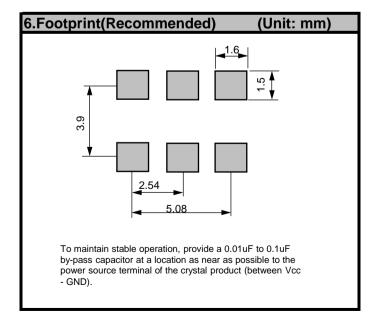
•Probe ground should be placed closely from test point and lead length should be as short as possible.

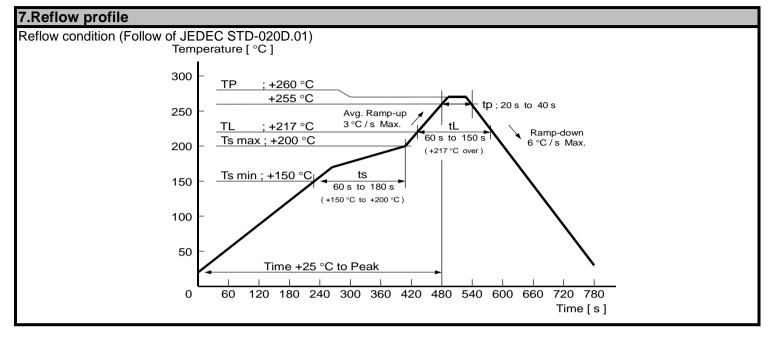
- B) By-pass capacitor 1 (approx. 0.01 µF to 0.1 µF) places closely between Vcc and GND.
- C) By-pass capacitor 2 (approx. 10 µF) places closely between power supply terminals on the board.
- D) Use the current meter whose internal impedance value is small.
- E) Power supply
- Start up time (0 Vg90 %Vcc) of power source should be more than 150  $\mu s$  and slew rate should be less than 19.8 mV/ $\mu s.$
- Impedance of power supply should be as low as possible.

#### 4.Timing chart

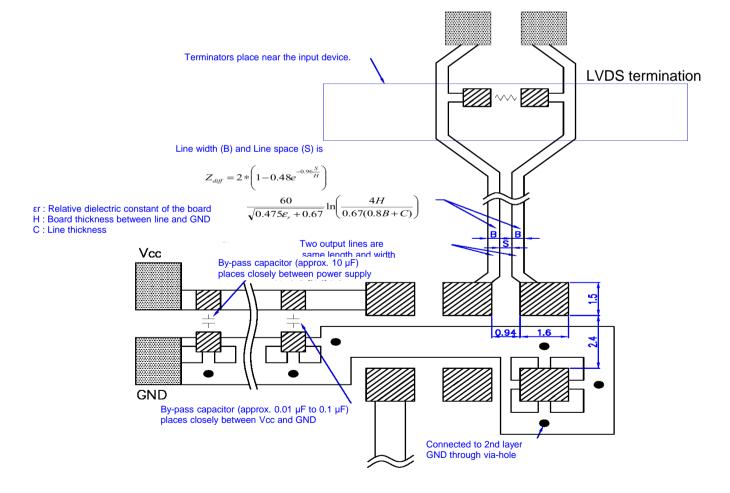






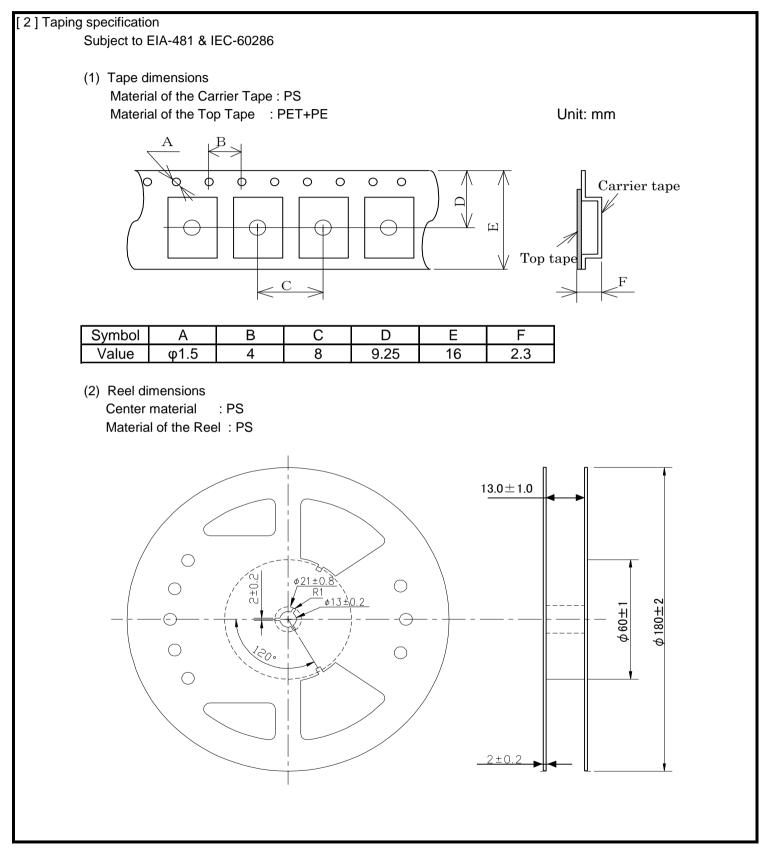


8.PCB layout (2 layers, 2nd layer is all GND pattern)



- \* By-pass capacitor (approx. 0.01  $\mu$ F to 0.1  $\mu$ F) places closely between Vcc and GND.
- \* By-pass capacitor (approx. 10 µF) places closely between power supply terminals on the board.
- \* Please design the two output lines by characteristic impedance 100  $\Omega$  and same length, and try to make the output lines as short as possible.

9.Packin	g informa	tion		
[1]Produc	ct number la	ast 2 digits code(xx) description		The recommended code is "00"
	X1M0003	3510014xx		
	Code	Condition	Code	Condition
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
	11	Any Q'ty / Reel	00	1000pcs / Reel
	12	250pcs / Reel		



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